ASHISH DAHARE

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♀ Bilaspur, Chhattisgarh, INDIA

in Ashish Dahare

EDUCATION

B.Tech in ECE and M.Tech in Electronics Engineering (Specialization-Microelectronics)

IIIT ALLAHABAD

Aug 2017-present

Prayagraj, Uttar Pradesh, INDIA

• CGPA: 8.05/10

12th PCM from CBSE Board

JNV Malhar

2015-16

Bilaspur, Chhattisgarh, INDIA

Percentage: 84.4

10th from CBSE Board

JNV Malhar

2013-14

Bilaspur, Chhattisgarh, INDIA

• CGPA: 9/10

PROJECTS

GUI Application For STM32F7 Development Board Personal Project

May - June 2020, May 2021 - present

STM32F7 development board uses Cortex-M7 microcontroller by ARM. And used in embedded system applications. GUI applications are developed from scratch for STM32F7 development board.

Applications - Digital Clock, Calculator, Note Pad, etc.

• C, C++, STM32CudeIDE, TouchGFX

Design of 32-bit 5-stage pipeline RISC CPU using Verilog Academic Project

RISC CPU with 5-stage(IF, ID, E, MA, WB) pipeline is designed using Verilog. It can perform logical, arithmetic, load, store and jump operations.

Verilog, Vivado

Image Based Seed for Cryptography Applications Academic Project

₩ July - Dec 2019

Images of the source which is random in nature were processed with Python to generate the seed. Seed was further processed with a circuit designed using LFSR, multiplexers and gates to generate random bit-sequence. It is used for true random number generation.

In Virtuoso circuit was designed, simulated and layout was made using umc180 library.

• Python, Cadence Virtuoso

Nutty Squirrel

e-Yantra a Robotics Competition by IIT Bombay, (Group Project)

Mar 2019 - Mar 2019

A line follower bot was programmed to mimic a squirrel. V-REP was used for simulation. Sensors are used to move bot from one place to another on the black line and avoiding obstacles. Colour sensor was used to find the colour of nuts so that they can be placed to their desired locations.

 C, AVR-C, V-REP, Atmel Studio, Visual Studio

TECHNICAL SKILLS

Programming Languages -

C C++ Python (Basic)

Hardware Description Language -

Verilog

• Other Language -

shell Script (Basic)

Tools used -

Matlab | Android Studio

Visual Studio | Atmel Studio

TouchGFX | STM32CubeIDE

Operating Systems Windows Linux

LINKS

- 🖸 Github
- A Hackerearth

ACHIEVEMENTS

 Awarded Certificate of Participation for e-Yantra by IIT Bombay.

EXTRA-CURRICULAR

- Interested in Hardware Hacking.
- Interested in playing Table Tennis.