

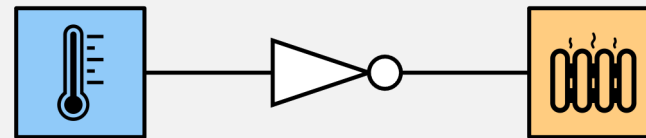
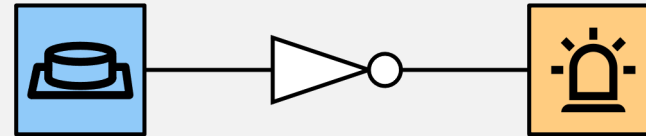
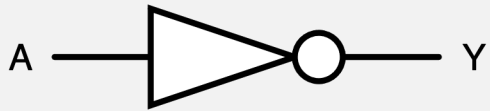


# Digitale Logik 2 – Gatter



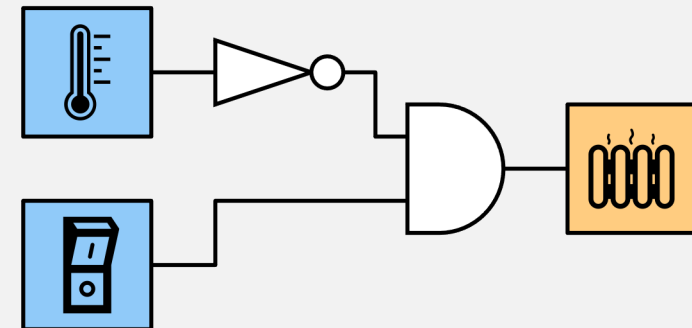
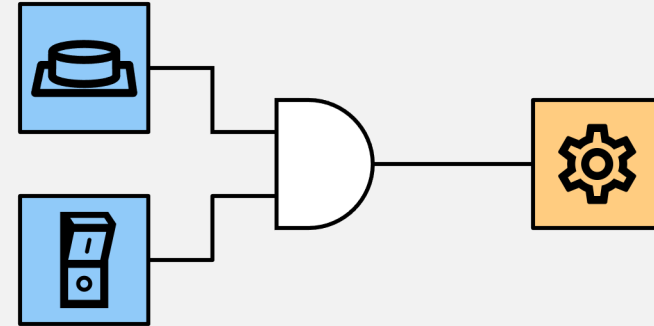
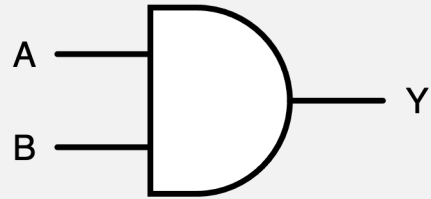


# Inverter





# AND-Gatter





# OR-Gatter

