1. Description

1.1. Project

Project Name	stm32f303
Generated with:	STM32CubeMX 4.2.0
Date	06/05/2014

1.2. MCU

MCU Serie	STM32F3
MCU Line	STM32F303/313
MCU name	STM32F303CBTx
MCU Package	LQFP48
MCU Pin number	48

2. Pinout Configuration
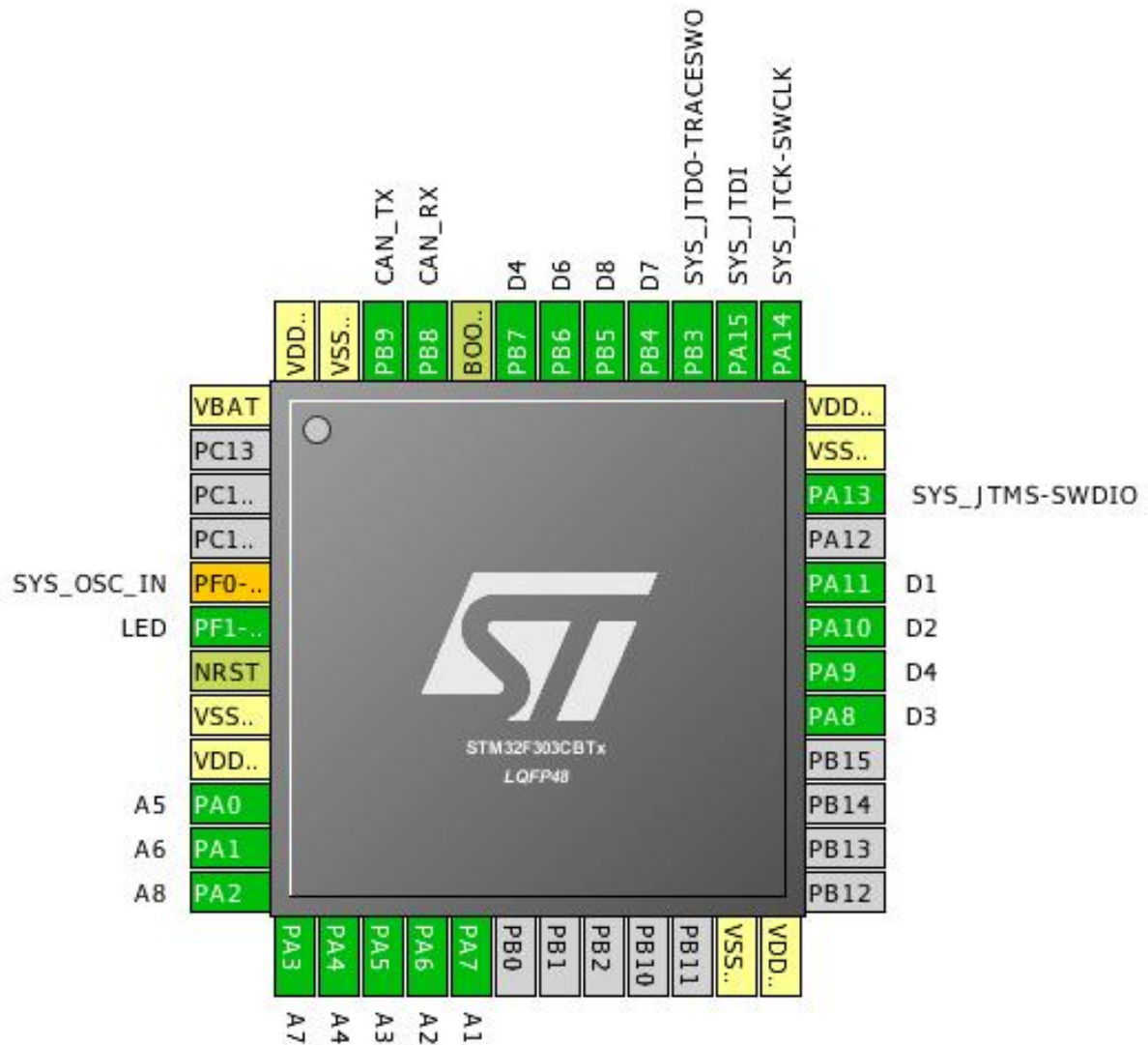
3. IPs and Middlewares Configuration

IP	Mode	Fonction	Pin
ADC1	CH1-Single-Ended	ADC1_IN1	PA0
	CH2-Single-Ended	ADC1_IN2	PA1
	CH3-Single-Ended	ADC1_IN3	PA2
	CH4-Single-Ended	ADC1_IN4	PA3
ADC2	CH1-Single-Ended	ADC2_IN1	PA4
	CH2-Single-Ended	ADC2_IN2	PA5
	CH3-Single-Ended	ADC2_IN3	PA6
	CH4-Single-Ended	ADC2_IN4	PA7
CAN	Mode	CAN_RX	PB8
		CAN_TX	PB9
SYS	Debug: JTAG (4 pins)	SYS_JTCK-SWCLK	PA14
		SYS_JTDI	PA15
		SYS_JTDO-TRACESWO	PB3
		SYS_JTMS-SWDIO	PA13

4. Pins Configuration

Pin	Pos	Function(s)	Label
PF0-OSC_IN *	5	SYS_OSC_IN	
PF1-OSC_OUT **	6	GPIO_Output	LED
PA0	10	ADC1_IN1	A5
PA1	11	ADC1_IN2	A6
PA2	12	ADC1_IN3	A8
PA3	13	ADC1_IN4	A7
PA4	14	ADC2_IN1	A4
PA5	15	ADC2_IN2	A3
PA6	16	ADC2_IN3	A2
PA7	17	ADC2_IN4	A1
PA8 **	29	GPIO_Input	D3
PA9 **	30	GPIO_Input	D4
PA10 **	31	GPIO_Input	D2
PA11 **	32	GPIO_Input	D1
PA13	34	SYS_JTMS-SWDIO	
PA14	37	SYS_JTCK-SWCLK	
PA15	38	SYS_JTDI	
PB3	39	SYS_JTDO-TRACESWO	
PB4 **	40	GPIO_Input	D7
PB5 **	41	GPIO_Input	D8
PB6 **	42	GPIO_Input	D6
PB7 **	43	GPIO_Input	D4
PB8	45	CAN_RX	
PB9	46	CAN_TX	

** The pin is affected with an I/O function

 * The pin is affected with a peripheral function but no peripheral mode is activated