Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058, India

(Autonomous College Affiliated to University of Mumbai)

Mid Semester Evaluation
Mar 2018

Max. Marks: 30	Duration: 90 Min
Class: S.E.	Semester: IV
Course Code: CE44 / IT42	Branch: Computer / IT
Name of the Course: Computer Organization and Architecture	

Instruction:
   (1) All questions are compulsory

Q No.		Max. Marks	CO
Q.1	Embedded System is a combination of computer hardware and software. Give in detail possible organization of Embedded System	5	CO1
Q.2	With neat diagram, describe the Harvard Model. OR Draw and explain in brief Von Numann Architecture	5	CO1
Q.3	Divide 145 by 13 in binary 2's complement notation, using 12 bits. OR Explain Booth's Multiplication approach with example	5	CO2
Q.4	The following numbers use the IEEE 32-bit floating point format. What is the equivalent decimal value. A. 1 10000011 11000000000000000000000 B. 0 01111110 10100000000000000000000	5	CO2
Q.5	Explain the basic Instruction Cycle.	5	CO3
Q.6	Discuss Register Organization in detail.	5	CO3