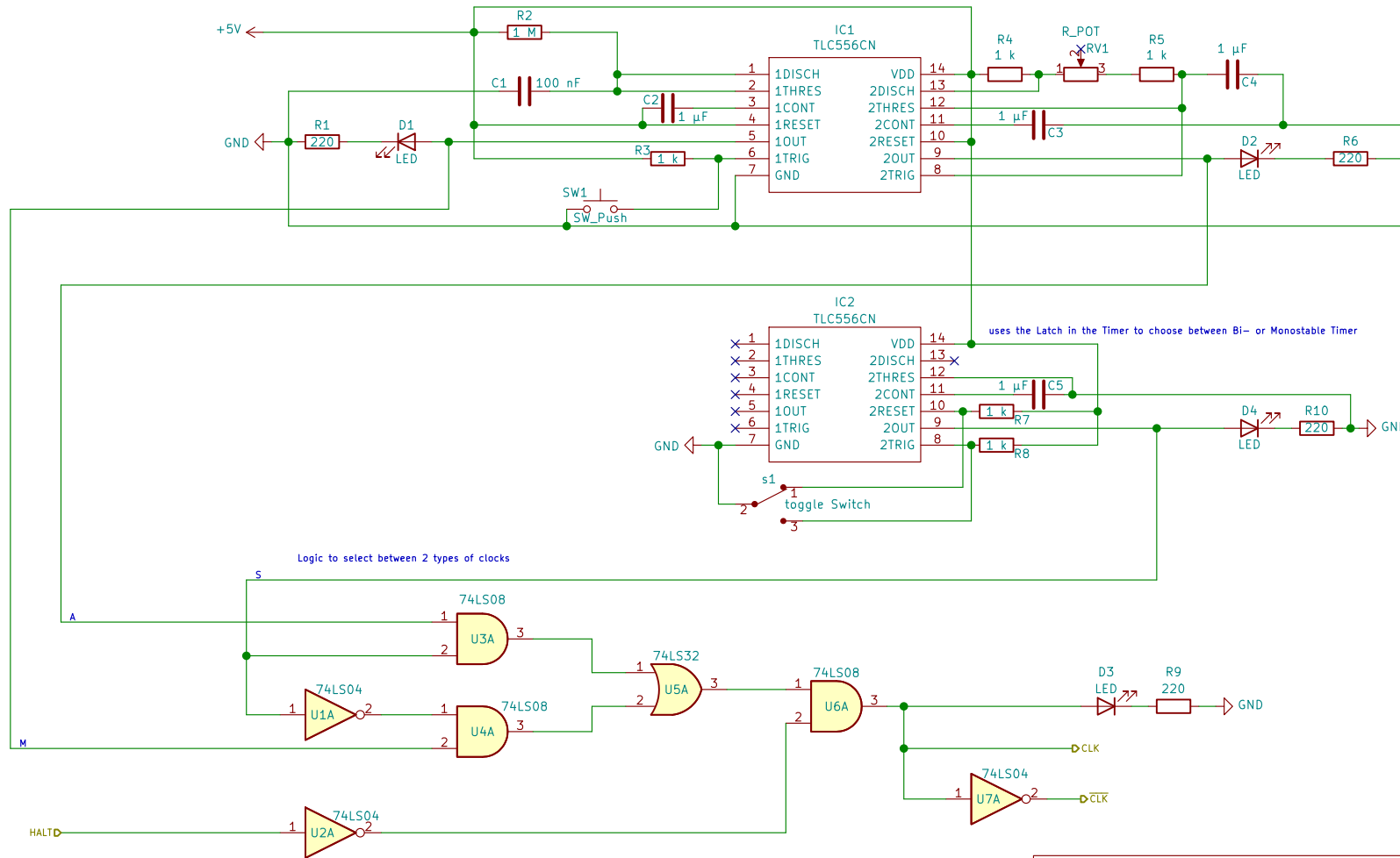


Bistable Timer

combined from A- and Monostable Timer

Monostable Timer

Astable Timer



following the model by Ben Eater

Design by Hendrik Wirthwein

Sheet: /ClockModule/

File: ClockModule.sch

Title: 8 Bit CPU

Size: A4 Date: 2021-06-07

KiCad E.D.A. kicad (5.1.10)-1

Rev: Version 1.0

Id: 2/5