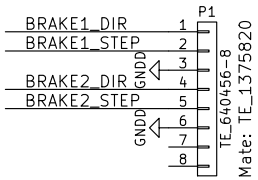
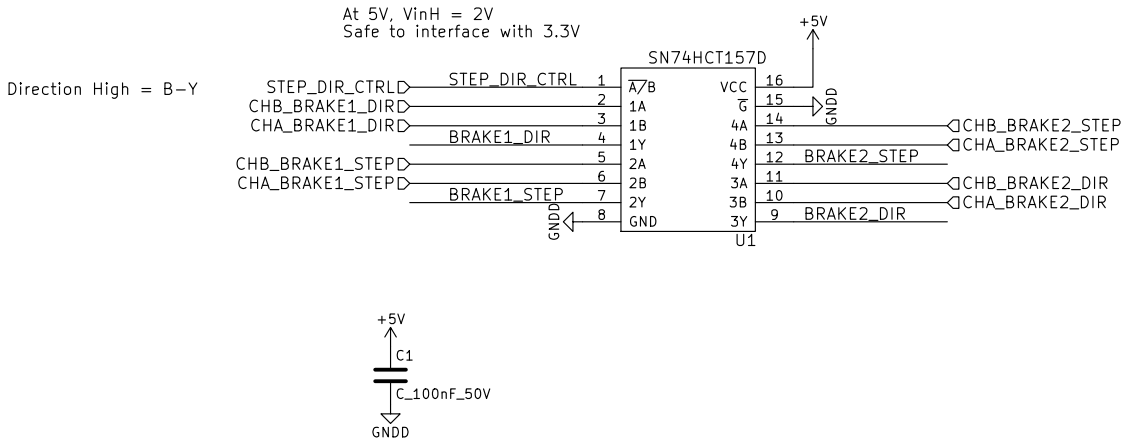
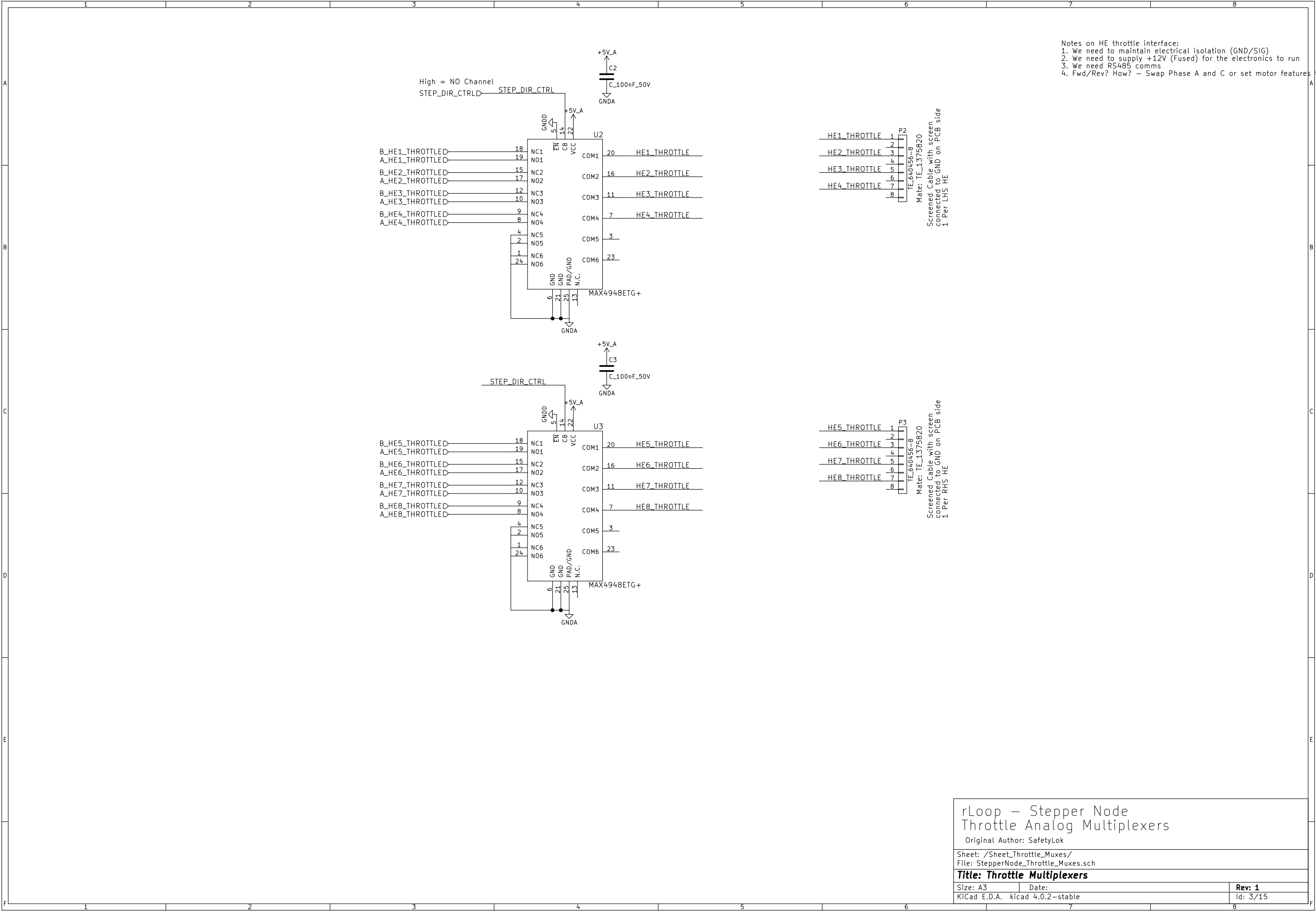


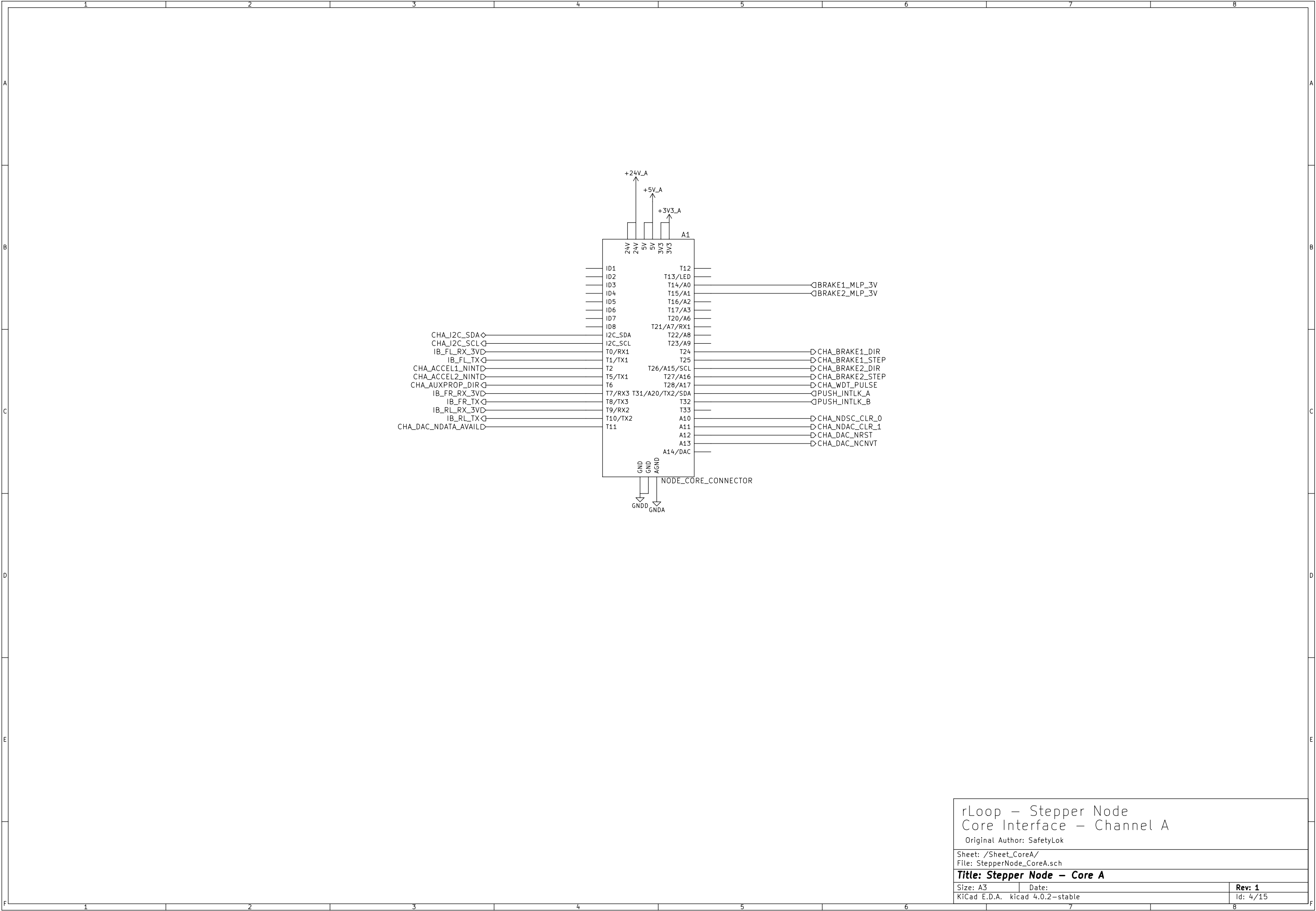
The purpose of this block is to route 6 Step and 6 Direction signals from each of the two teensys and also buffer those signals.

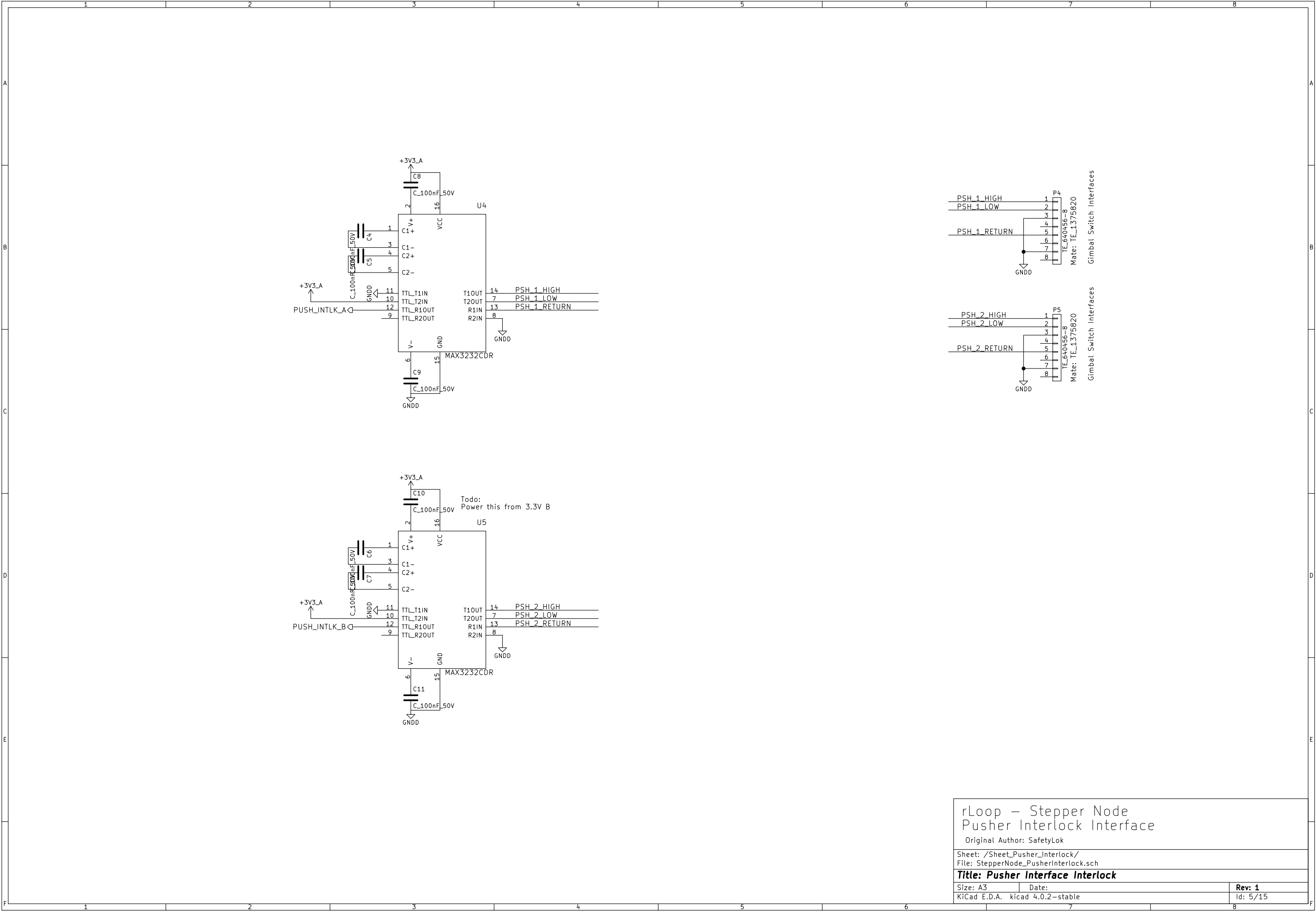


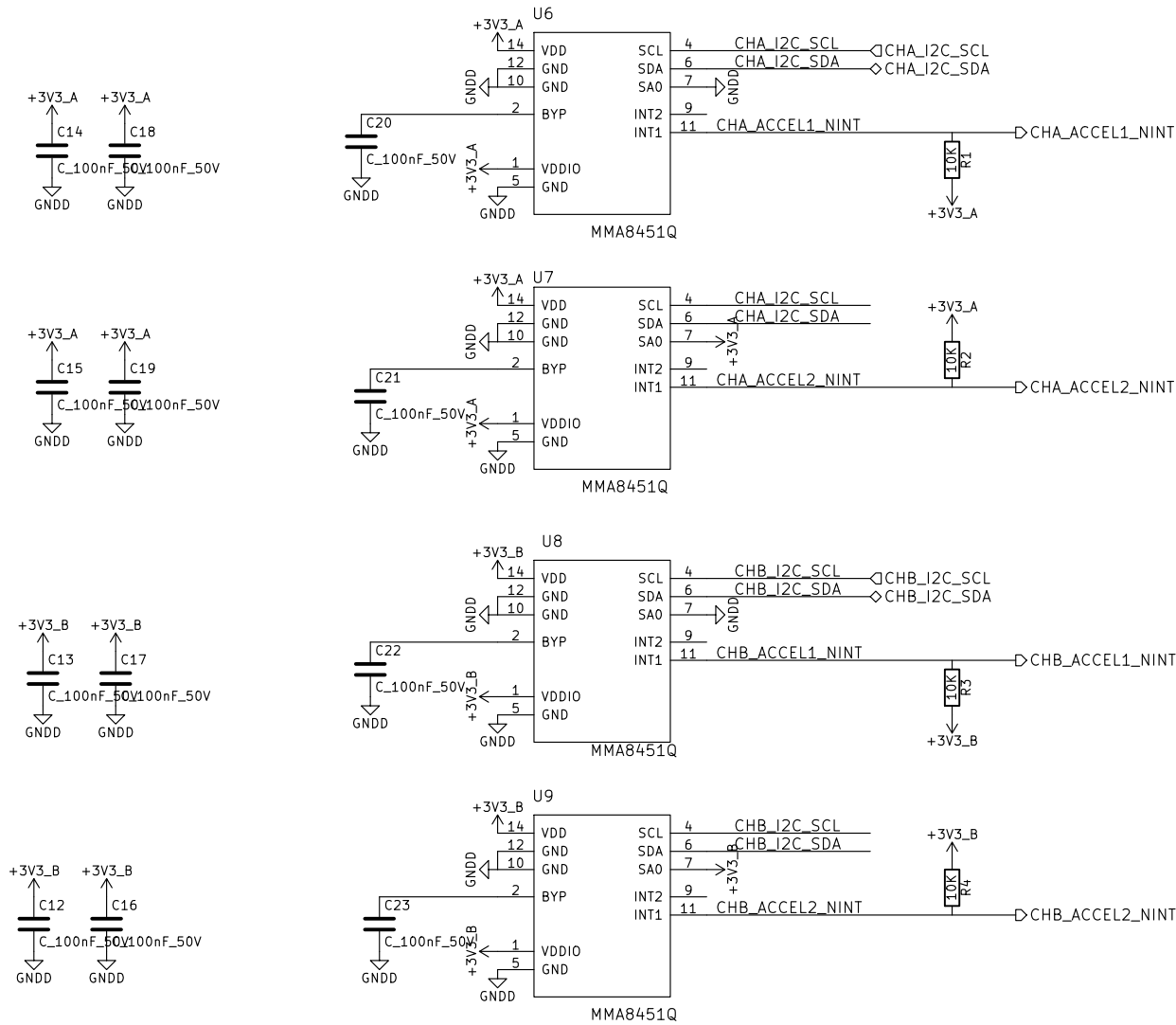
Twisted pair wiring
Grounded at this end only

Left and Right Brake Controllers

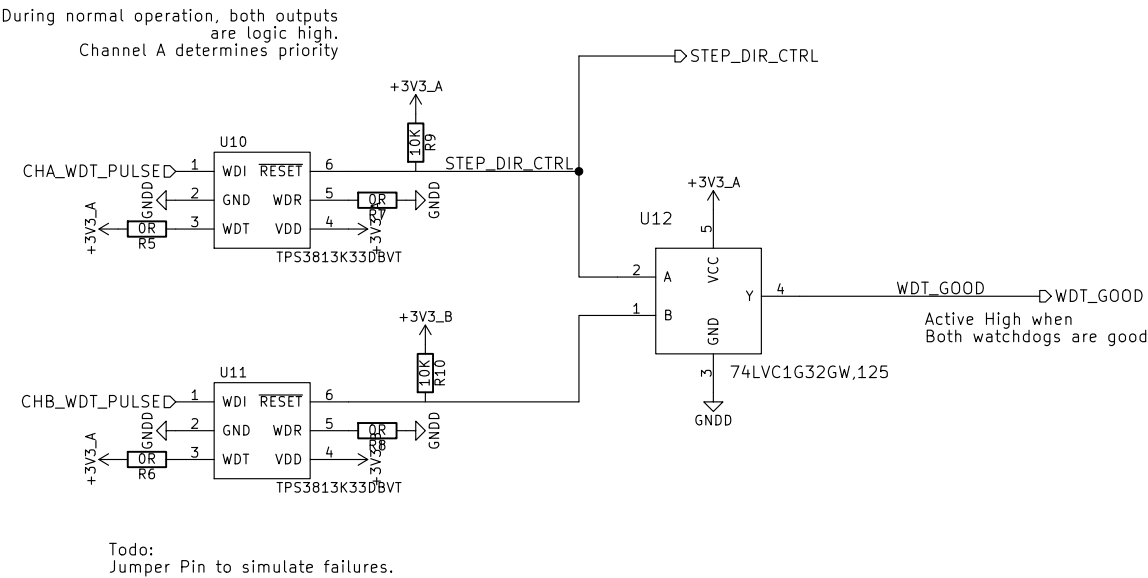






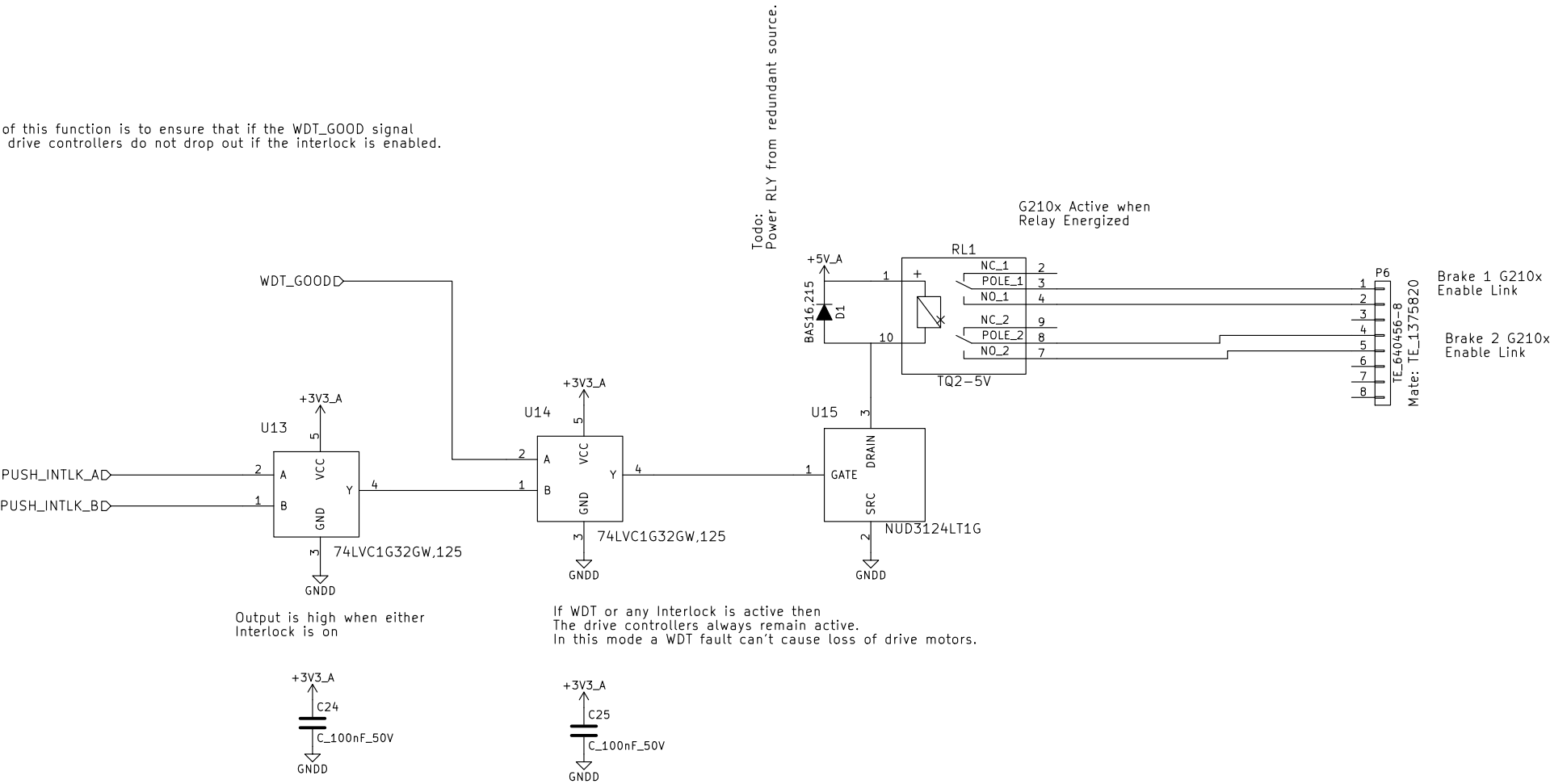


All accels must be mounted to as the +VE X-AXIS is in the direction of pod travel.



- DESIGN REVIEW NOTES:
1. STEP_DIR_CTRL should go low if channel A fails, giving channel B priority
 2. STEP_DIR_CTRL is pulled up, so the only way it can go low is if the TPS3813 is powered
 3. All devices here need redundant supplies
 4. Need to provide isolation in case of parasitic power from a failed node.

The purpose of this function is to ensure that if the WDT_GOOD signal goes low the drive controllers do not drop out if the interlock is enabled.



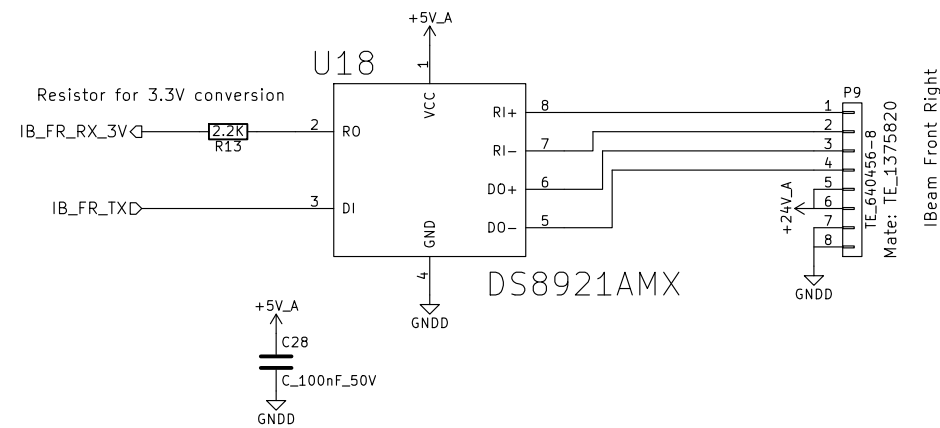
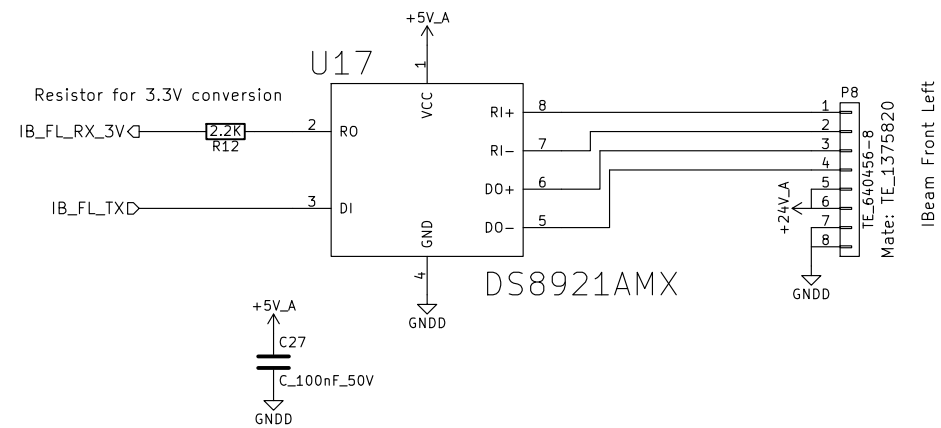
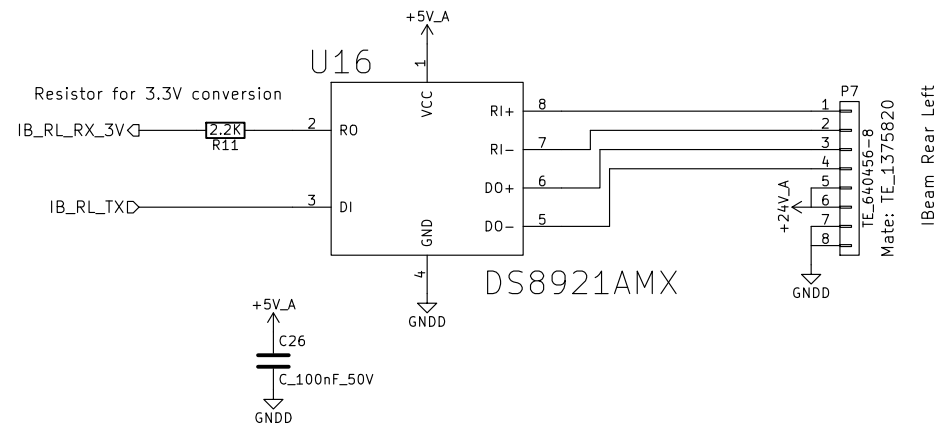
rLoop – Stepper Node
Failsafe Brake Interface

Original Author: SafetyLok

Sheet: /Sheet_Brake_Failsafe/
File: StepperNode_Brake_Failsafe.sch

Title: Brake Failsafe Control

Size: A3	Date:	Rev: 1
KiCad E.D.A. kicad 4.0.2–stable		Id: 8/15



TODO: CHECK ELECTRICAL ISOLATION OF RAGEBRIDGE CONTROLLER

