

2. 1, 2, 3, 4, 5, 6, 7, 9, 10, 12, 18, 21, 22, 26, 29, 38, 41 SYDNEY CADY

2.1

$$f = g + (h - 5)$$

subi f, h, 5
add f, f, g

2.2

add f, g, h
add f, i, f } $f = g + h + i$

2.3

$$B[8] = A[i - j]$$

f, g, h, i, j → \$s0, \$s1, \$s2, \$s3, \$s4
A, B → \$s6, \$s7

sub \$t0, \$s3, \$s4
add \$t0, \$s6, \$t0
lw \$t1, 16(\$t0)
sw \$t1, 32(\$s7)

2.4

$$B[g] = A[f] + A[f + f]$$

2.5

memimize

add \$t0, \$s6, \$s0
add \$t1, \$s7, \$s1
lw \$s0, 0(\$t0)
lw \$t0, 4(\$t0)
add \$t0, \$t0, \$s0
sw \$t0, 0(\$t1)

2.6

Addr.	Data	
24	2	0
36	3	1
32	3	2
36	6	3
40	1	4

2.6.1

$tmp = Array[0]$
 $tmp2 = Array[1]$
 $array[0] = array[4]$
 $array[1] = tmp$
 $array[4] = array[3]$
 $array[3] = tmp2$



2.6.2

$lw \$t0, 0(\$s6)$
 $lw \$t1, 4(\$s6)$
 $lw \$t2, 16(\$s6)$
 $sw \$t2, 0(\$s6)$
 $sw \$t0, 4(\$s6)$
 $lw \$t0, 12(\$s6)$
 $sw \$t0, 16(\$s6)$
 $sw \$t1, 12(\$s6)$

2.7

Little-Endian	
Address	Data
12	ab
8	cd
4	ef
0	12

Big-Endian	
Address	Data
12	12
8	ef
4	cd
0	ab

2.9

$B[8] = A[1] + A[3]$



$sw \$t0, \$s1, 2$
 $add \$t0, \$t0, \$s3$
 $lw \$t0, 0(\$t0)$
 $addi \$t0, \$t0, 1$
 $sll \$t0, \$t0, 2$
 $lw \$t0, 0(\$t0)$

$\# t0 = 4g$
 $\# t0 = \text{Addr}(B[9])$
 $\# t0 = B[9]$
 $\# t0 = t0 + 1$
 $\# t0 = 4(B[9] + 1)$
 $\# f = A[B[9] + 1]$

2.10

$\# t0 = A[1]$
 $\# t1 = A[0]$
 $\# \$t1 = A[1]$
 $\# \$t0 = A[1]$
 $\# f = t1 + t0 \rightarrow 2(A)$

2.12 $\$50 = 0x80000000$ $\$51 = 0xD0000000$

2.12.1

$$\$10 = \$0 + \$1 = 50000000$$

2.12.2

overflow

2.12.3

$$S1 - S0 = \text{~~50~~ } 0x50000000$$

A	B	C	D
10	11	12	13

2.12.4

10

2.12.5

$$t0 = (\$0 + \$1) + \$0 = D0000000$$

2.12.6

yes, over

2.18

$$MIP \rightarrow 128 \quad \log_2 128 = 7$$

2.18.1

$$R \rightarrow rs, rt, rd = 7 \text{ bits}$$

$$\text{opcode} = 8$$

leaving 3

2.18.2

$$\text{op} = 8$$

$$rs, rt = 7$$

16M/10 for address

12. 18, 3

more reg \rightarrow immediate data could be reduced
so it can have higher reg lookup
plus instr. count requires more bits to
be stored.

\uparrow instr \rightarrow would need \uparrow reg

2.21

not \$t1, \$t2 \rightarrow NOR \$t1, \$t2, \$t2

2.22

?

$A = \lfloor t0 \rfloor \ll 4$

lw \$t3, 0(\$s1)
slr \$t1, \$t3, 4

2.26

2.26.1

$\left. \begin{array}{l} \$t1 = 10 \\ \$s2 = 0 \end{array} \right\} \$s2 = 20$

2.26.2

addi \$t2, \$t2, -1
beq \$t2, \$0, loop

2.26.3

$5 \times N$

?

2.29

\$t1 = i

\$s2 = result

\$s0 = Memarray

for (i=0; i<100; i++)

{

result += MemArray[s0];

s0 = s0 + 4;

}

2.38

lbw \$t0, 0(\$t1)
sw \$t0, 0(\$t2)

\$t1 = 0x1000 0000

\$t2 = 0x1000 0011

ⓐ 0x1000 0000 = 0x11223344

ⓑ \$t2 = 0x0000 0011

2.47

70% arithmetic
10% load/store
20% branch

2.47.1

arith: 2 cycles
load/store: 6 cycles
branch: 3 cycles

$$\frac{(70 \times 2) + (6 \times 10) + (3 \times 20)}{100} = 2.6$$


2.47.2

25% improvement performance, load/store & branch

$$\frac{1.25(70 \times 2) + 60 + 60}{100} = 1.07$$

2.47.3

50% ↑ perf, wad/store & branch


$$\frac{0.5 (70.2) + 60 + 60}{100} = 0.14$$