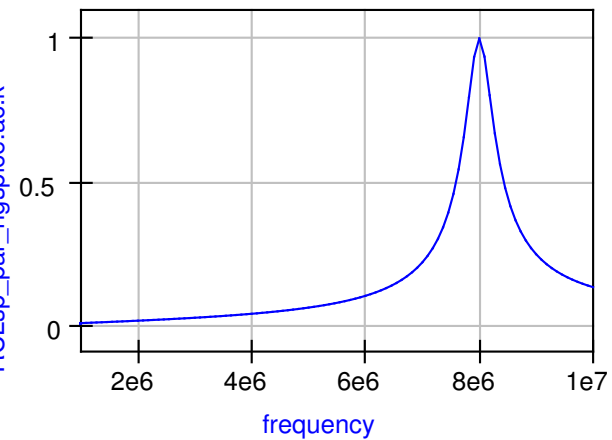
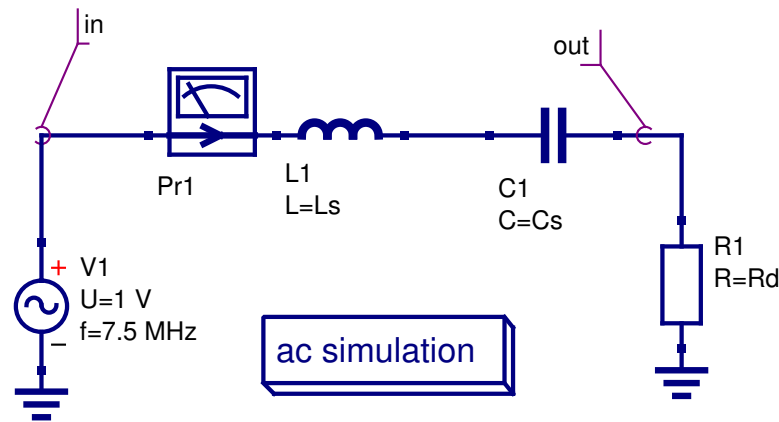
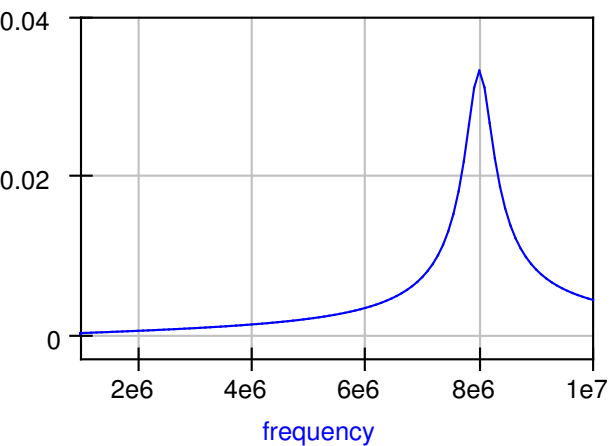


RCLsp_par_ngspice:ac.k



RCLsp_par_ngspice:ac.pwr



ac simulation

AC1
Type=lin
Start=1 MHz
Stop=10 MHz
Points=100

.PARAM

SpicePar1
Rd=30
f=8e6
Cs=40e-12
Ls={1/(4*(4*atan(1))**2*f**2*Cs)}

Nutmeg

NutmegEq1
Simulation=ac
K=v(out)/v(in)
Pwr=v(in)*VPr1#branch

```
* Qucs 0.0.19 /home/vvk/.qucs/RCLsp_par.sch
.PARAM Rd = 30
.PARAM f = 8e6
.PARAM Cs = 40e-12
.PARAM Ls = {1/(4*(4*atan(1))**2*f**2*Cs)}
L1 _net0 _net1 {LS}
C1 _net1 out {CS}
VPr1 in _net0 DC 0 AC 0
V1 in 0 DC 0 SIN(0 1 7.5MEG 0 0) AC 1
R1 0 out {RD}
.control
set filetype=ascii

AC LIN 500 1MEG 10MEG
let K = v(out)/v(in)
let Pwr = v(in)*VPr1#branch
write RCLsp_par_ac.txt v(in) v(out) K Pwr
destroy all
reset

exit
.endc
.END
```