

CS & IT ENGINEERING



Computer Network

Error Control

Lecture No. - 04

By - Abhishek Sir





Recap of Previous Lecture



Topic

CRC



Topics to be Covered



Topic

CRC



#Q. Consider the message $M = 1010001101$. The cyclic redundancy check (CRC) for this message using the divisor polynomial $x^5 + x^4 + x^2 + 1$ is

[GATE 2005]

- (A) 01110
- (B) 01011
- (C) 10101
- (D) 10110

Ans: A



ABOUT ME

Hello, I'm **Abhishek**

- GATE CS AIR - 96
- M.Tech (CS) - IIT Kharagpur
- 12 years of GATE CS teaching experience

Telegram Link : https://t.me/abhisheksirCS_PW



$$\begin{array}{r} 110101 \longdiv{101000110100000} \\ 110101 \\ \hline 1110110100000 \\ 110101 \\ \hline 111010100000 \\ 110101 \\ \hline 1111100000 \\ 110101 \\ \hline 10110000 \\ 110101 \\ \hline 1100100 \\ 110101 \\ \hline 01110 \\ \text{CRC} \end{array}$$

CRC = (5 bit)

$$\begin{aligned} G(x) &= x^5 + x^4 + x^2 + 1 \\ \text{Divisor} &= \underbrace{110101} \end{aligned}$$

#Q. The message 11001001 is to be transmitted using the CRC polynomial $x^3 + 1$ to protect it from errors. The message that should be transmitted is:

[GATE 2007]

- (A) 11001001000
- (B) 11001001011
- (C) 11001010
- (D) 110010010011

4bit CRC

Ans : B

$$\begin{array}{r} 1001 \longdiv{11001001000} \\ 1001 \\ \hline 1011001000 \\ 1001 \\ \hline 10001000 \\ 1001 \\ \hline 11000 \\ 1001 \\ \hline 1010 \\ 1001 \\ \hline 011 \\ \text{CRC} \end{array}$$

(CRC = 3 bit)

$$G(x) = x^3 + 1$$

$$\text{Divisor} = 1001$$



CASE II : Error Included

Transmitter transmit : $[M(X) * X^n] + [R(X)]$

Receiver received : $[M(X) * X^n] + [R(X)] + [E(X)]$

Received Data = Transmitted Data + Error



Topic : Error Polynomial

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$E(X)$: Error Polynomial Function

→ Coefficient are either Zero or One

Data : m bits CRC : n bits

Codeword : $(m + n)$ bits

Degree($E(X)$) < $(m + n)$

$$E(X) \neq 0$$



Topic : Error Polynomial

For single bit error :

$$\underline{E(X)} = \boxed{X^i}$$

Where [$0 \leq i < (m + n)$]

$$i \rightarrow 0 \text{ to } (m+n-1)$$



Topic : Error Polynomial



Transmitter transmitted :

$$X^{10} + X^7 + X^6 + X^5 + X^3 + X + 1$$



Receiver received :

$$X^{10} + X^7 + X^6 + X^5 + X + 1$$



$$E(X) = X^3$$



Topic : Error Polynomial

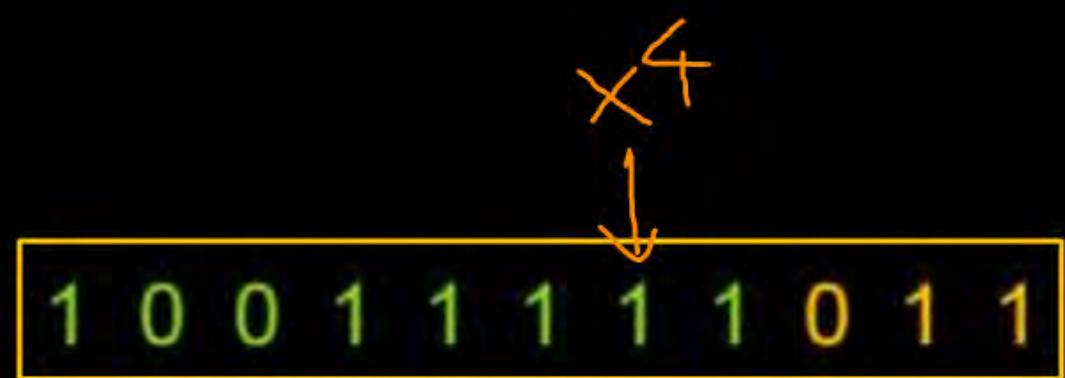
Transmitter transmitted :

$$X^{10} + X^7 + X^6 + X^5 + X^3 + X + 1$$



Receiver received :

$$X^{10} + X^7 + X^6 + X^5 + X^4 + X^3 + X + 1$$



$$E(X) = X^4$$



Topic : Error Polynomial



For two bit error :

$$\underline{E(X)} = (X^i + X^j)$$

Where i & j are 0 to $(m + n - 1)$ and $[i > j]$



Topic : Error Polynomial

Transmitter transmitted :

$$X^{10} + X^7 + X^6 + X^5 + X^3 + \underline{X} + 1$$



Receiver received :

$$X^{10} + \underline{X^9} + X^7 + X^6 + X^5 + X^3 + 1$$



$$E(X) = X^9 + X$$

$$BL = (g - 1 + 1) = g$$



Topic : Error Polynomial



For two bit error :

$$E(X) = (X^i + X^j)$$

Where i & j are 0 to $(m + n - 1)$ and $\underbrace{i > j}$

$$\text{Burst length} = (i - j + 1)$$



Topic : CRC



CASE II : Error Included

Transmitter transmit : $[M(X) * X^n] + [R(X)]$

Receiver received : $[M(X) * X^n] + [R(X)] + [E(X)]$

Receiver protocol :

$[M(X) * X^n + R(X) + E(X)]$ [Modulo-2 Division] $[G(X)]$



Topic : CRC

$$\frac{A+B+C}{G} = \frac{A+B}{G} + \frac{C}{G}$$



CASE II : Error Included

Receiver protocol :

$$[M(X) * X^n + R(X) + E(X)] \text{ [Modulo-2 Division]} \quad [G(X)] \rightarrow R'(X)$$

$$\begin{aligned} & [M(X) * X^n + R(X)] \text{ [Modulo-2 Division]} \quad [G(X)] \rightarrow R_1(X) \\ + \quad & [E(X)] \text{ [Modulo-2 Division]} \quad [G(X)] \rightarrow R_2(X) \end{aligned}$$

$$R'(X) = R_1(X) + R_2(X)$$



Topic : CRC



CASE II : Error Included

$$\begin{aligned} & [M(X) * X^n + R(X)] \text{ [Modulo-2 Division]} [G(X)] \rightarrow R_1(X) \\ & + [E(X)] \text{ [Modulo-2 Division]} [G(X)] \rightarrow R_2(X) \end{aligned}$$

$$\frac{E(X)}{G(X)}$$

First equation definitely lead “zero remainder”

For successful error detection, second equation should lead “non-zero remainder”



Topic : CRC



Example 4 :

$$G(X) = X^3 + 1$$

$$\text{Data} = \underbrace{10011101}_0$$

$$\text{Transmitted Data} = \boxed{10011101 \underline{1} \underline{0} \underline{0}}$$



Topic : CRC

Example 4 :

$$\text{degree}(G(x)) = 3$$

$$G(x) = x^3 + 1 \quad x^6 \quad x^3$$

$$\text{Transmitted Data} = \boxed{10011101100}$$

$$E(x) = x^6 + x^3$$

$$\text{Received Data} = \boxed{10010100100}$$

$$\frac{E(x)}{G(x)} = \frac{x^6 + x^3}{x^3 + 1}$$

$$= \frac{x^3(x^3 + 1)}{(x^3 + 1)}$$

*Complete
division

$$BL = (6 - 3 + 1) = 4$$





Topic : CRC



Example 4 :

$$G(X) = X^3 + 1$$

Received Data = 1 0 0 1 0 1 0 0 1 0 0

Receiver Conclusion : No any Error detected,
Accept the data

AT Recv :-

$$\begin{array}{r} 1001 \\ \overline{)10010100100} \\ 1001 \\ \hline 1001 \\ \overline{)100} \\ 100 \\ \hline 0 \end{array}$$

$\curvearrowleft R'(x)$

$$G(X) = X^3 + 1$$

$$\text{Divisor} = 1001$$



Topic : CRC

Example 5 :

$$\text{degree}[G(x)] = 3$$

$$G(x) = x^3 + 1$$

x^5	x^3									
↓	↓									
1	0	0	1	1	1	0	1	1	0	0

Transmitted Data = 10011101100

$$E(x) = x^5 + x^3$$

$$\text{Received Data} = \boxed{10011000100}$$

$$\frac{E(x)}{G(x)} = \frac{(x^5+x^3)}{(x^3+1)}$$

$$= \frac{x^3 * (x^2+1)}{(x^3+1)}$$

* Not causes complete division

$$BL = (5-3+1) = 3$$



Topic : CRC



Example 5 :

$$G(X) = X^3 + 1$$

Received Data = **1 0 0 1 1 0 0 0 1 0 0**

Receiver Conclusion : Error Detected
(Reject the data)

AT Recv :-

$$\begin{array}{r} 1001 \longdiv{10011000100} \\ 1001 \\ \hline 1000100 \\ 1001 \\ \hline 1100 \\ 1001 \\ \hline 101 \\ \text{\textbraceleft } R'(x) \end{array}$$

$$G(X) = X^3 + 1$$

$$\text{Divisor} = 1001$$



Topic : CRC



CASE II : Error Included

$$\frac{E(x)}{G(x)}$$

[E(X)] [Modulo-2 Division] [G(X)]

→ G(X) have (n+1) terms (length) $\Rightarrow \text{degree}[G(x)] = n$

→ If E(X) causes at-most (n length burst error)
then above equation always lead non-zero remainder



Topic : CRC Property

** CRC limit

- CRC can detect any length burst error,
up-to the degree of generator polynomial function





Topic : CRC Property

- if the count of total number of corrupted bits is odd
then the $E(X)$ definitely contains odd number of terms



Topic : CRC Property

→ if $(X+1)$ is a factor of $G(X)$
[$\underline{\text{G}(X)}$ is completely divisible by $\underline{(X+1)}$]

then $\underline{\text{G}(X)}$ definitely contains even number of terms

$$G(X) = \underbrace{F_1(X)} * \underbrace{F_2(X)}$$

$$G(X) = \underbrace{(X+1)} * F_2(X)$$



No. of terms, must be even



Topic : CRC Property *



→ if $(X+1)$ is a factor of $G(X)$

then CRC can detect “all the errors where count of corrupted bits are odd”

$$\frac{E(x)}{G(x)}$$

odd terms

even terms



Topic : CRC

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Example 6 :

$$G(X) = X^3 + 1 \quad \begin{matrix} X^7 \\ \downarrow \\ X^5 \\ \downarrow \\ X^4 \end{matrix}$$

Transmitted Data = 1 0 0 1 1 1 0 1 1 0 0

$$E(X) = X^7 + X^5 + X^4$$

Received Data = 1 0 0 0 | 0 1 1 1 0 0

$$\frac{E(X)}{G(X)} = \frac{x^7+x^5+x^4}{x^3+1}$$

* Not causes complete division



Topic : CRC



Example 6 :

$$G(X) = X^3 + 1$$

Received Data = 1 0 0 0 1 0 1 1 1 0 0

Receiver Conclusion : Error Detected

AT Recv :-

$$\begin{array}{r} 1001 \overline{)10001011100} \\ 1001 \\ \hline 11011100 \\ 1001 \\ \hline 1001100 \\ 1001 \\ \hline 000 \\ R'(x) \end{array}$$

$$G(X) = X^3 + 1$$

$$\text{Divisor} = 1001$$

#Q. Let $G(X)$ be the (generator polynomial) used for CRC checking. What is the condition that should be satisfied by $G(X)$ to detect odd number of bits in error?

[GATE 2009]

IIT-R

~~(A) $G(X)$ contains more than two terms^{one}~~

(B) $G(X)$ does not divide $1 + X^k$, for any k not exceeding the frame length

~~(C) $(1 + X)$ is a factor of $G(X)$~~

(D) $G(x)$ has an odd number of terms

Ans: C



Topic : CRC Property

- For two-bit error,
E(X) must have two terms only

$$E(X) = (X^i + X^j)$$

Where i & j are 0 to $(m+n-1)$, $i > j$

$$E(X) = X^j (X^{(i-j)} + 1)$$

suppose $k = (i-j)$

$$E(X) = X^j (X^k + 1)$$



Topic : CRC Property *



→ To detect any two-bit error,
G(X) does not divide $(X^k + 1)$
[for any k from 1 to $(m + n - 1)$]

$\Rightarrow G(X)$ does not divide $(1+X)(1+X^2)(1+X^3)$
..... $(1+X^{(m+n-1)})$



Topic : CRC

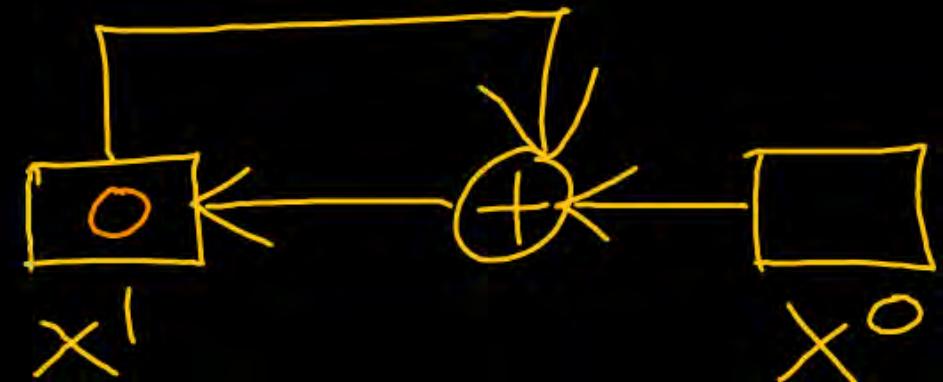
P
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Example 7:

$$G(X) = X + 1$$

Message (DATA) = 1 0 1 1 1 0 1
7bit data

Transmitted Data = 1 0 1 1 1 0 1 1
DATA ↑
 one bit CRC



one-bit parity
with even parity

$$\begin{array}{r} \boxed{10111010} \\ \underline{-1111010} \\ \hline 11010 \\ \underline{-11} \\ \hline 10 \\ \underline{-11} \\ \hline -1 \end{array}$$

CRC

$$G(X) = X + 1$$

$$\text{Divisor} = | |$$



Topic : CRC



Example 7 :

$$G(X) = X + 1$$

Transmitted DATA = 1 0 1 1 1 0 1 1

One-bit CRC [CRC - 1] is same as **one-bit parity** with “**even parity**”.



Topic : CRC



Example 8 :

$$G(X) = X^3 + 1$$

Message (DATA) = 1 0 0 1 1 0 0 1

CRC = 0 0 0

$$\begin{array}{r} 1001 \longdiv{10011001000} \\ \underline{1001} \\ \hline 1001000 \\ \underline{1001} \\ \hline 000 \\ \underbrace{\hspace{1cm}}_{\text{CRC}} \end{array}$$

$$G(X) = X^3 + 1$$

$$\text{Divisor} = 1001$$



Topic : CRC



Example 9:

$$G(X) = X^3 + X^2 + X + 1$$

Message (DATA) = 1 1 1 1 0 0 0 1

CRC = | | |

$$\begin{array}{r} \boxed{111100001000} \\ \hline 1111 \\ \hline 1000 \\ \hline 111 \\ \hline \underbrace{\quad\quad\quad}_{\text{CRC}} \end{array}$$

$$G(X) = X^3 + X^2 + X + 1$$

$$\text{Divisor} = \boxed{111}$$



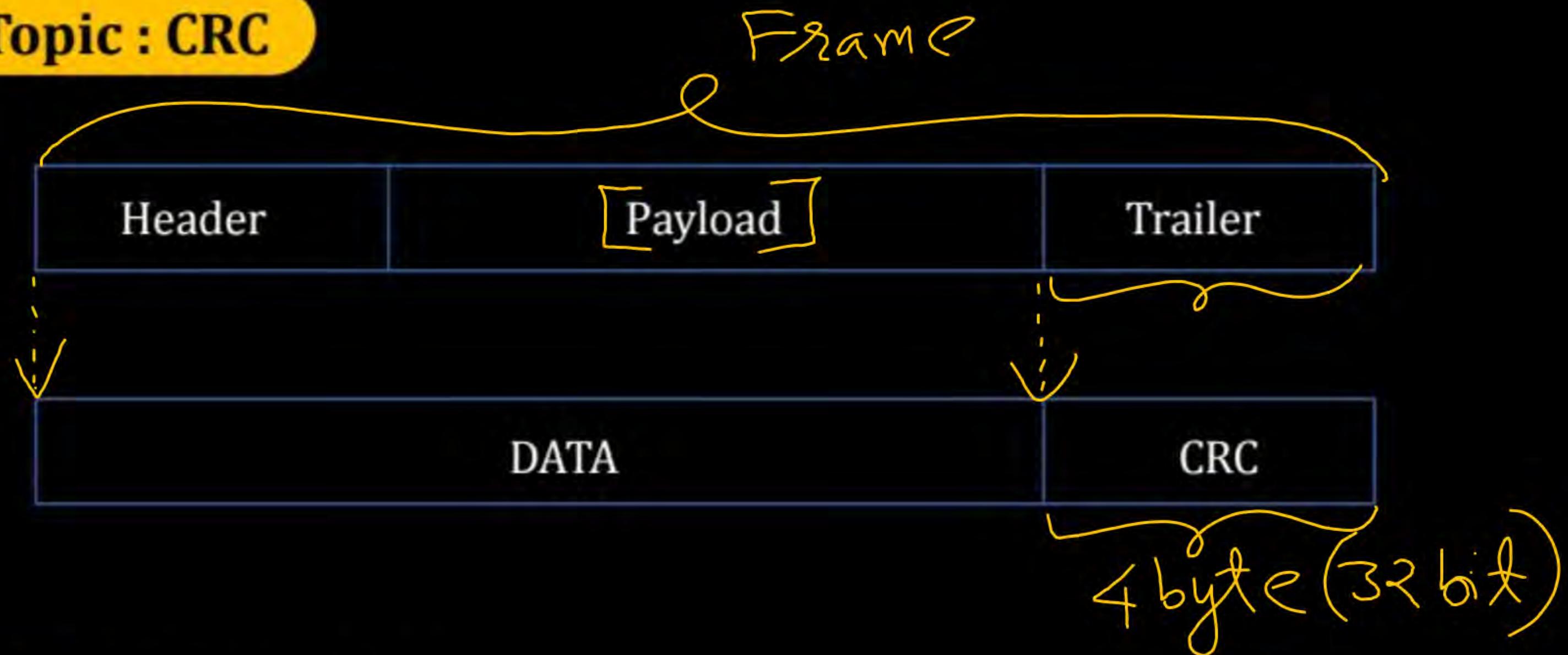
Topic : CRC Property



- CRC can be “all zero bits” and can be “all one bits”



Topic : CRC



CRC-32 : [32-bit / 4 byte]

$$G(X) = X^{32} + \dots + 1$$



Topic : Cyclic Code

CRC [$G(X) = X^3 + 1$] and 3 data bits

\Rightarrow CRC always produces cyclic code.

Data --> Codeword
 $d_1d_2d_3$ --> $d_1d_2d_3C_1C_2C_3$

0 0 0	-->	0 0 0 0 0 0
0 0 1	-->	0 0 1 0 0 1
0 1 0	-->	0 1 0 0 1 0
0 1 1	-->	0 1 1 0 1 1
1 0 0	-->	1 0 0 1 0 0
1 0 1	-->	1 0 1 1 0 1
1 1 0	-->	1 1 0 1 1 0
1 1 1	-->	1 1 1 1 1 1

2^3

2^3 valid codewords
of length 6





Topic : Valid Codewords vs Invalid Codewords

m bits input (data) → N bits output (codeword)

$$\rightarrow \text{Number of parity bits} = (N - m)$$

$$\rightarrow \text{Number of valid codewords} = 2^m$$

$$\rightarrow \text{Number of invalid codewords} = (2^N - 2^m)$$



2 mins Summary

Topic

CRC





THANK - YOU

