

CS & IT ENGINEERING



Basics of Computer System

Memory System

Lecture No.- 08

By- Vishvadeep Gothi sir



Recap of Previous Lecture



Topic

CPU Cycle

Topic

Memory Cycle

Topic

Memory Access Time



Topics to be Covered



Topic

Memory Access

Topic

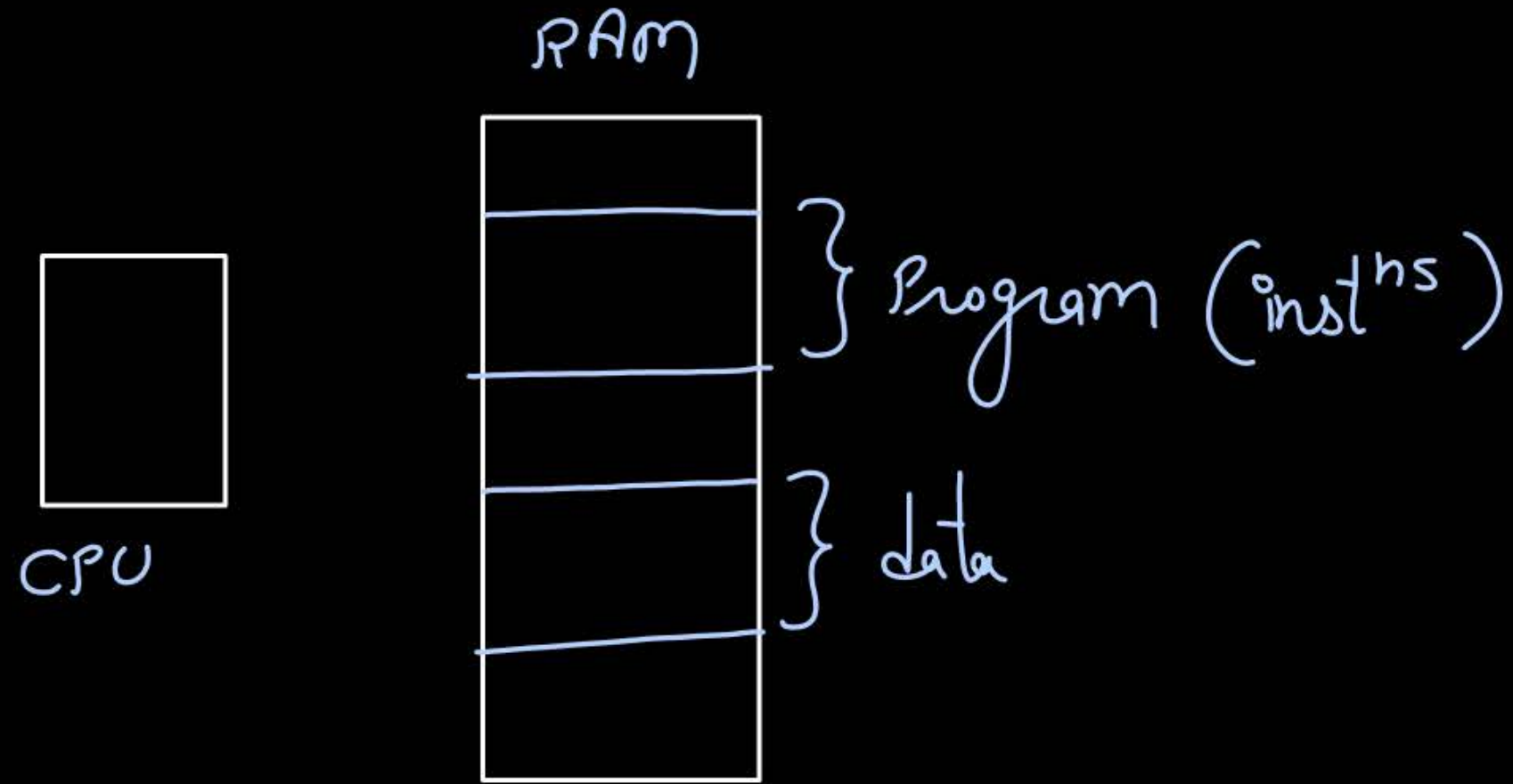
Memory Address

Topic

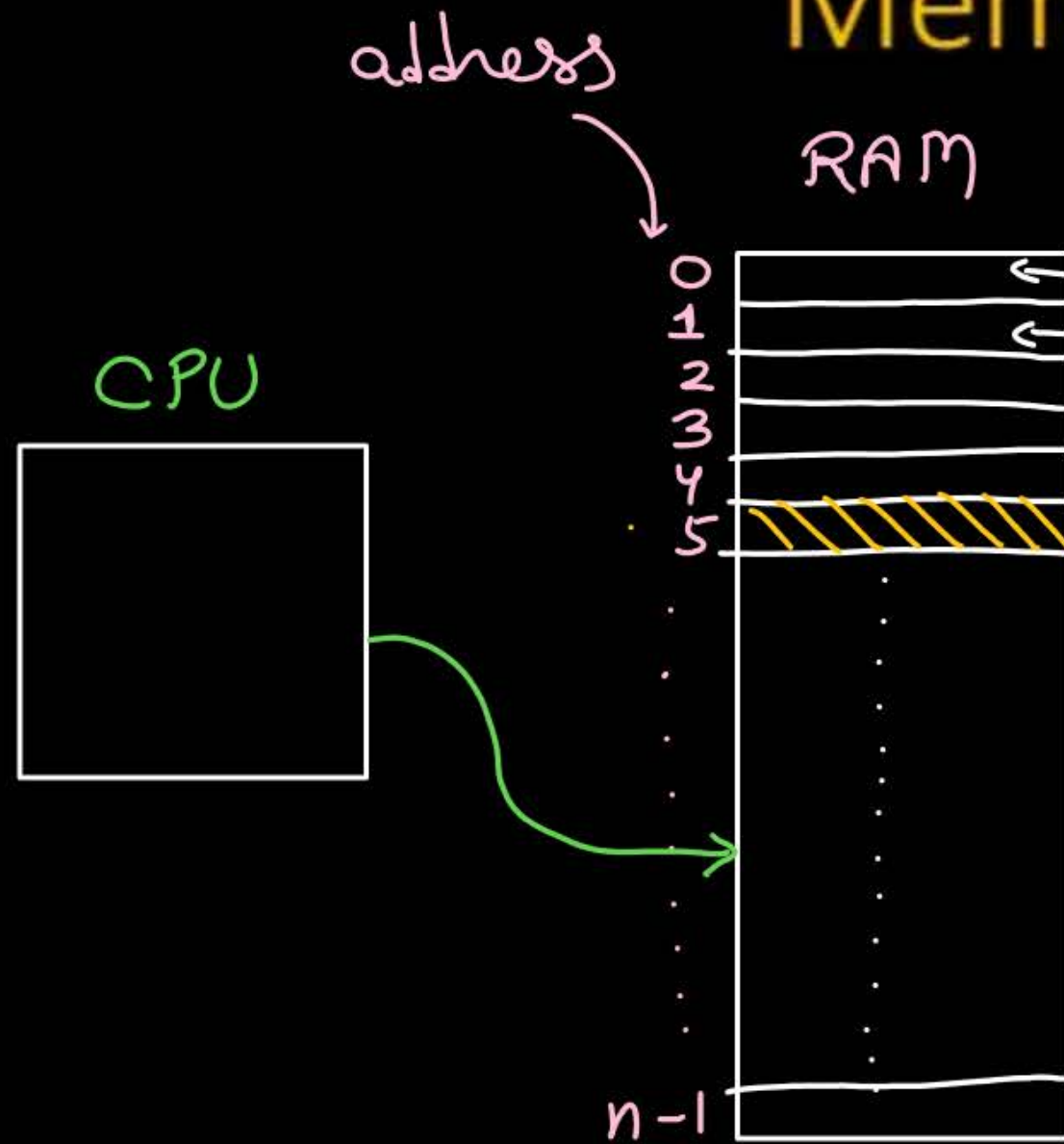
Read & Write



Memory Access



Memory Address



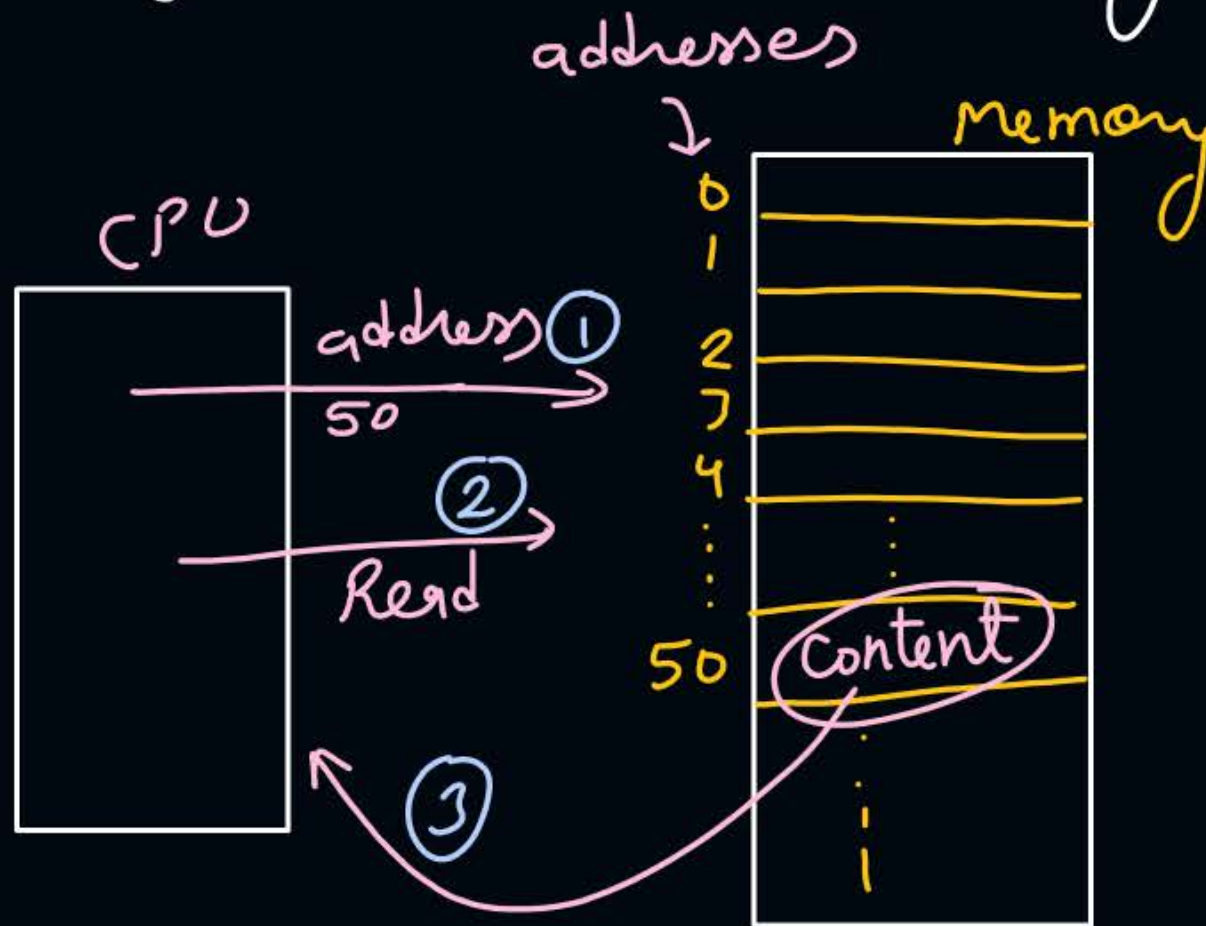
each cell has an address.

for each memory access, CPU sends address to memory.

Memory Read

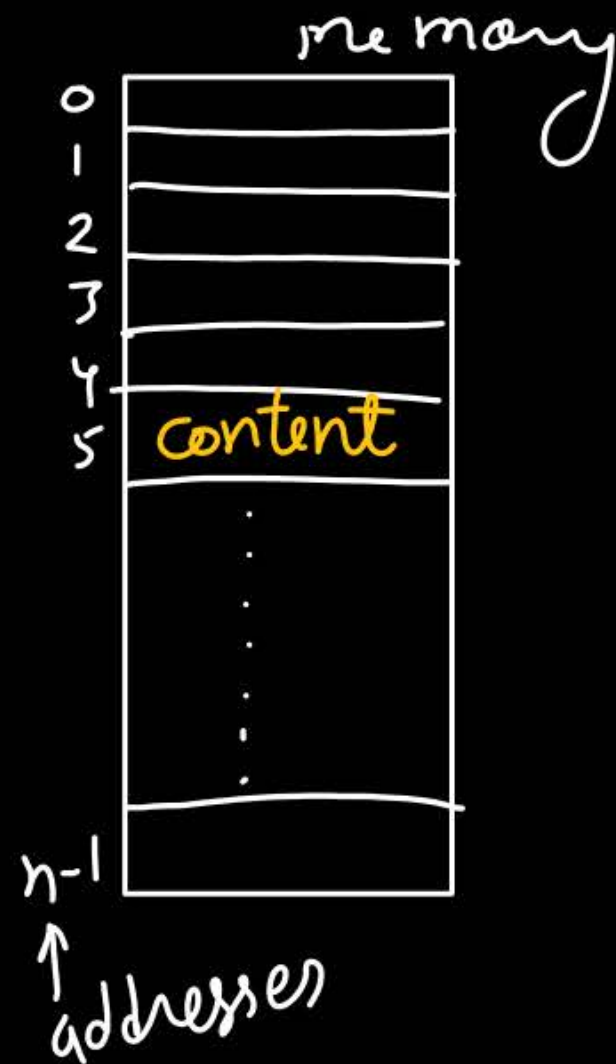
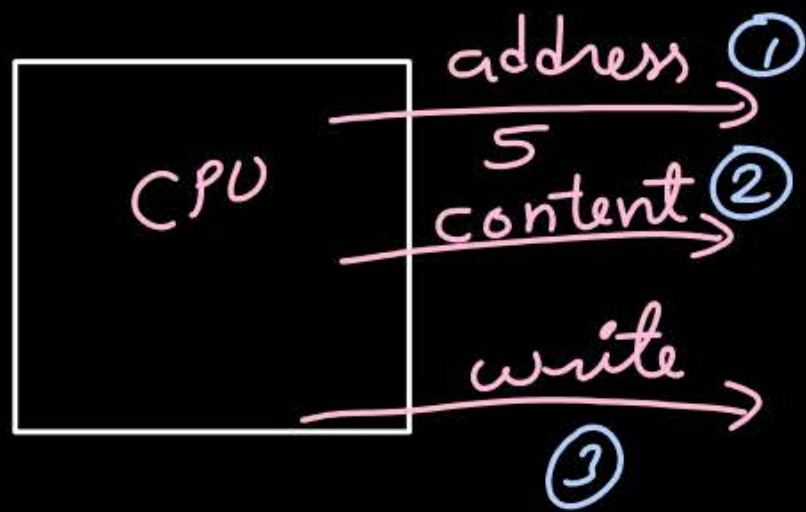
→ CPU sends address to memory and tells it to perform read.

→ memory sends content of given address to CPU.

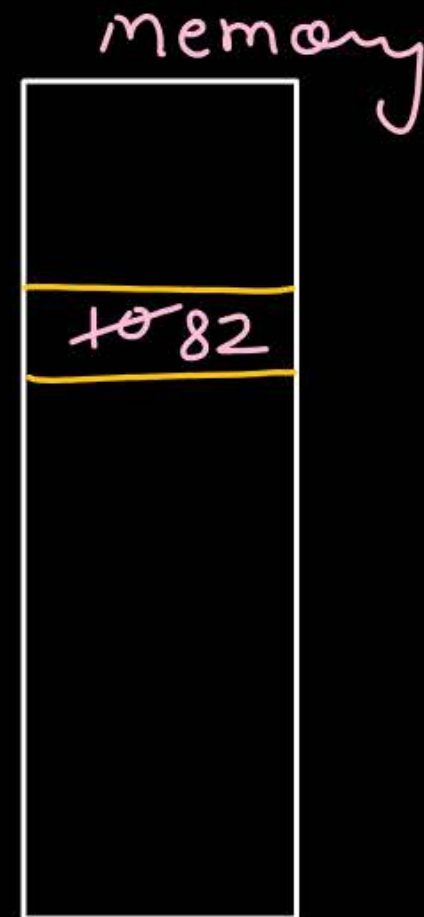


Memory Write

- CPU sends address and content to memory, and tells it to perform write
- memory writes given content on given address.



CPU says
perform
write at address
25, content is 82





2 mins Summary



Topic

Memory Access

Topic

Memory Address

Topic

Read & Write



Happy Learning

THANK - YOU

```
int x = 5;
```

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...
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```
x = 8
```