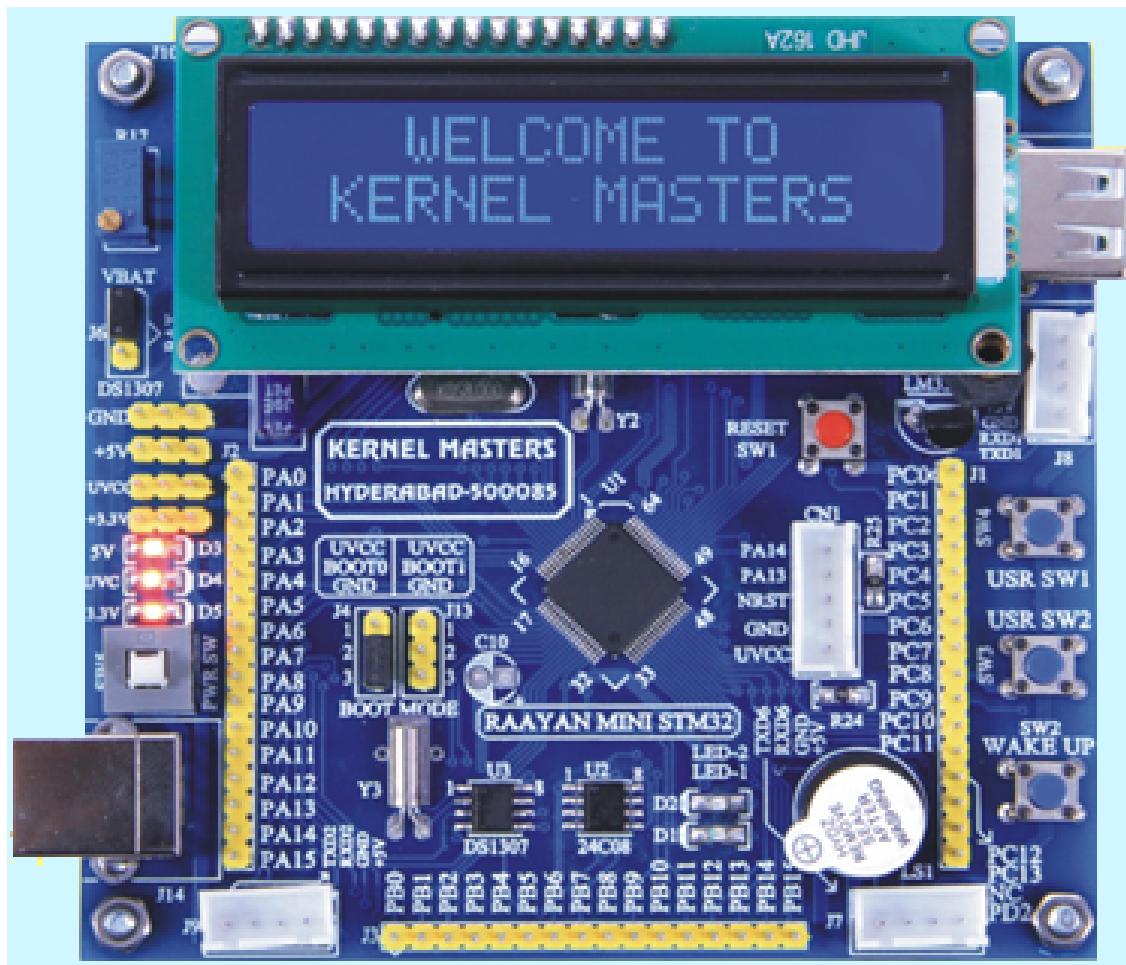


# Raayan Mini Board (ARM Cortex M0/M3/M4) User Manual Rev 3.0



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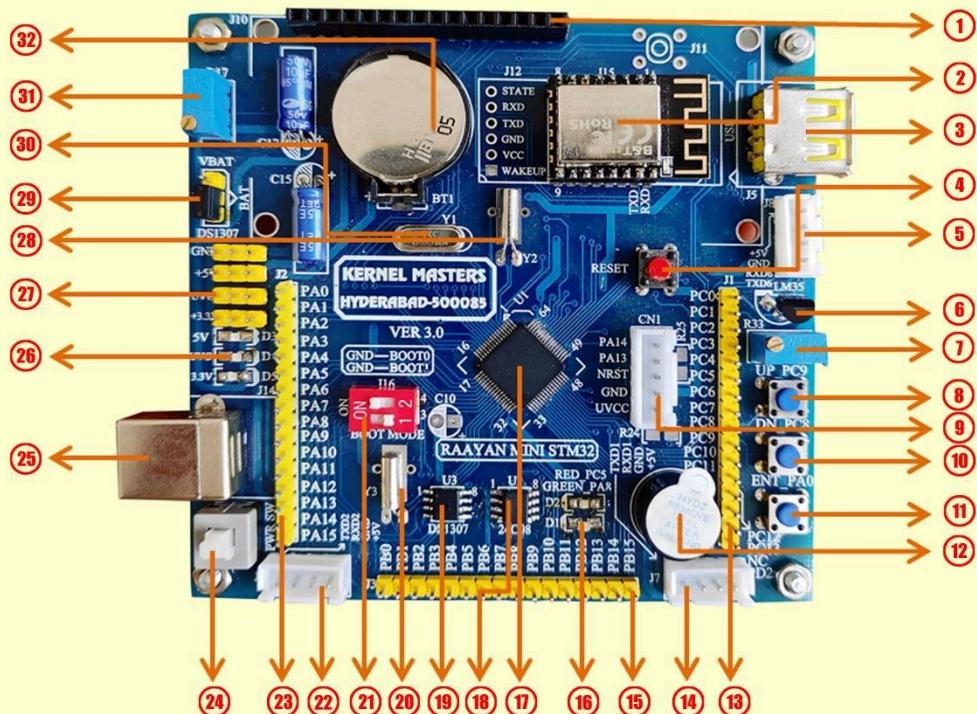


## 1. Introduction to Raayan Mini Board

### 1.1. Board Overview

The STM32 Based Raayan Mini Development board allows users to easily develop applications with the STM32F401RBTx high-performance microcontroller with the Arm® Cortex®-M4 32-bit core. It includes everything required either for beginners or experienced users to get started quickly. The Raayan Mini Development board also features programmable user buttons and an RG LED for custom applications. STM32 Based Raayan Mini Development board comes with the STM32 comprehensive free software libraries and examples available with the STM32CubeF4 MCU Package. Figure 1-1 shows a photo of the Raayan Mini Development Board.

**Figure 1-1. Raayan Mini Development Board**



1.16x2 LCD	9.DEBUGGER	17.STM32F401RBT6	25.USB TYPE - A
2.BLUETOOTH/WI - FI	10.SWITCH_DN	18.EEPROM	26.POWER LEDs
3.USB TYPE - B	11.SWITCH_ENT	19.RTC	27.POWER PINS
4.RESET	12.BUZZER	20.XTAL - 32.7KHZ	28.XTAL - 32.7KHZ
5.UART 6	13.PORT - C	21.BOOT SWITCH	29.VBatt
6.LM35	14.UART 1	22.UART 2	30.XTAL - 8MHz
7.ADC POT	15.PORT - B	23.PORT - A	31.LCD TRIM PORT
8.SWITCH_UP	16.USER LED	24.POWER SWITCH	32.BATTERY

## 1.2. Kit Contents

The Raayan Mini STM32 Development Board Kit contains the following items:

- Raayan Mini Development Board (STM32F401RBT6)
- ST-Link Serial wire Debugger
- USB – TTL module(CH340)
- USB Type A to B cable
- Jumper wires
- User manual

## 1.3. Getting Started

Follow the sequence below to configure the Raayan Mini STM32F4 Development Board and launch application:

1. Check the switch positions of BOOT0, BOOT1 pins on the board.
2. Connect the STM32F4 Development board to a PC with a USB cable Type-A to B through USB connector J14 to power the board.
3. Press the push button SW5 (PWR SW). LEDs D3, D4, D5 then lights up.
4. The board runs a preloaded Board test application. The 16 X 2 LCD screen shows “Welcome to KM” followed by some test cases for testing the board and peripherals. Perform each test cases as shown in the LCD screen until all the peripherals are tested OK.
5. Discover the STM32F401RBT6 features and develop the application using available source code and libraries.
6. Connect the ST LINK to CN1 before flashing your application code to the microcontroller.

## 1.4. Features

Your Raayan Mini Development board includes the following features:

- STM32F401RBT6 microcontroller featuring 32-bit Arm® Cortex®-M4 with FPU core, Up to 128-Kbytes of Flash memory and 64-Kbytes of RAM, Adaptive real-time accelerator (ART Accelerator™) allowing 0-wait state execution from Flash memory, frequency up to 84 MHz, memory protection unit, 105 DMIPS/1.25 DMIPS/MHz (Dhrystone 2.1), and DSP instructions
- On-board ST-LINK Serial wire debugger/programmer
- 1x12-bit, 2.4 MSPS A/D converter: up to 16 channels
- One PWM timer for motor control
- GPIO ports with interrupt capability
  - All IO ports 5 V tolerant
  - Up to 3 UART Headers
  - Up to 78 fast I/Os up to 42 MHz

- Communication interfaces
  - I2C interface (1Mbit/s, SMBus/PMBus)
  - Up to 3 USARTs (2 x 10.5 Mbit/s, 1 x 5.25 Mbit/s), ISO 7816 interface, LIN, IrDA, modem control)
  - SPI interface (up to 42 Mbits/s at fCPU= 84 MHz), SPI2 and SPI3 with muxed full-duplex I2S to achieve audio class accuracy via internal audio PLL or external clock
- Up to 8-Kbytes of On – board EEPROM
- On – board temperature sensor
- On-board Buzzer
- 16 X 2 LCD interface
- On-board Wi-Fi interface.
- USB Type A interface
- Flexible power-supply options: ST-LINK, USB Type B connector
- External application power supply: 3 V and 5 V
- User (application/wake) and reset push-buttons
- On - board RTC
- 5 LEDs:
  - 3 Power LEDs and 2 user LEDs
- Support for a wide choice of Integrated Development Environments (IDEs) including IAR Embedded Workbench®, MDK-ARM, and STM32CubeIDE.

## 1.5. Specifications

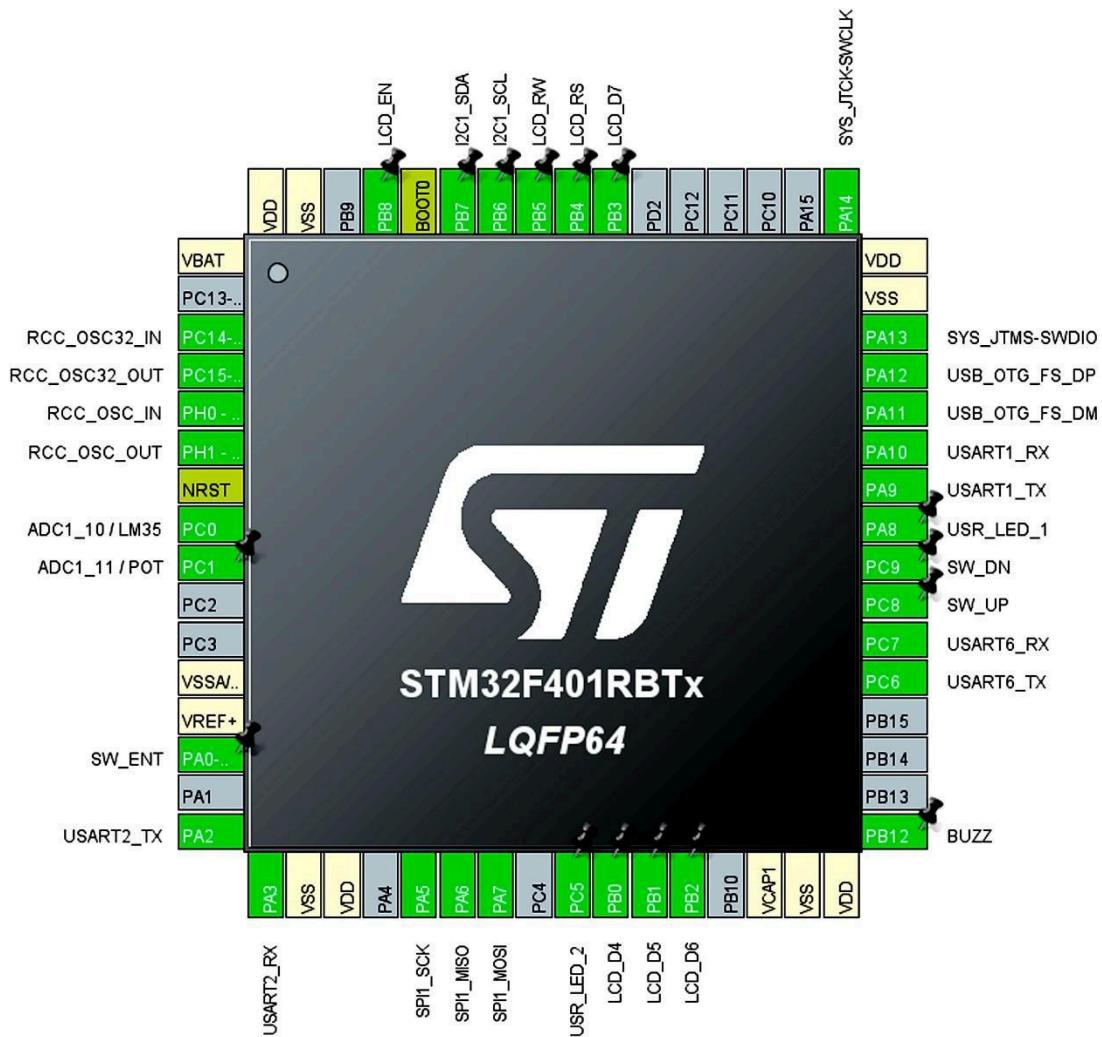
Table 1-1 summarizes the specifications for the Raayan Mini STM32F4 Development Board.

**Table 1-1. Raayan Mini STM32F4 Development Board Specifications**

Parameter	Value
Board supply voltage	4.75 VDC to 5.25 VDC from USB Device Type A to B cable (connected to a PC)
Dimensions	(L x W x H)
Break-out power output	<ul style="list-style-type: none"> <li>• 3.3 VDC (current rating)</li> <li>• 5.0 VDC (current rating)</li> </ul>
RoHS status	Compliant

### 1.6. Raayan Mini Pin diagram

## RAAYAN MINI PIN DIAGRAM

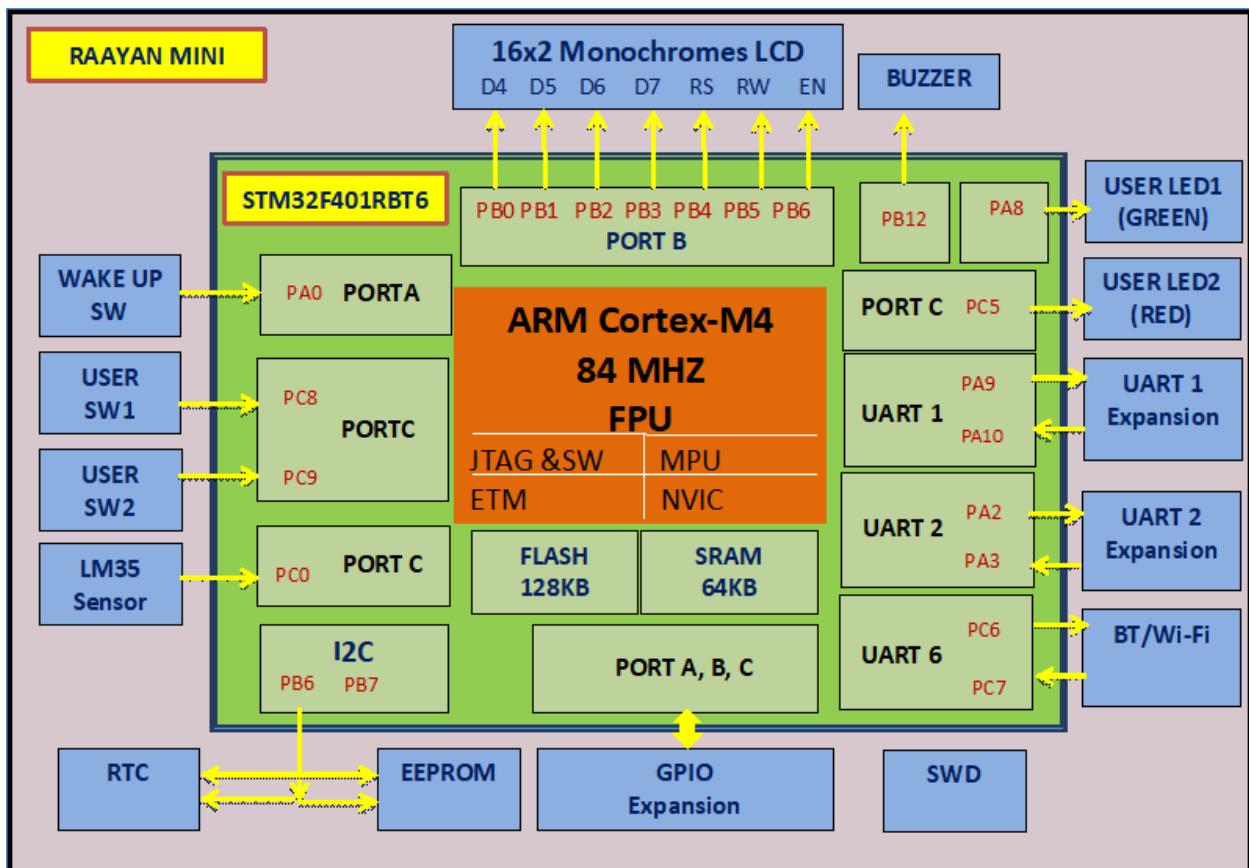


## 2. Hardware Description

### 2.1. Raayan Mini Board Block Diagram

The Raayan Mini STM32F4 Development Board includes a STM32F401RBT6 microcontroller and a serial wire debugger as well as a range of useful peripheral features.

**Figure 2-1. Raayan Mini STM32F4 Development Board Block Diagram**



## 2.2. Functional Description

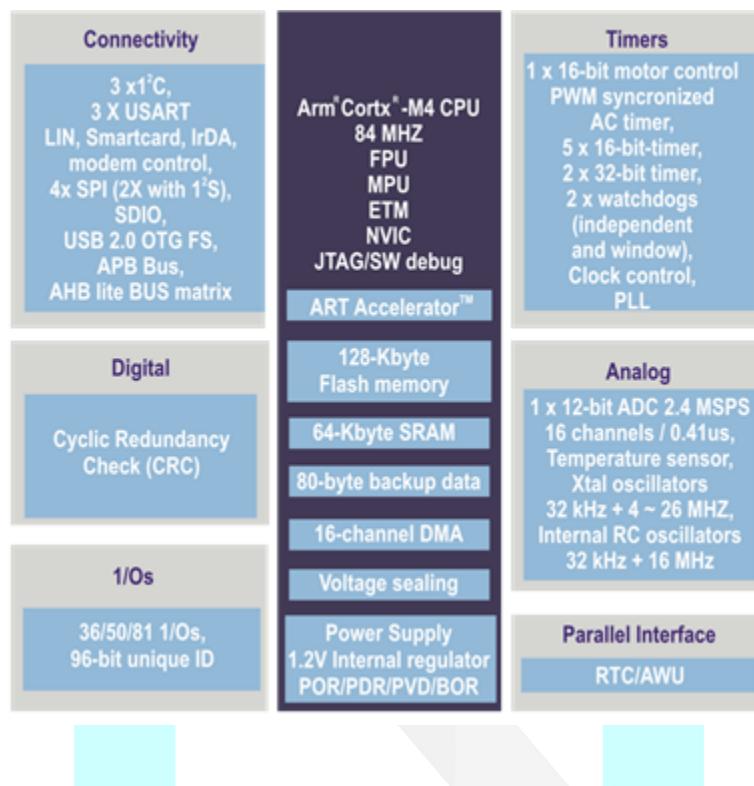
### 2.2.1. Microcontroller

The STM32F401RBT6 is a high performance 32-bit ARM Cortex-M4 based microcontroller with 128-kB Flash memory, 64-kB SRAM and a frequency of up to 84MHz. The Cortex®-M4 core features a Floating point unit (FPU) single precision which

supports all Arm single-precision data-processing instructions and data types. (See the STM32F401RBT6 microcontroller data sheet.)

It also implements a full set of DSP instructions and a memory protection unit (MPU) which enhances application security. All devices offer one 12-bit ADC, a low-power RTC, six general-purpose 16-bit timers including one PWM timer for motor control, two general-purpose 32-bit timers. They also feature standard and advanced communication interfaces.

**Figure 2-2. STM32F401RBT6 Specifications**

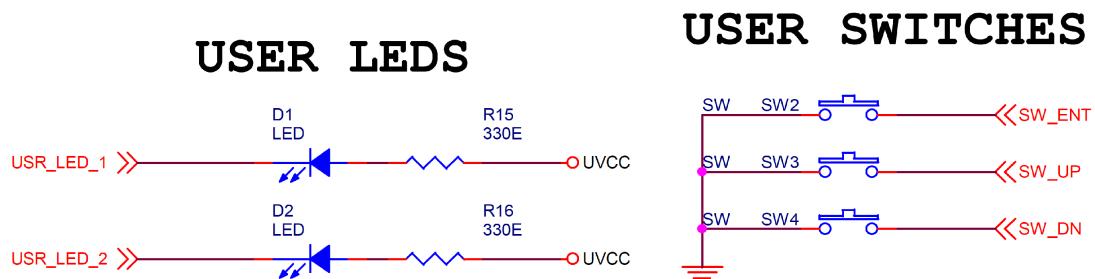


The STM32F401RBT6 microcontroller is factory-programmed with a Board test program. The Board test program resides in on-chip Flash memory and runs each time power is applied, unless the Board test application has been replaced with a user program.

## 2.2.2. User Switches and User LEDs

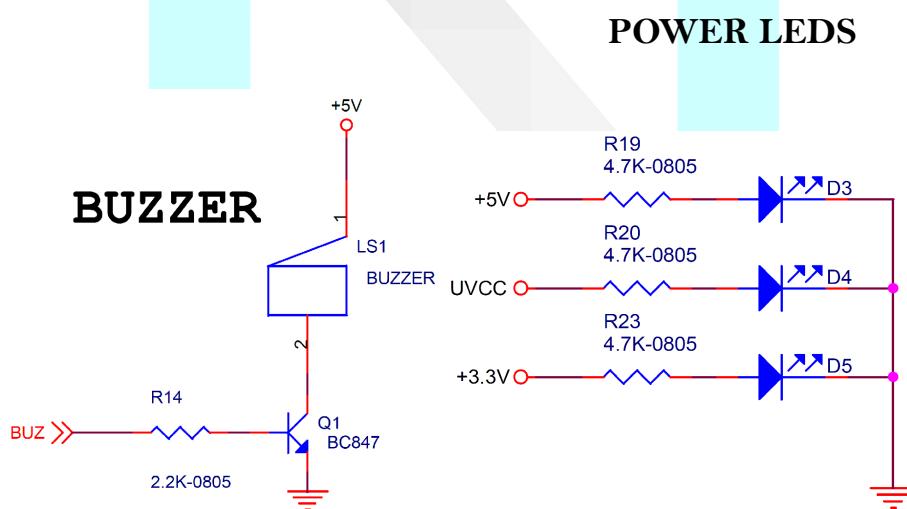
The Raayan Mini STM32F4 Development Board comes with five LEDs of which two LEDs can be configured for use in custom applications. The other 3 LEDs represent the power input to the board. Three user switches and one buzzer are also included on the board namely SW\_ENT (Enter), SW\_DN and SW\_UP respectively. All these can be used for some purposes in the user's custom application. Table 2-1 shows how these features are connected to the pins on the microcontroller.

**Figure 2-3. Raayan Mini STM32F4 Development Board LEDs, Switches and Buzzer schematics**

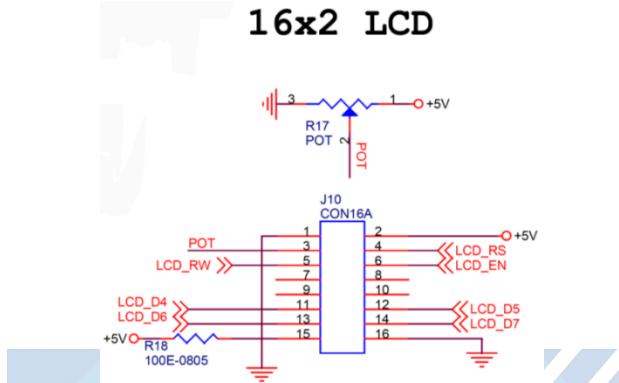


**Table 2-1. User Switches, LED and Buzzer signals**

GPIO Pin	Pin Function	Device
PA8	GPIO	USR_LED1(Green)
PC5	GPIO	USR_LED2(Red)
PA0	GPIO	SW_ENT(SW2)
PC8	GPIO	SW_UP(SW3)
PC9	GPIO	SW_DN(SW4)
PB12	GPIO	BUZZER



### 2.2.3. Display



The Raayan Mini STM32F4 Development Board comes with a 16 X 2 Monochrome LCD display that can be used in custom user applications. The LCD can also be removed if it is not required for some application. It is also provided with an LCD trim pot to adjust the brightness of the LCD screen. Table 2-3 shows how these pins are connected to the microcontroller.

**Figure 2-4. Raayan Mini STM32F4 Development Board LCD Display schematics**

**Table 2-2. LCD signals**

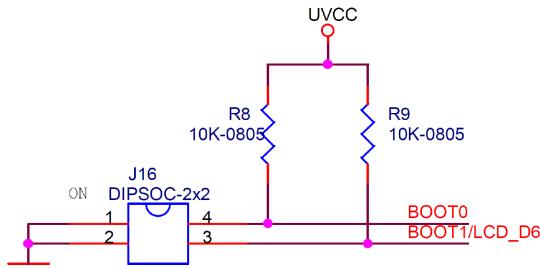
GPIO Pin	Pin function	Device
PB0	GPIO	LCD_DATA_4
PB1	GPIO	LCD_DATA_5
PB2	GPIO	LCD_DATA_6
PB3	GPIO	LCD_DATA_7
PB4	GPIO	LCD_RS
PB5	GPIO	LCD_RW
PB8	GPIO	LCD_EN

### 2.2.4. Boot Mode Switch

At startup, boot switch is used to select one out of three boot options:

- Boot from user flash
- Boot from system memory
- Boot from embedded SRAM

**Figure 2-4. Raayan Mini STM32F4 Development Board Boot switch schematics**



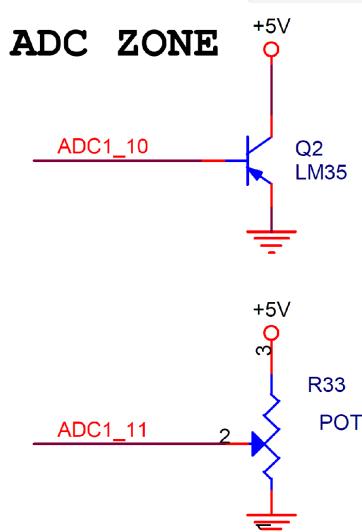
**Table 2-3. Boot modes**

Boot mode selection pins		Boot mode	Aliasing
BOOT1 (DIP Switch 2)	BOOT0 (DIP Switch 1)		
x	0	Main Flash memory	Main Flash memory is selected as the boot space
0	1	System memory	System memory is selected as the boot space
1	1	Embedded SRAM	Embedded SRAM is selected as the boot space

## 2.2.5. Analog to Digital Converter

The Raayan Mini Development board has two on-board analog devices which is one LM35 temperature sensor and one potentiometer. These devices are internally connected to ADC1\_10 and ADC1\_11 to convert the sensor output voltage into a digital value. One 12-bit analog-to-digital converter is embedded and shares up to 16 external channels, performing conversions in the single-shot or scan mode.

**Figure 2-5. Raayan Mini STM32F4 Development Board ADC Schematic**

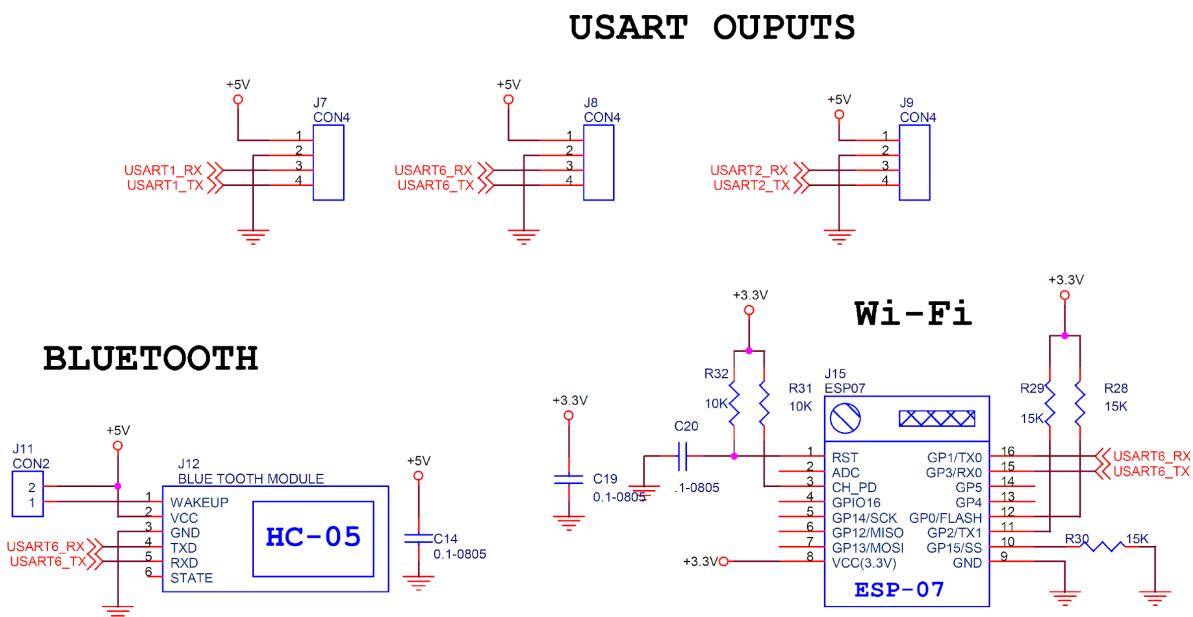


## 2.2.6. USART Zone

The Development Board provides three dedicated USART headers which helps in communicating various peripherals or sensors that may be used in your custom projects. USART headers are named J7, J8 AND J9 respectively. J7 is USART1, J8 is USART6 and J9 is USART2 respectively. In Raayan Mini board version 3.0, USART6 is internally connected to the BLUETOOTH / Wi-Fi module (ESP8266) provided on board. These STM32F4 microcontrollers, ADC, USART and Bluetooth/Wi-Fi combination can also be used as a platform for making many IoT projects.

**Figure 2-6. Raayan Mini STM32F4 Development Board USART schematics**

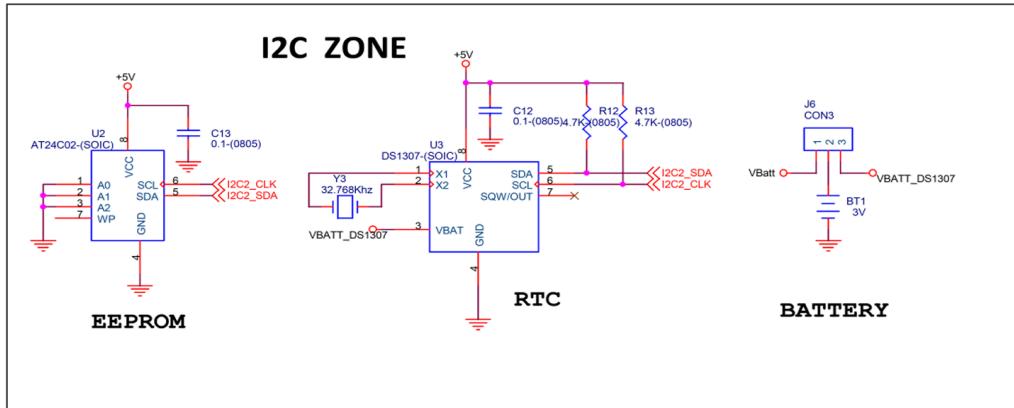
## USART ZONE



**Table 2-4. USART Pin configuration**

GPIO Pin	Pin Function	On-Board Function
PA9	GPIO	USART1 TX
PA10	GPIO	USART1 RX
PA2	GPIO	USART2 TX
PA3	GPIO	USART2 RX
PC6	GPIO	USART6 TX
PC7	GPIO	USART6 RX

## 2.2.7. I2C Zone



The Raayan Mini Development Board has extended one I2C bus for connecting slaves. I2C2 is the master and is already connected to two slaves on the board. One is EEPROM (slave address is 0X50H) and the second slave is RTC (slave address is 0X68H).

**Figure 2-7. Raayan Mini STM32F4 Development Board I2C schematics**

**Table 2-5. I2C Pin Configuration**

GPIO Pin	Pin Function	On-Board Function
PB6	GPIO	I2C1_CLK
PB7	GPIO	I2C1_SDA

## 2.2.8. SPI Zone

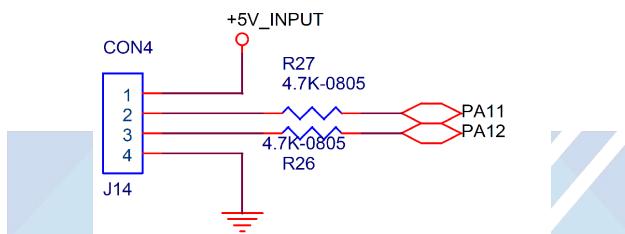
The SPI interface can be configured for communication in master mode and slave mode. The Raayan Mini Development board provides SPI1 as master which can be interfaced to another SPI device. The Table 2-5 shows the pin configuration for SPI.

**Table 2-6. SPI Pin Configuration**

GPIO Pin	Pin Function	On-Board Function
PA4	GPIO	SPI1_NSS
PA5	GPIO	SPI1_SCK
PA6	GPIO	SPI1_MISO
PA7	GPIO	SPI1_MOSI

## 2.2.9. Power supply

### POWER SUPPLY



The power supply is provided by the host PC through the USB cable, or by an external 5V power supply. 5V and 3V3 can be used as output power supplies when another application board is connected to the corresponding pins, power consumption must be lower than 100 mA.

**Figure 2-8. Raayan Mini STM32F4 Development Board Power supply schematics**

## 2.2.10. Oscillators

The Raayan Mini STM32F4 Development Board provides an external 32.768-KHz crystal (Y3) as the clock source for external RTC. Another 32.768-KHz crystal (Y2) serves as a clock for internal RTC. This board also uses an 8 MHz crystal (Y1) to complete the STM32F401RBT6 microcontroller main internal clock circuit.

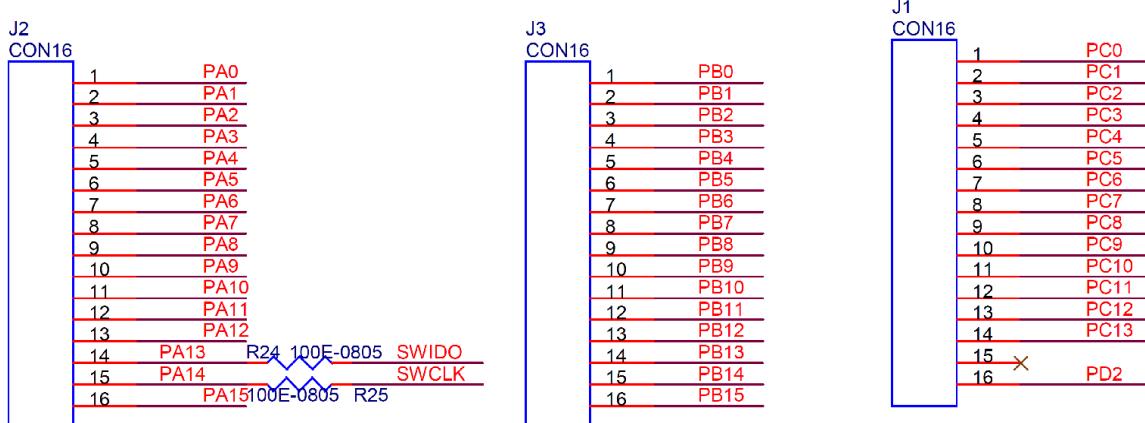
## 2.2.11. Electrically Erasable and Programmable Read-Only Memory (EEPROM)

The Raayan Mini STM32F4 Development Board provides an EEPROM of size 8K with 2-wire serial interface bus, I2C compatible. It is internally Organized 128 x 8 (1K), 256 x 8 (2K), 512 x 8 (4K), 1024 x 8 (8K) or 2048 x 8 (16K). It also has a write Protect Pin for Hardware Data Protection.

## 2.2.12. Real Time clock (RTC)

The Raayan Mini STM32F4 Development Board provides an external RTC. It is a low power, full binary-coded decimal (BCD) serial Real Time Clock. Address and data are transferred serially through an I2C, bidirectional bus. The clock/calendar provides seconds, minutes, hours, day, date, month, and year information. An external crystal (32.7 KHz) is also provided exclusively for RTC.

### 2.2.13. Expansion Headers



**Figure 2-9. Raayan Mini STM32F4 Development Board GPIO Header schematics**

The three rows of stackable headers are mapped to most of the GPIO pins of the STM32F401RBT6 microcontroller. These rows are labeled as connectors J1, J2, and J3. Each pin has to be configured in their respective GPIO Port Mode register as shown below as per your requirement of your project.

**Table 2-7. STM32F401 MuX/Pad Configuration**

GPIOIX_MODER		
0	0	GPIO - Input
0	1	GPIO - Output
1	0	Alternate Function
1	1	Analog mode

All 48 header pins represent the GPIO Port C, Port A and Port B respectively. These GPIO pins can be configured according to the functionality required by the users.

**Table 2-8. J1 Connector**

J1 Pin	STM32F4 MCU Pin	Port	GPIO	On-board Function	Analog Function	AF00 SYS_AF	AF01 TIM1/TIM2	AF02 TIM3/TIM4 /TIM5	AF03 TIM9/TIM 10/TIM11	AF04 I2C1/I2C2 /I2C3	AF05 SPI1/SPI2/I 2S2/SPI3/I2 S3/SPI4	AF06 SPI2/I2S2/S PI3/I2S3	AF07 SPI3/I2S3 /USART1 /USART2	AF08 I2C2/I2C 3	AF09 USART6	AF10 OTG1_FS	AF11 SDIO	AF12	AF13	AF14	AF15
1.01	8	Port C	PC0	-	ADC1_IN10	-	-	-	-	-	-	-	-	-	-	-	-	-	-	EVENT OUT	
1.02	9		PC1	-	ADC1_IN11	-	-	-	-	-	-	-	-	-	-	-	-	-	-	EVENT OUT	
1.03	10		PC2	-	ADC1_IN12	-	-	-	-	-	SPI2_MISO	I2S2ext_SD	-	-	-	-	-	-	-	EVENT OUT	
1.04	11		PC3	-	ADC1_IN13	-	-	-	-	-	SPI2_MOSI /I2S2_SD	-	-	-	-	-	-	-	-	EVENT OUT	
1.05	24		PC4	-	ADC1_IN14	-	-	-	-	-	-	-	-	-	-	-	-	-	-	EVENT OUT	
1.06	25		PC5	USER_LED_2	ADC1_IN15	-	-	-	-	-	-	-	-	-	-	-	-	-	-	EVENT OUT	
1.07	37		PC6	USART6_TX	-	-	-	TIM3_CH1	-	-	I2S2_MCK	-	-	USART6_TX	-	-	SDIO_D6	-	-	EVENT OUT	
1.08	38		PC7	USART6_RX	-	-	-	TIM3_CH2	-	-	-	I2S3_MCK	-	USART6_RX	-	-	SDIO_D7	-	-	EVENT OUT	
1.09	39		PC8	SW_UP	-	-	-	TIM3_CH3	-	-	-	-	-	USART6_CK	-	-	SDIO_D0	-	-	EVENT OUT	
1.1	40		PC9	SW_DN	-	MCO_2	-	TIM3_CH4	-	I2C3_SDA	I2S_CKIN	-	-	-	-	-	SDIO_D1	-	-	EVENT OUT	
1.11	51		PC10	-	-	-	-	-	-	-	SPI3_SCK/I 2S3_CK	-	-	-	-	-	SDIO_D2	-	-	EVENT OUT	
1.12	52		PC11	-	-	-	-	-	-	-	I2S3ext_SD	SPI3_MISO	-	-	-	-	SDIO_D3	-	-	EVENT OUT	
1.13	53		PC12	-	-	-	-	-	-	-	-	SPI3_MOSI /I2S3_SD	-	-	-	-	SDIO_CK	-	-	EVENT OUT	
1.14	2		PC13	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	EVENT OUT	
1.15			NC	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	
1.16	54	Port D	PD2	-	-	-	-	-	TIM3_ETR	-	-	-	-	-	-	-	SDIO_CMD	-	-	EVENT OUT	

**Table 2-9. J2 Connector**

J2 Pin	STM32F4 MCU Pin	Port	GPIO	On-board Function	Analog Function	AF00	AF01	AF02	AF03	AF04	AF05	AF06	AF07	AF08	AF09	AF10	AF11	AF12	AF13	AF14	AF15
2.01	14	Port A	PA0	SW_ENT	ADC1_IN0	-	TIM2_CH1/ TIM2_ETR	TIM5_CH1	-	-	-	USART2_ CTS	-	-	-	-	-	-	-	-	EVENT OUT
2.02	15		PA1	-	ADC1_IN1	-	TIM2_CH2	TIM5_CH2	-	-	-	USART2_ RTS	-	-	-	-	-	-	-	-	EVENT OUT
2.03	16		PA2	USART2_TX	ADC1_IN2	-	TIM2_CH3	TIM5_CH3	TIM9_CH1	-	-	USART2_ TX	-	-	-	-	-	-	-	-	EVENT OUT
2.04	17		PA3	USART2_RX	ADC1_IN3	-	TIM2_CH4	TIM5_CH4	TIM9_CH2	-	-	USART2_ RX	-	-	-	-	-	-	-	-	EVENT OUT
2.05	20		PA4	SPI1_NSS	ADC1_IN4	-	-	-	-	-	SPI1_NSS	SPI3_NSS/ I2S3_WS	USART2_ CK	-	-	-	-	-	-	-	EVENT OUT
2.06	21		PA5	SPI1_SCK	ADC1_IN5	-	TIM2_CH1/ TIM2_ETR	-	-	-	SPI1_SCK	-	-	-	-	-	-	-	-	-	EVENT OUT
2.07	22		PA6	SPI1_MISO	ADC1_IN6	-	TIM1_BKIN	TIM3_CH1	-	-	SPI1_MISO	-	-	-	-	-	-	-	-	-	EVENT OUT
2.08	23		PA7	SPI1_MOSI	ADC1_IN7	-	TIM1_CH1_N	TIM3_CH2	-	-	SPI1_MOSI	-	-	-	-	-	-	-	-	-	EVENT OUT
2.09	41		PA8	USER_LED1	-	MCO_1	TIM1_CH1	-	-	I2C3_SCL	-	-	USART1_ CK	-	-	OTG_FS_ SOF	-	-	-	-	EVENT OUT
2.10	42		PA9	USART1_TX	-	-	TIM1_CH2	-	-	I2C3_SM_BA	-	-	USART1_ TX	-	-	OTG_FS_ VBUS	-	-	-	-	EVENT OUT
2.11	43		PA10	USART1_RX	-	-	TIM1_CH3	-	-	-	-	-	USART1_ RX	-	-	OTG_FS_ ID	-	-	-	-	EVENT OUT
2.12	44		PA11	-	-	-	TIM1_CH4	-	-	-	-	-	USART1_ CTS	USART6_ TX	-	OTG_FS_ DM	-	-	-	-	EVENT OUT
2.13	45		PA12	-	-	-	TIM1_ETR	-	-	-	-	-	USART1_ RTS	USART6_ RX	-	OTG_FS_ DP	-	-	-	-	EVENT OUT
2.14	46		PA13	-	-	JTMS_S_WDIO	-	-	-	-	-	-	-	-	-	-	-	-	-	-	EVENT OUT
2.15	49		PA14	-	-	JTCK_S_WCLK	-	-	-	-	-	-	-	-	-	-	-	-	-	-	EVENT OUT
2.16	50		PA15	-	-	JTDI	TIM2_CH1/ TIM2_ETR	-	-	-	SPI1_NSS	SPI3_NSS/ I2S3_WS	-	-	-	-	-	-	-	-	EVENT OUT

Table 2-10. J3 Connector

J3 Pin	STM32F4 MCU Pin	Port	GPIO	On-board Function	Analog Function	AF00	AF01	AF02	AF03	AF04	AF05	AF06	AF07	AF08	AF09	AF10	AF11	AF12	AF13	AF14	AF15
3.01	26	Port B	PB0	LCD_DATA_4	ADC1_IN8	-	TIM1_CH2N	TIM3_CH3	-	-	-	-	-	-	-	-	-	-	-	-	EVENT OUT
3.02	27		PB1	LCD_DATA_5	ADC1_IN9	-	TIM1_CH3N	TIM3_CH4	-	-	-	-	-	-	-	-	-	-	-	-	EVENT OUT
3.03	28		PB2	LCD_DATA_6	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	EVENT OUT
3.04	55		PB3	LCD_DATA_7	-	JTDO-SWO	TIM2_CH2	-	-	-	SPI1_SCK	SPI3_SCK/I2S3_CK	-	-	I2C2_SDA	-	-	-	-	-	EVENT OUT
3.05	56		PB4	LCD_RS	-	JTRST	-	TIM3_CH1	-	-	SPI1_MISO	SPI3_MISO	I2S3ext_SD	-	I2C3_SDA	-	-	-	-	-	EVENT OUT
3.06	57		PB5	LCD_RW	-	-	-	TIM3_CH2	-	I2C1_SMBA	SPI1_MOSI	SPI3_MOSI/I2S3_SD	-	-	-	-	-	-	-	-	EVENT OUT
3.07	58		PB6	I2C1_SCL	-	-	-	TIM4_CH1	-	I2C1_SCL	-	-	USART1_TX	-	-	-	-	-	-	-	EVENT OUT
3.08	59		PB7	I2C1_SDA	-	-	-	TIM4_CH2	-	I2C1_SDA	-	-	USART1_RX	-	-	-	-	-	-	-	EVENT OUT
3.09	61		PB8	LCD_EN	-	-	-	TIM4_CH3/TIM10_CH1	I2C1_SCL	SPI1_NSS/I2S2_WS	-	-	-	-	-	-	SDIO_D4	-	-	-	EVENT OUT
3.10	62		PB9	-	-	-	-	TIM4_CH4/TIM11_CH1	I2C1_SDA	SPI1_SCK/I2S2_CK	-	-	-	-	-	-	SDIO_D5	-	-	-	EVENT OUT
3.11	29		PB10	-	-	-	-	TIM2_CH3	-	I2C2_SCL	-	-	-	-	-	-	-	-	-	-	EVENT OUT
3.12	30		PB11	-	-	-	-	TIM2_CH4	-	I2C2_SDA	-	-	-	-	-	-	-	-	-	-	EVENT OUT
3.13	33		PB12	BUZZER	-	-	-	TIM1_BKIN	-	I2C2_SMBA	SPI2_NSS/I2S2_WS	-	-	-	-	-	-	-	-	-	EVENT OUT
3.14	34		PB13	-	-	-	-	TIM1_CH1N	-	-	SPI2_SCK/I2S2_CK	-	-	-	-	-	-	-	-	-	EVENT OUT
3.15	35		PB14	-	-	-	-	TIM1_CH2N	-	-	SPI2_MISO	I2S2ext_SD	-	-	-	-	-	-	-	-	EVENT OUT
3.16	36		PB15	-	-	RTC_REFN	TIM1_CH3N	-	-	-	SPI2_MOSI/I2S2_SD	-	-	-	-	-	-	-	-	-	EVENT OUT



## 3. Software Development

This chapter provides general information on software development as well as instructions for Flash memory programming.

### 3.1. Software Description

### 3.2. Source Code

<https://github.com/raayanmini>

### 3.3. Tool Options

The following tools can be used for developing and flashing purposes.

1. MDK-ARM (Keil) Microcontroller Development tool.
2. STM32CUBEMX Initialization code generator.
3. STM32CubeIDE (Embedded Integrated Development Environment)

Download evaluation versions of these tools from their respective websites. Instructions on installing and using each of the evaluation tools can be found in the Quick start guides which are available for download with the tools from their respective websites.

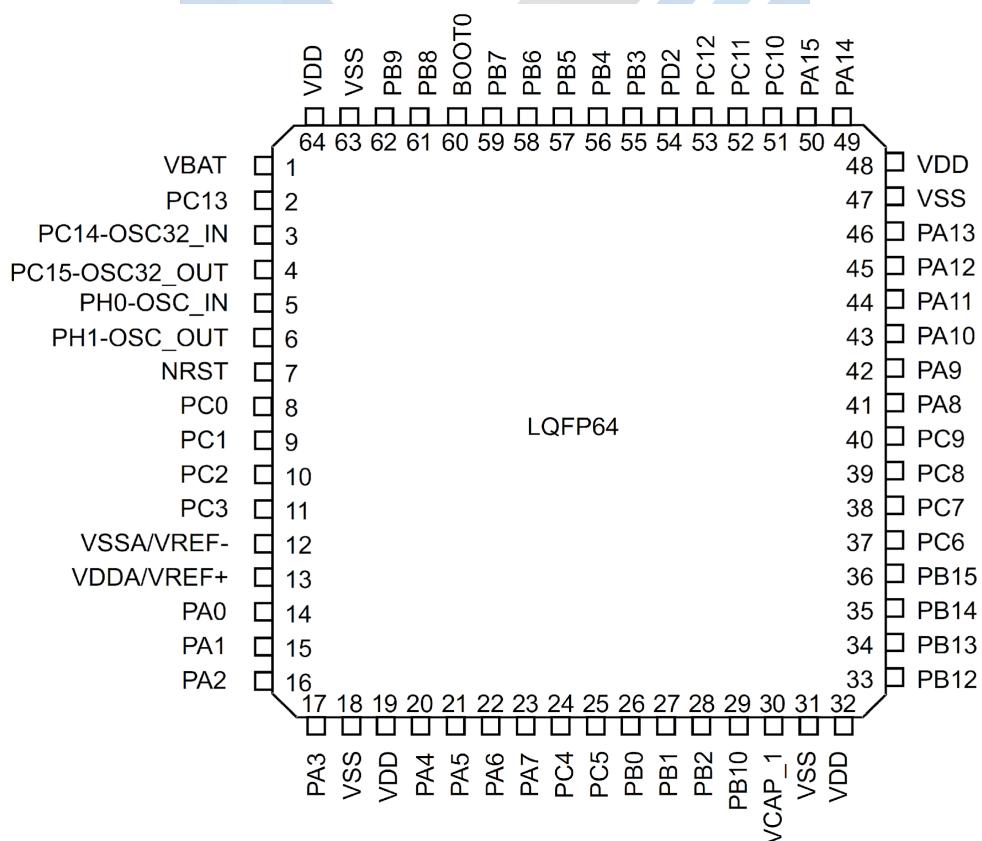
## 4. References, PCB Layout, and Bill of Materials

## 4.1. References

- MDK-ARM ([Keil MDK](#))
  - STM32CubeMX([www.st.com/en/development-tools/stm32cubemx.html](http://www.st.com/en/development-tools/stm32cubemx.html))
  - STM32CubeIDE([STM32CubeIDE | Software - STMicroelectronics](#))

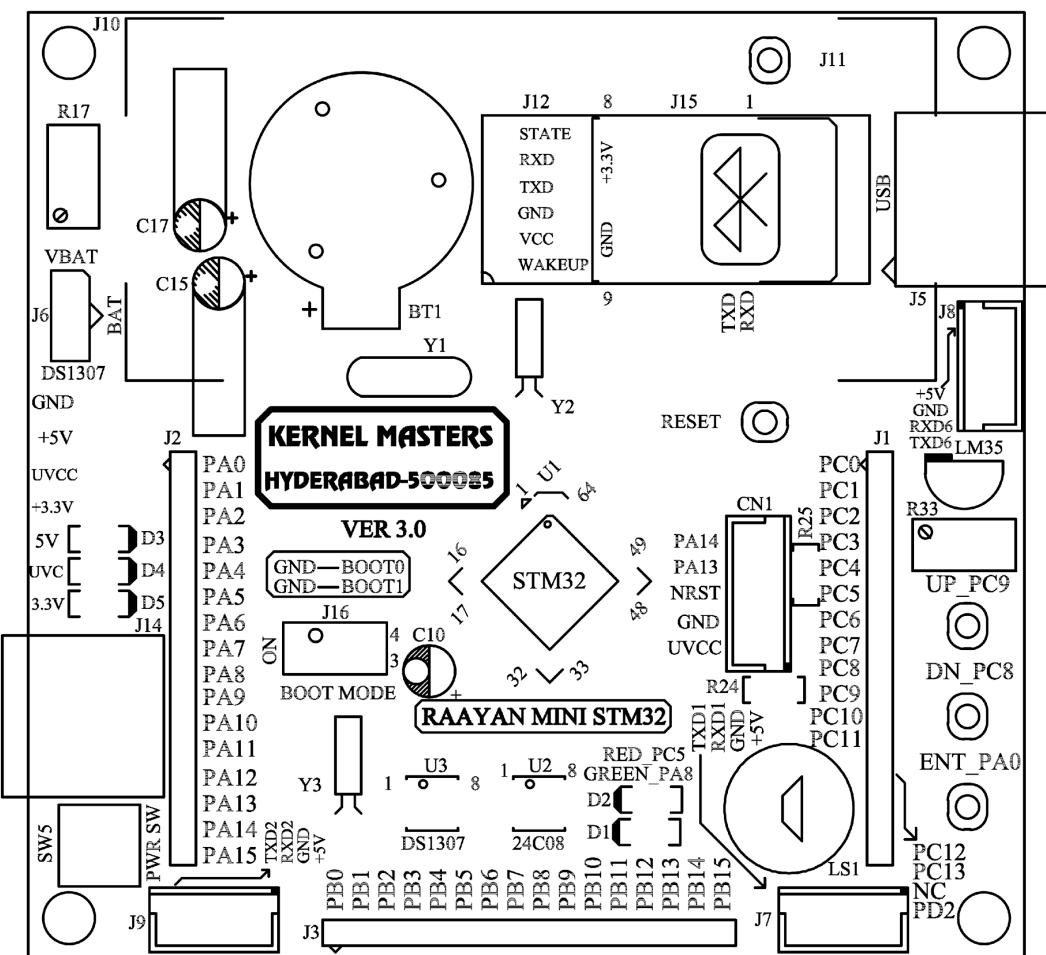
## 4.2. Pin out Diagram

**Figure 4-1. STM32F401RBT6 LQFP64 pin out**

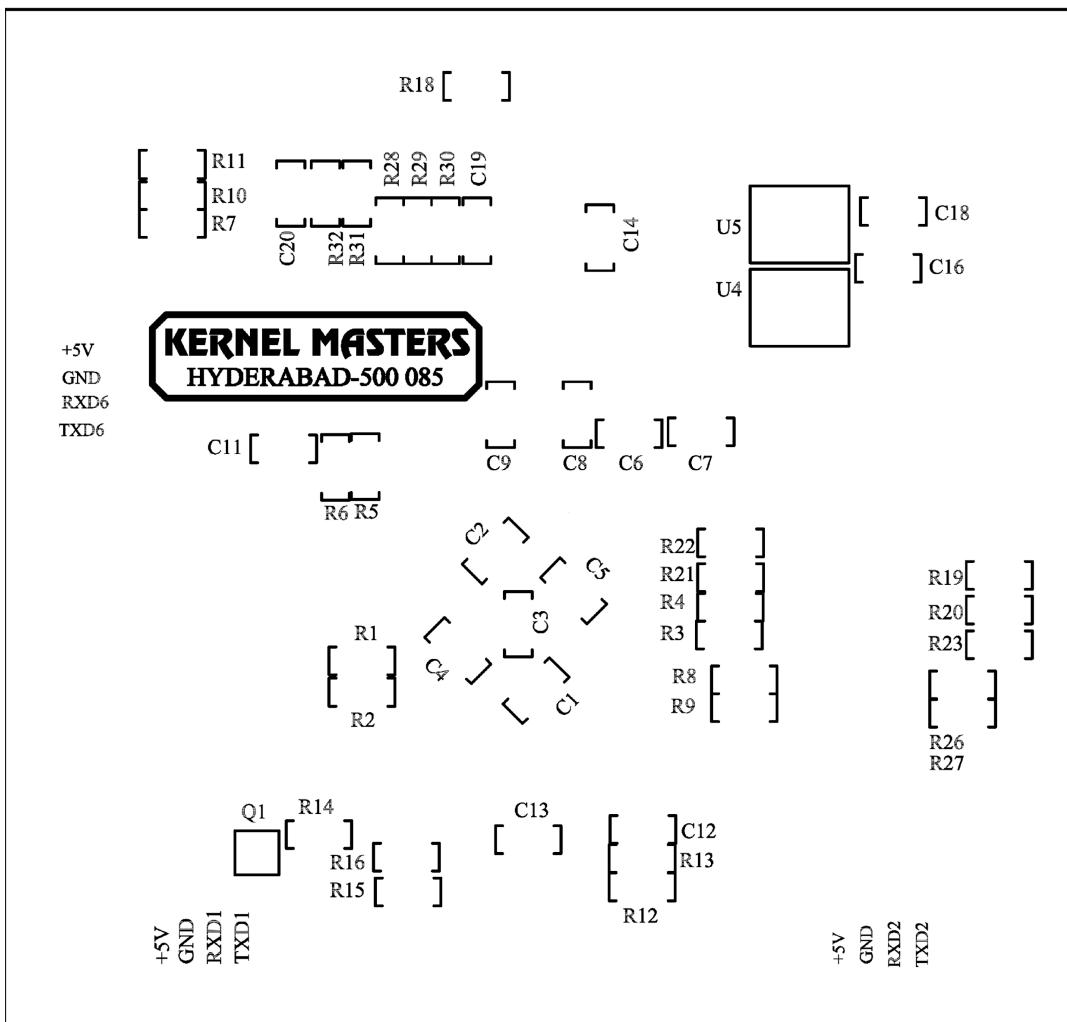


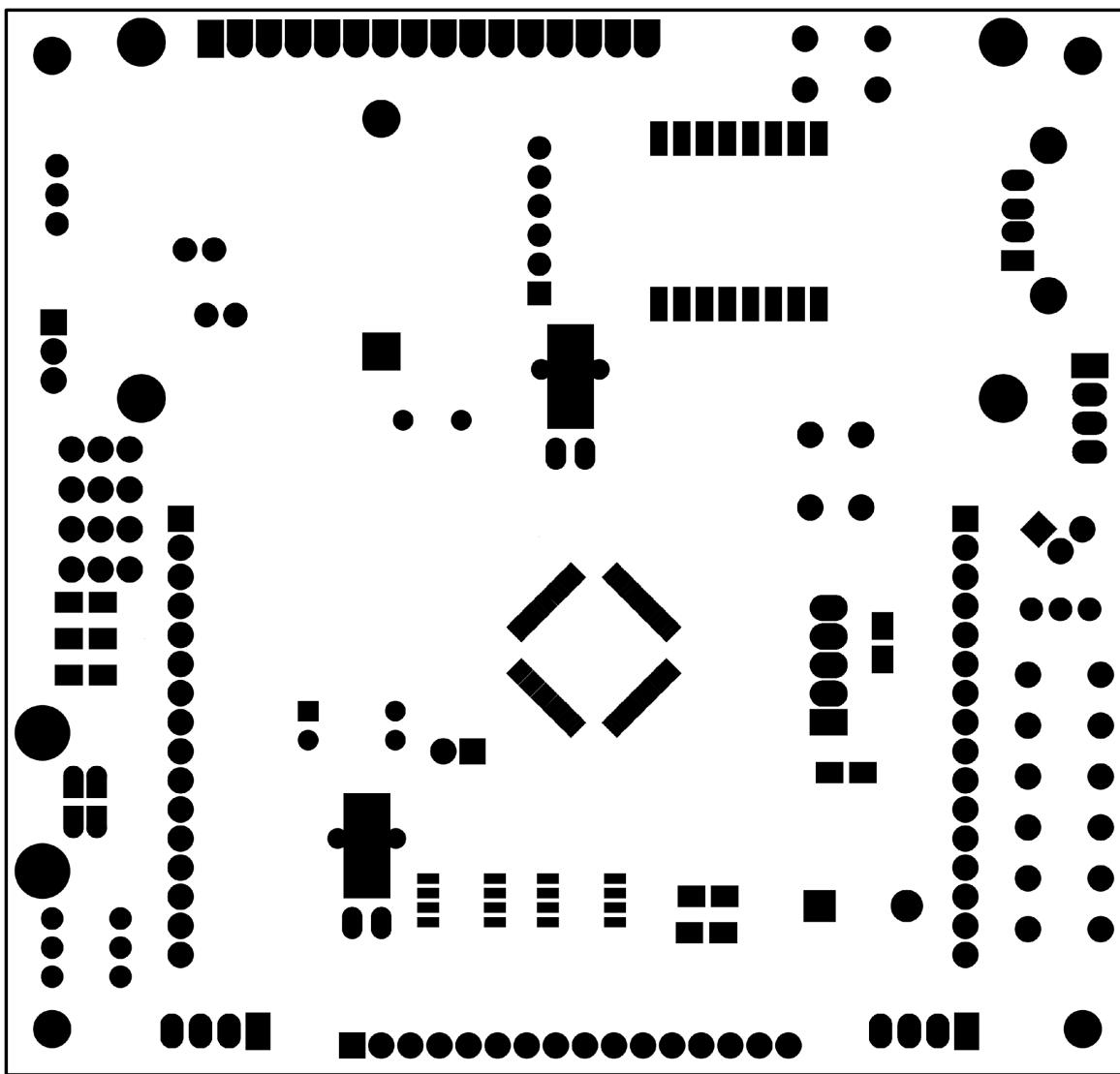
### 4.3. Component Locations

**Figure 4-2. Raayan Mini STM32F4 Development Board Component Locations (Top View)**



**Figure 4-3. Raayan Mini STM32F4 Development Board Component Locations (Bottom View)**



**Figure 4-4. Raayan Mini STM32F4 Development Board**

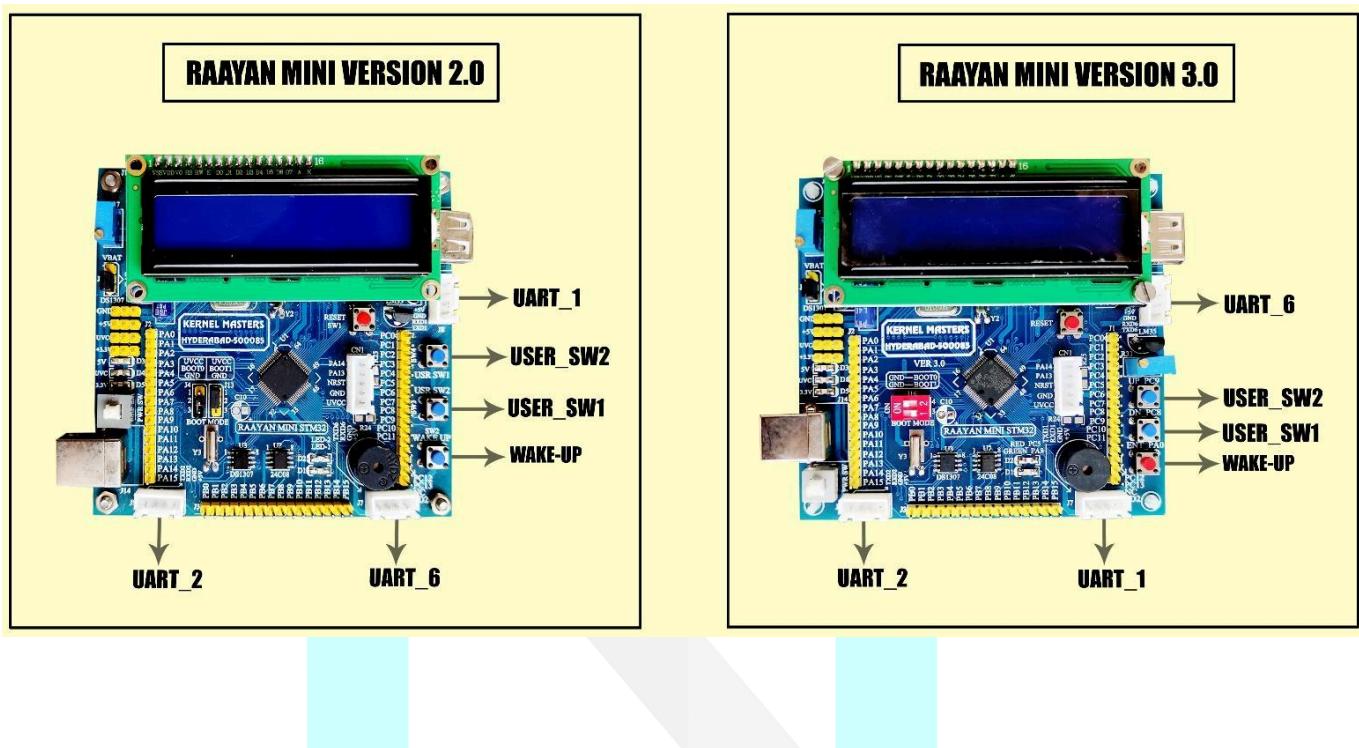
#### 4.4 Bill of Materials (BOM)

Table 4-1 shows the bill of materials for the Raayan Mini STM32F4 Development board.



## Appendix A

Raayan Mini board version 2.0 vs version 3.0



## Appendix B Schematics

This section contains the complete schematics for the Raayan Mini STM32F4 Development board.



