

Course Code: RME 2202

Course Name: Microcontroller and Programmable Logic Controller Lab

Experiment No: 03

Experiment Name: Implementing multi-key safety lock for bank lockers using

PLC.

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Objective:

- To learn the basic principle of the PLC
- To learn the applications of the PLC
- To get introduced with the PLC programming using LOGO! Soft Comfort V8.2

Theory:

A programmable logic controller (PLC) or programmable controller is an industrial digital computer which has been ruggedized and adapted for the control of manufacturing processes, such as assembly lines, or robotic devices, or any activity that requires high reliability control and ease of programming and process fault diagnosis. They were first developed in the automobile manufacturing industry to provide flexible, ruggedized and easily programmable controllers to replace hard-wired relays, timers and sequencers. The most basic function of a programmable controller is to emulate the functions of electro-mechanical relays. Discrete inputs are given a unique address, and a PLC instruction can test if the input state is on or off. Just as a series of relay contacts perform a logical AND function and a parallel set of instructions will perform a logical OR, not allowing current to pass unless all the contacts are closed.

IEC 61131-3 standard currently defines five programming languages for programmable control systems: function block diagram (FBD), ladder diagram (LD), structured text (ST; similar to the Pascal programming language), instruction list (IL; similar to assembly language), and sequential function chart (SFC). These techniques emphasize logical organization of operations. The most commonly used programming language is Ladder diagram (LD) also known as Ladder). It uses Contact-Coil logic to make programs like an electrical control diagram.

The main difference from most other computing devices is that PLCs are intended-for and therefore tolerant-of more severe conditions (such as dust, moisture, heat, cold), while offering extensive input/output (I/O) to connect the PLC to sensors and actuators. PLC input can include simple digital elements such as limit switches, analog variables from process sensors (such as temperature and pressure), and more complex data such as that from positioning or machine vision systems.

In PLC discrete (digital) signals behave as binary switches, yielding simply an On or Off signal (1 or 0, True or False, respectively). Push buttons, limit switches, and photoelectric sensors are examples of devices providing a discrete signal.

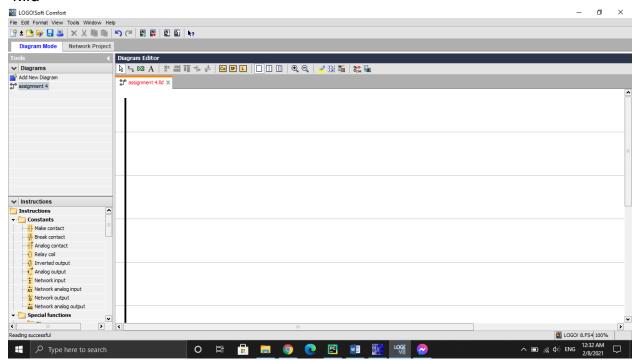
Discrete signals are sent using either voltage or current, where a specific range is designated as On and another as Off. For example, the PLC used in the experiment use 24 V DC I/O, with values above 22 V DC representing On, values below 22V DC representing Off, and intermediate values undefined. Initially, PLCs had only digital I/O.

Equipment:

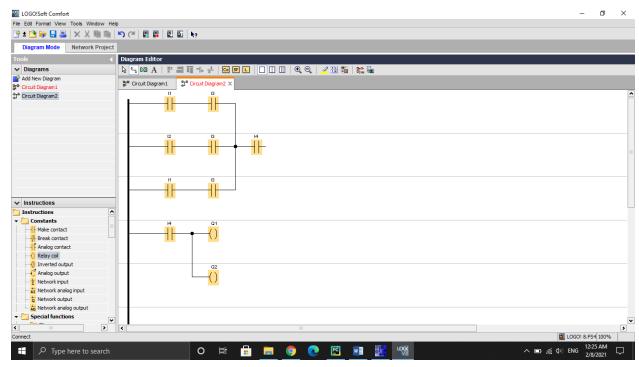
- Computer
- LOGO! Soft Comfort V8.2

Procedure:

- At first I opened the LOGO! Soft Comfort V8.2 software.
- Then I opened new file in ladder diagram format naming it 'assignment 4.lld'



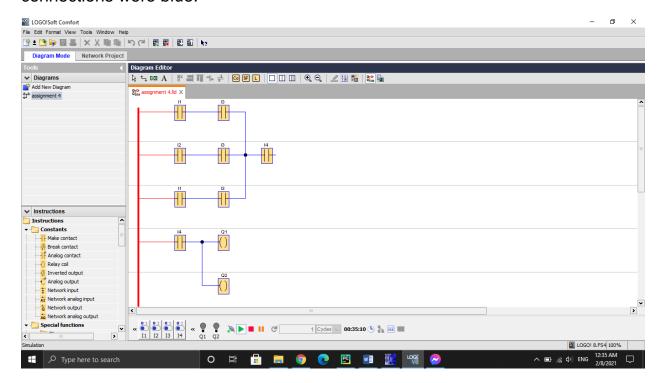
 Then I took all the input contacts and outputs (relay coil) and connected them as the given circuit and completed the ladder diagram.



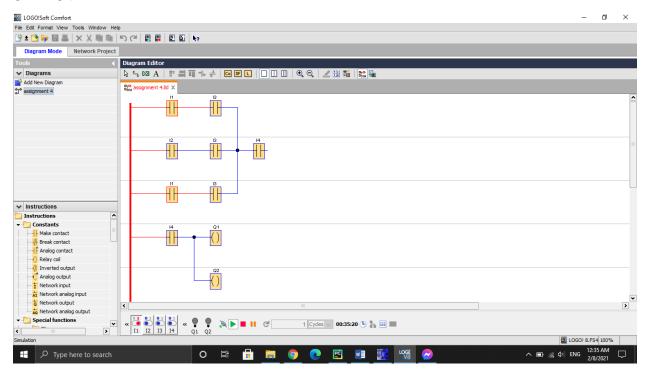
• Finally I simulated the ladder.

Result:

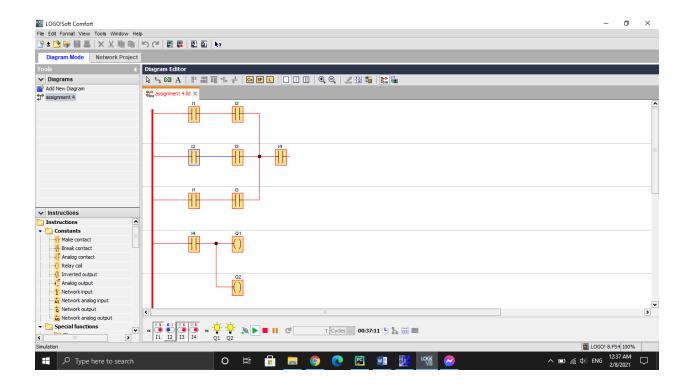
During the simulation when no input was on it didn't show any result and all the connections were blue.



When I turn on only one input (I1) the result was still the same. No output was getting power.



But when I turned on any two of the three inputs (I1 and I3) then output I4 was getting power and it was turned on. And it consequently turned on the two coils (Q1 and Q2). That means all the outputs were getting power.



However, I got the expected result that is when any two inputs out of the three inputs is turned on then output coils will be turned on, otherwise it won't give any output.

Discussion & conclusion:

PLC can be used to control different peripheral devices. We simulated the ladder circuit and we saw the output matches with the implemented logic. So we can consider the experiment successful.