Inputs			Outputs	
X	Y	C_in	S	C_out
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1

Therefore, we have

- $S = \overline{X} \cdot \overline{Y} \cdot C_{-in} + \overline{X} \cdot Y \cdot \overline{C_{in}} + X \cdot \overline{Y} \cdot \overline{C_{in}} + X \cdot Y \cdot C_{in}$ = $X \times xor Y \times xor C_{in}$ (one possible implementation using an xor gate)
- $C_{out} = \overline{X} \cdot Y \cdot C_{in} + X \cdot \overline{Y} \cdot C_{in} + X \cdot Y \cdot \overline{C_{in}} + X \cdot Y \cdot C_{in}$ = $X \cdot Y + X \cdot C_{in} + Y \cdot C_{in}$ (after minimization)



