Specification for HTPA32x31L17/0.8HiM(UDP)

Rev.1: 2013.09.23 Fg

The HTPA32x31L_/_M(UDP) is a fully calibrated, low cost thermopile array module, with fully digital UDP interface. The module delivers an electrical offset and ambient temperature compensated output stream, which can be already used for image processing, pattern recognition and presence detection purposes. Object temperatures can be easily obtained by this data stream.

Order Code Example

HTPA32x31L17/0.8HiM(UDP)[Si]

Lens material: Si→Silicon, if not declared Germanium

Interface: SPI→ SPI device (14bit ADC)
              LC→SPI, 12bit ADC, low speed, external processing required
              UDP→Ethernet, CAT5 cable connection
              UART→RS232-like, Level: 3.3V

Type: A→Application set: comes with GUI, housing, power supply
       M→Module: HTPA sensor soldered to PCB, calibrated stream
       S→Sensor: HTPA sensor only. Analogous output.

Sensitivity: Hi→Increased sensitivity
              Without "Hi"→ Standard sensitivity

Optics: L→focal length: In example L17 = 17 mm focal length.
        /→ F-Number: In example /0.8
        For optics see also "HTPA standard optics"

Type: HTPA32x31 (Please contact support for all available HTPA and module combinations.)

For modules, M(UART) and M(LC) are not recommended anymore. M(SPI) and M(UDP) offer a wider input voltage range, better ADC resolution and a wider measurement range.

Pinout

| Pin Assignment HTPA32x31M(UDP)			
Pin	Name	Description	Type
1	TPOut+	Differential Signal Output	Digital Output
2	VDD	Positive supply voltage	Power
3	TPOut-	Differential Signal Output	Digital Output
4	TPIn+	Differential Signal Input	Digital Input
5		not connected	
6	TPIn-	Differential Signal Input	Digital Input
7		not connected	
8	VSS	Ground reference	Power
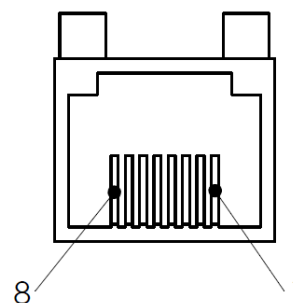
Ethernet-Interface:

Protocol Specifications:

Protocol type: UDP
All communication on Port: 30444

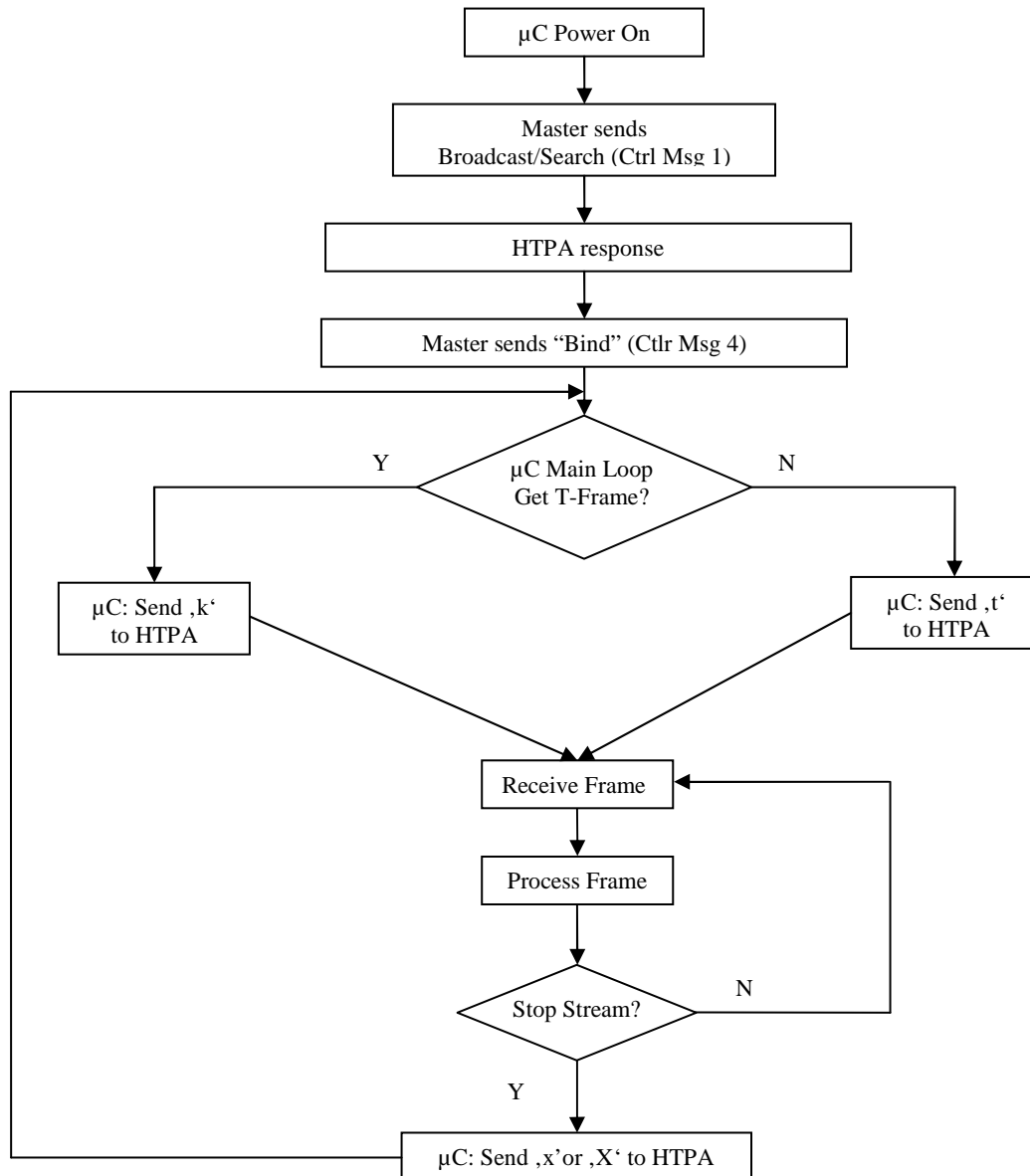**Power connection at Ethernet device:**
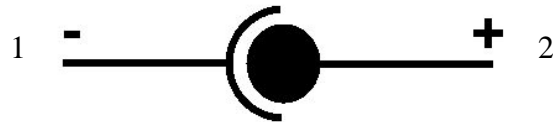
| 1 | VSS | (-) | GND |
| 2 | VDD | (+) | Supply (+3.3V DC) |

Power Supply: **3.3** VDC +/- 5%, 300mA

HTPA32x31L17/0.8M(UDP) Optical Orientation of Pixels:

First Row

Last Row
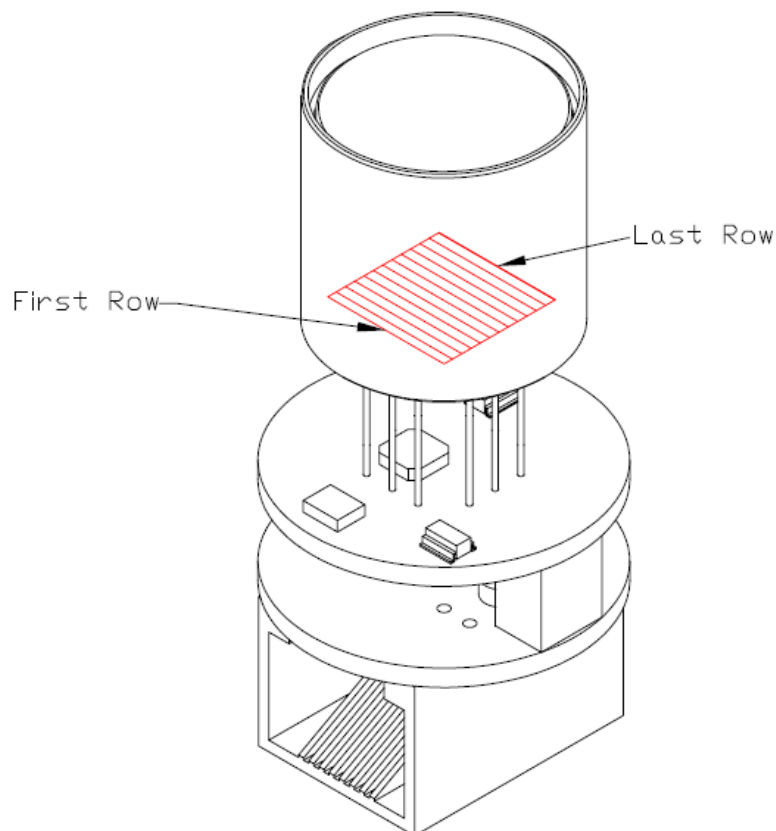
Communication and Timings:

Proposed flow chart of communication. (Master is referred as µC, Slave as HTPA module)

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Communication:

Sent Char	HTPA8x8	HTPA16x16	HTPA32x31 HTPA64x62	Result/Received message
				Communication via Terminal / UDP
'a'	X	X	X	Decreases the operating frequency of the array
'A'	X	X	X	Increases the operating frequency of the array
'b'	X	X	X	Measure VDD (referenced to VREF1225)
'C'	X	X		Capture single voltage frame. Use ADC of ASIC. Output via ASCII if sent via UART, binary if sent via UDP.
'c'	X	X	X	Capture single voltage frame. Use ADC of µC. Output via ASCII if sent via UART, binary if sent via UDP.
'd'/'D'	X	X		Toggle POR_N
'f'	X	X	X	Toggle Resetbit
'F'	X	X		Analog operating point is at start of AD-range, only positive signals convertable
'G'	X	X		Analog operating point is in the middle of AD-range, positive and negative signals convertable
'g'	X	X		Analog operating point is at end of AD-range, only negative signals convertable
'h'	X	X	X	pushes binary EEDATA out
'i'			X	Read single voltage frame. Output in ASCII format. Serial order: Pixeldata[K*10], el. Offsets, Ambient Temperature
'I'			X	Read single temperature frame. Output in ASCII format. Serial order: Pixeldata[K*10], el. Offsets, Ambient Temperature
'J'	X	X	X	Toggle Amplification
'k'	X	X	X	Read single temperature frame. Output in binary format.
'K'	X	X	X	send continous binary temperature datastream(µC-ADC)[K*10] Output of a complete cycle in this order: *HTPA 8x8 and HTPA16x16: Pixel0,Pixel1, ...PixelX, el.Offset0, el.Offset1,..., el.OffsetY,PTAT0,PTAT1,...,PTATZ* *HTPA32x31: see Table2.* ***For a detailed Description of the serial order see Table2.*** 16x16 Array: 8x8 Array: X=255; Y=7; Z=7 X=63; Y=4; Z=4 One dataset has exactly 2 bytes: first the low-Byte is send, then the high-byte. Each Dataset contains the measured Temperature in Kelvin*10. The first 4 datasets *el.Offset0...el.Offset3* after the last Pixel voltage *PixelX* transmit additional the current VDD in the MSB's: **VDD and TAmb for HTPA8x8 and HTPA16x16:** <table><tr><td>Dataset</td><td>Bit15</td><td>Bit14</td><td>Bit13</td><td>Bit12</td><td>Bit11</td><td>Bit10</td><td>...</td><td>Bit1</td><td>Bit0</td></tr><tr><td>elOff0</td><td>MSB VDD</td><td>...</td><td>...</td><td>Bit12 VDD</td><td>MSB elOff0</td><td>...</td><td>...</td><td>...</td><td>LSB elOff0</td></tr><tr><td>elOff1</td><td>Bit11 VDD</td><td>...</td><td>...</td><td>Bit8 VDD</td><td>MSB elOff1</td><td>...</td><td>...</td><td>...</td><td>LSB elOff1</td></tr><tr><td>elOff2</td><td>Bit7 VDD</td><td>...</td><td>...</td><td>Bit4 VDD</td><td>MSB elOff2</td><td>...</td><td>...</td><td>...</td><td>LSB elOff2</td></tr><tr><td>elOff3</td><td>Bit3 VDD</td><td>...</td><td>...</td><td>LSB VDD</td><td>MSB elOff3</td><td>...</td><td>...</td><td>...</td><td>LSB elOff3</td></tr></table> The Sensor temperature is available in the datasets after *el.Offset3* : <table><tr><td>Dataset</td><td>Bit15</td><td>Bit14</td><td>Bit13</td><td>Bit12</td><td>Bit11</td><td>Bit10</td><td>...</td><td>Bit1</td><td>Bit0</td></tr><tr><td>elOff3+1</td><td>MSB TAmb</td><td>...</td><td>...</td><td>Bit12 TAmb</td><td>MSB elOff3+1</td><td>...</td><td>...</td><td>...</td><td>LSB elOff3+1</td></tr><tr><td>elOff3+2</td><td>Bit11 TAmb</td><td>...</td><td>...</td><td>Bit8 TAmb</td><td>MSB elOff3+2</td><td>...</td><td>...</td><td>...</td><td>LSB elOff3+2</td></tr><tr><td>elOff3+3</td><td>Bit7 TAmb</td><td>...</td><td>...</td><td>Bit4 TAmb</td><td>MSB elOff3+3</td><td>...</td><td>...</td><td>...</td><td>LSB elOff3+3</td></tr><tr><td>elOff3+4</td><td>Bit3 TAmb</td><td>...</td><td>...</td><td>LSB TAmb</td><td>MSB elOff3+4</td><td>...</td><td>...</td><td>...</td><td>LSB elOff3+4</td></tr><tr><td>elOff3+5</td><td>0</td><td>0</td><td>...</td><td>0</td><td>MSB elOff3+5</td><td>...</td><td>...</td><td>...</td><td>LSB elOff3+5</td></tr></table>
'l'	X	X	X	Get Ambient Temperature (Calculates the Ambient Temperature from the **last measured** Frame)
'm'	X	X	X	Toggle usage of µC-Buffer for el. Offsets (Stack depth = 64 for HTPA8x8 and HTPA16x16; Stack depth = 32 for HTPA32x31)
'M'	X	X	X	Shows current and calibration settings. Device prints the following stream: **"HTPA series responsed! I am Arraytype X"** Possible values for X: "0"=HTPA8x8, "1"=HTPA16x16, "3"=HTPA32x31 **"Firmware v.X.XX written by B.Forg; Heimann Sensor GmbH; YYYY-MM-DD"** Version information. **"I am running on XXXX.X kHz"** Actual MCLK-setting in kHz **"Amplification is X" Actual set amplification.** Possible strings for X: "low" or "high" **"MAC-ID: X IP: Y DevID: Z\r\n"** (Only Ethernet devices show a MAC-ID, DevID is shown in any case) X= MAC-ID of the device, i.e. "00.97.FF.00.10.08"; Y=current IP of the device, Z=user setable ID, range 00000…65535 **"PIXCvsTA *X* , BFL3 *X* , F8_14 *X* , THvsTA *X* IGNORE_ELOFF *X* ELOFF32 *X* SBY *Y* FC *X* EXP *Z* "**

Table 1a: Control Characters

Sent Char	HTPA8x8	HTPA16x16	HTPA32x31 HTPA64x62	Result/Received message
				Communication via Terminal / UDP
'o'		X	X	Use external reference voltages
'O'		X	X	Use internal reference voltages
'q'/'Q'	X	X	X	Allow Changes (required for Calibration)
't'	X	X	X	Continuous binary voltage data of the µC-ADC is transmitted. Output of a complete cycle in this order: *HTPA 8x8 and HTPA16x16: Pixel0,Pixel1, ….PixelX, el.Offset0, el.Offset1,…, el.OffsetY,PTAT0,PTAT1,…,PTATZ* ***HTPA32x31: see Table2.*** ***For a detailed Description of the serial order see Table2.*** 16x16 Array: X=255; Y=7; Z=7 8x8 Array: X=63; Y=4; Z=4 One dataset has exactly 2 bytes: first the low-Byte is send, then the high-byte. Each Dataset contains the ADC-Data in digits and The first 4 datasets *el.Offset0…el.Offset3* after the last Pixel voltage *PixelX* transmit additional the current VDD in the MSB's:
'T'	X	X		Continuous binary data of the ASIC-ADC is transmitted. Output order is equal to 't'.
'u'	X	X		Continuous binary data of the ASIC-ADC is transmitted. PTAT-Voltages are sampled with the uC-ADC. Output order is equal to 't'.
'U'	X	X		Capture single frame. Use ADC of ASIC. Output via ASCII. PTAT-Voltages are sampled with the uC-ADC.
'v'	X	X	X	Announce IP (Only Ethernet devices)
'V'	X	X	X	Device awaits control message (only non-Ethernet devices)
'w'	X	X	X	shows Calibration-constants
'W'	X	X	X	Calibration. ATTENTION! Old Dataset cannot be restored!
'x'	X	X	X	Stops Stream without prompt.
'X'	X	X	X	Stops Stream by sending "STOP!\r\n"
'y'	X	X	X	switch off ASIC-Supply (5V)
'Y'	X	X	X	switch on ASIC-Supply (5V)

VDD for HTPA8x8 and HTPA16x16:

Dataset	Bit15	Bit14	Bit13	Bit12	Bit11	Bit10	..	Bit1	Bit0
elOff0	MSB VDD	...	..	Bit12 VDD	MSB elOff0	...	...	...	LSB elOff0
elOff1	Bit11 VDD	...	..	Bit8 VDD	MSB elOff1	...	...	...	LSB elOff1
elOff2	Bit7 VDD	...	..	Bit4 VDD	MSB elOff2	...	...	...	LSB elOff2
elOff3	Bit3 VDD	...	..	LSB VDD	MSB elOff3	...	...	...	LSB elOff3

Table 1b: Control Characters (continuation)

Please be aware, that the source and destination port has to be 30444

Serial order of data in stream:

HTPA32x31 Temperature Mode	
Dataset	Value
0	Temperature of Pixel0 in K*10
1	Temperature of Pixel16 in K*10
2	Temperature of Pixel1 in K*10
3	Temperature of Pixel17 in K*10
…	…
30	Temperature of Pixel15 in K*10
31	Temperature of Pixel31 in K*10
32	Temperature of Pixel32 in K*10
33	Temperature of Pixel48 in K*10
…	…
991	Temperature of Pixel991 in K*10
992	elOff0 in digits
993	elOff16 in digits
994	elOff1 in digits
995	elOff17 in digits
…	…
1022	elOff15 in digits
1023	elOff31 in digits
1024	least significant 12 bits of VDD
1025	most significant 4 bits of VDD
1026	least significant 12 bits of TAmb
1027	most significant 4 bits of TAmb
1028	no value, ignore
1029	no value, ignore
…	…
1039	no value, ignore
1040	PTAT0 in digits
1041	no value, ignore
1042	PTAT1 in digits
…	…
1053	no value, ignore
1054	PTAT7 in digits
1055	no value, ignore

HTPA32x31 Voltage Mode	
Dataset	Value
0	absolute Voltage of Pixel0 in digits
1	absolute Voltage of Pixel16 in digits
2	absolute Voltage of Pixel1 in digits
3	absolute Voltage of Pixel17 in digits
…	…
30	absolute Voltage of Pixel15 in digits
31	absolute Voltage of Pixel31 in digits
32	absolute Voltage of Pixel32 in digits
33	absolute Voltage of Pixel48 in digits
…	…
991	absolute Voltage of Pixel991 in digits
992	elOff0 in digits
993	elOff16 in digits
994	elOff1 in digits
995	elOff17 in digits
…	…
1022	elOff15 in digits
1023	elOff31 in digits
1024	least significant 12 bits of VDD
1025	most significant 4 bits of VDD
1026	no value, ignore
1027	no value, ignore
1028	no value, ignore
1029	no value, ignore
…	…
1039	no value, ignore
1040	PTAT0 in digits
1041	no value, ignore
1042	PTAT1 in digits
…	…
1053	no value, ignore
1054	PTAT7 in digits
1055	no value, ignore

Table 2: Serial order of data in stream

Each dataset consists of a 16 bit value. If a frame consists out of more than one packet, packets are appended.

Pixel Map:

0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95
96	97	98	99	100	101	102	103	104	105	106	107	108	109	110	111	112	113	114	115	116	117	118	119	120	121	122	123	124	125	126	127
128	129	130	131	132	133	134	135	136	137	138	139	140	141	142	143	144	145	146	147	148	149	150	151	152	153	154	155	156	157	158	159
160	161	162	163	164	165	166	167	168	169	170	171	172	173	174	175	176	177	178	179	180	181	182	183	184	185	186	187	188	189	190	191
192	193	194	195	196	197	198	199	200	201	202	203	204	205	206	207	208	209	210	211	212	213	214	215	216	217	218	219	220	221	222	223
224	225	226	227	228	229	230	231	232	233	234	235	236	237	238	239	240	241	242	243	244	245	246	247	248	249	250	251	252	253	254	255
256	257	258	259	260	261	262	263	264	265	266	267	268	269	270	271	272	273	274	275	276	277	278	279	280	281	282	283	284	285	286	287
288	289	290	291	292	293	294	295	296	297	298	299	300	301	302	303	304	305	306	307	308	309	310	311	312	313	314	315	316	317	318	319
320	321	322	323	324	325	326	327	328	329	330	331	332	333	334	335	336	337	338	339	340	341	342	343	344	345	346	347	348	349	350	351
352	353	354	355	356	357	358	359	360	361	362	363	364	365	366	367	368	369	370	371	372	373	374	375	376	377	378	379	380	381	382	383
384	385	386	387	388	389	390	391	392	393	394	395	396	397	398	399	400	401	402	403	404	405	406	407	408	409	410	411	412	413	414	415
416	417	418	419	420	421	422	423	424	425	426	427	428	429	430	431	432	433	434	435	436	437	438	439	440	441	442	443	444	445	446	447
448	449	450	451	452	453	454	455	456	457	458	459	460	461	462	463	464	465	466	467	468	469	470	471	472	473	474	475	476	477	478	479
480	481	482	483	484	485	486	487	488	489	490	491	492	493	494	495	496	497	498	499	500	501	502	503	504	505	506	507	508	509	510	511
512	513	514	515	516	517	518	519	520	521	522	523	524	525	526	527	528	529	530	531	532	533	534	535	536	537	538	539	540	541	542	543
544	545	546	547	548	549	550	551	552	553	554	555	556	557	558	559	560	561	562	563	564	565	566	567	568	569	570	571	572	573	574	575
576	577	578	579	580	581	582	583	584	585	586	587	588	589	590	591	592	593	594	595	596	597	598	599	600	601	602	603	604	605	606	607
608	609	610	611	612	613	614	615	616	617	618	619	620	621	622	623	624	625	626	627	628	629	630	631	632	633	634	635	636	637	638	639
640	641	642	643	644	645	646	647	648	649	650	651	652	653	654	655	656	657	658	659	660	661	662	663	664	665	666	667	668	669	670	671
672	673	674	675	676	677	678	679	680	681	682	683	684	685	686	687	688	689	690	691	692	693	694	695	696	697	698	699	700	701	702	703
704	705	706	707	708	709	710	711	712	713	714	715	716	717	718	719	720	721	722	723	724	725	726	727	728	729	730	731	732	733	734	735
736	737	738	739	740	741	742	743	744	745	746	747	748	749	750	751	752	753	754	755	756	757	758	759	760	761	762	763	764	765	766	767
768	769	770	771	772	773	774	775	776	777	778	779	780	781	782	783	784	785	786	787	788	789	790	791	792	793	794	795	796	797	798	799
800	801	802	803	804	805	806	807	808	809	810	811	812	813	814	815	816	817	818	819	820	821	822	823	824	825	826	827	828	829	830	831
832	833	834	835	836	837	838	839	840	841	842	843	844	845	846	847	848	849	850	851	852	853	854	855	856	857	858	859	860	861	862	863
864	865	866	867	868	869	870	871	872	873	874	875	876	877	878	879	880	881	882	883	884	885	886	887	888	889	890	891	892	893	894	895
896	897	898	899	900	901	902	903	904	905	906	907	908	909	910	911	912	913	914	915	916	917	918	919	920	921	922	923	924	925	926	927
928	929	930	931	932	933	934	935	936	937	938	939	940	941	942	943	944	945	946	947	948	949	950	951	952	953	954	955	956	957	958	959
960	961	962	963	964	965	966	967	968	969	970	971	972	973	974	975	976	977	978	979	980	981	982	983	984	985	986	987	988	989	990	991

Table 3: Pixelmap

Packets (UDP, only Ethernet device):

Number of packets	Packet size [byte]	HTPA type	Comments
1	144	HTPA8x8	-
1	544	HTPA16x16	-
2	1058+1054	HTPA32x31	see below for details
8	1101+621	HTPA64x62	see below for details

Packet details for HTPA32x31		
Packet No.	Packet size	Packet contains
1	1058	Data of Pixel0 - Pixel528
2	1054	Data of Pixel529 to end of frame

Each dataset (except of packet index) consists out of a 16 bit value. For serial order of the datasets refer to section "serial order in Frame".

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Control Messages:

In the set of control messages, expressions in angled braces have to be substituted by following strings:

[IP]   insert IP in ASCII format, i.e.: *"192.168.240.122"*

[MACID]   insert MAC ID in ASCII format and hexadecimal, i.e.: *"00.1A.22.33.44.55"*

[AT]   insert index of array types in ASCII format

Array type	Index
HTPA 8x8	*"0"*
HTPA 16x16	*"1"*
HTPA 32x31	*"3"*
HTPA 64x62	*"5"*

[MCLK]   insert Frequency of MCLK in ASCII format and kHz, i.e.: *"1050.1"*

[AMP]   insert state of amplification in ASCII format:

State	String
Low	*"low"*
High	*"high"*

[MSK]   insert subnet mask in ASCII format, i.e.: *"255.255.255.000"*

[DEVID]   insert 5 digit device ID in ASCII format, i.e. *"00197"* Range: 00000… 65535

Set of control messages:

Message1:	"Calling HTPA series devices"	(only Ethernet device)
Conditions:	Can be sent as Broadcast, or if device already known as normal packet.	
Answer:	"HTPA series responsed! I am Arraytype **[AT]**"	
	Firmware version, date and author information.	
	"I am running on **[MCLK]** kHz"	
	"Amplification is **[AMP]**\r\n"	
	"MAC-ID: **[MACID]** IP: **[IP]**\r\n"	
	A second packet with calibration depending information is send.	

Message2:	"x Release HTPA series device"	(only Ethernet device)
Result:	Device disables hardware IP filter. All packets except ARP's, DHCP requests, Broadcasts, Message1, Message3 and Message4 are discarded.	
Answer:	"HW-Filter released\r\n"	

Message3:	"HTPA device IP change request to **[IP]**.**[MSK]**."	(only Ethernet device)
Result:	The device changes the IP and the subnet mask to the given value and writes it to EEPROM. The IP becomes the default IP, therefore the device will use it at the next reset, if no DHCP is found.	
Answer:	"Device changed IP to **[IP]**. and Subnet to **[MSK]**.\r\n"	

Message4:	"Bind HTPA series device"	(only Ethernet device)
Result:	Device enables hardware IP filter. Only packets from sender IP, ARP's, DHCP requests and Broadcasts are accepted. Device accepts now the control characters listed in **Table 1.**	
Answer:	"HW Filter is **[IP]** MAC **[MACID]**\n\r""	
	Insert in the above string the IP and MAC-ID of the Sender from Message4.	

Control Messages [continued]:

Message5:	"Set EEPROM data"
Conditions:	Only possible if Message 4 already successful sent.
	ATTENTION! Calibration data is overwritten!!!
Result:	Writes the next received packets into EEPROM, if packet size is equal to 1024 bytes. Device writes to EEPROM, until EEPROM is completely filled. EEPROM size depends on Device type: HTPA8x8, HTPA16x16 and HTPA32x31: 16384 byte; HTPA64x62: 65536 byte.
Answer:	"Write was successful.\n\r"

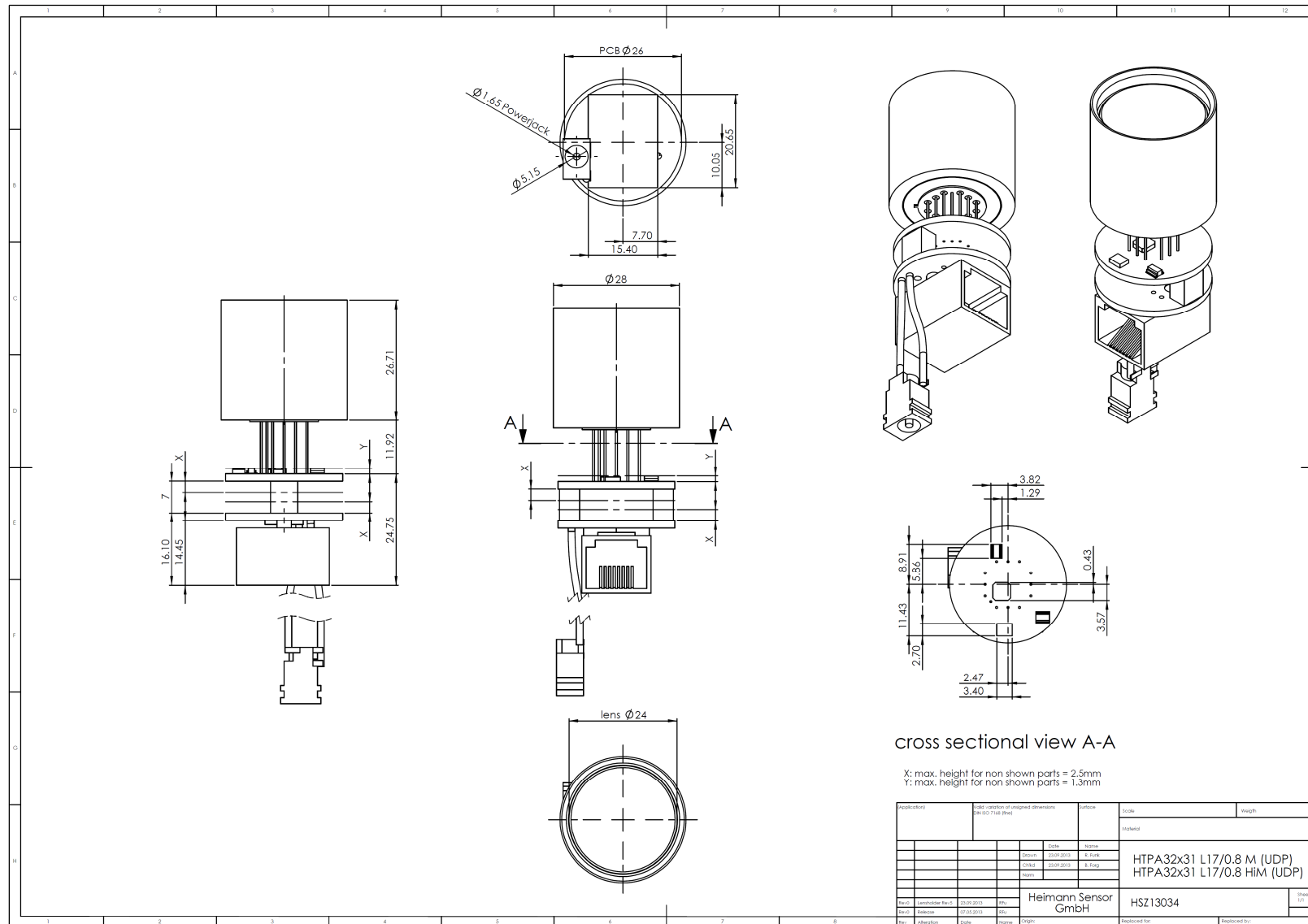|	
Message6:	"Set DeviceID to **[DEVID]**"
Result:	The given Device ID **[DEVID]** is written to EEPROM. This ID is shown on receive of 'M'. The eDevice ID can be used for customer specific purposes.
Answer:	"DeviceID changed to **[DEVID]**\r\n"

Specification for HTPA32x31L17/0.8HiM(UDP)

Rev.1: 2013.09.23 Fg

Dimensions:

cross sectional view A-A

X: max. height for non shown parts = 2.5mm
Y: max. height for non shown parts = 1.3mm

				Date	Name	HTPA32x31 L17/0.8 M (UDP)		
			Drawn	23.09.2013	R. Funk	HTPA32x31 L17/0.8 HiM (UDP)		
			Chkd	23.09.2013	B. Forg			
			Norm					
Rev.0	Lensholder Rev.5	23.09.2013	RFu		Heimann Sensor GmbH	HSZ13034		Sheet 1/1
Rev.0	Release	07.03.2013	RFu					
Rev.	Alteration	Date	Name		Origin:	Replaced for:	Replaced by:	

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