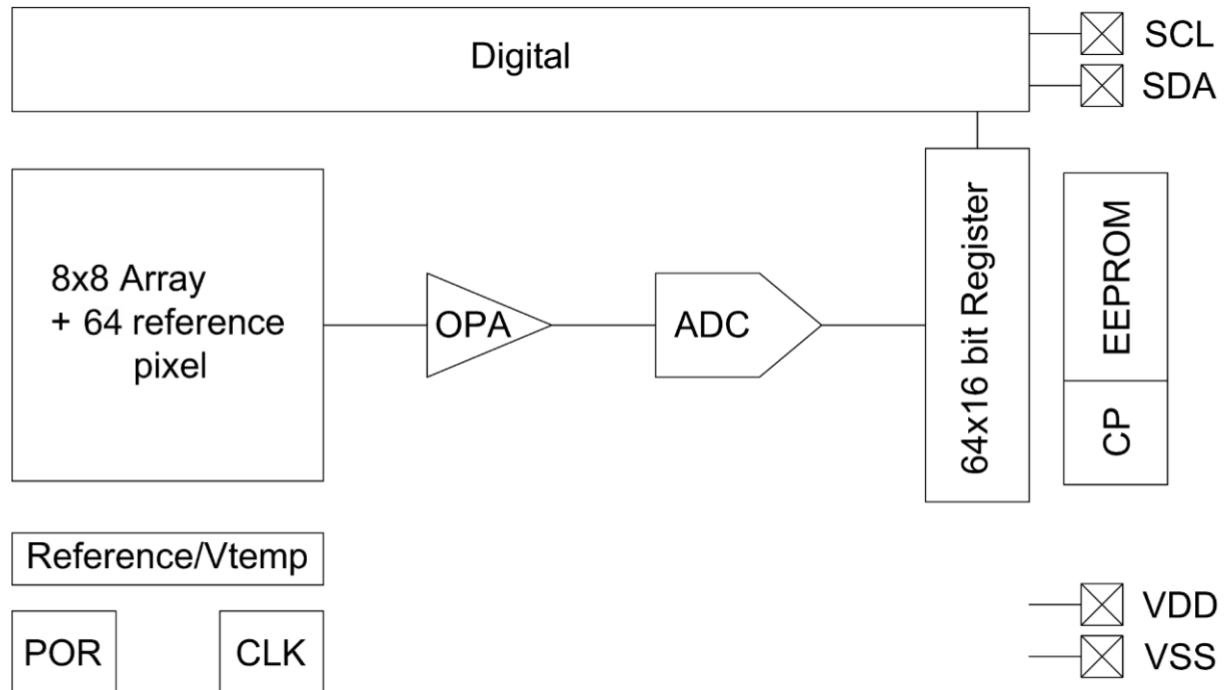


1 Principal Schematic for HTPA8x8d:



Prelim

2 Pin Assignment– Bottom View:

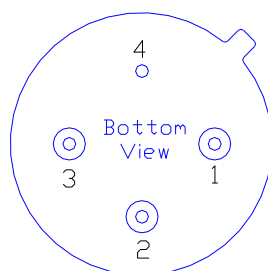
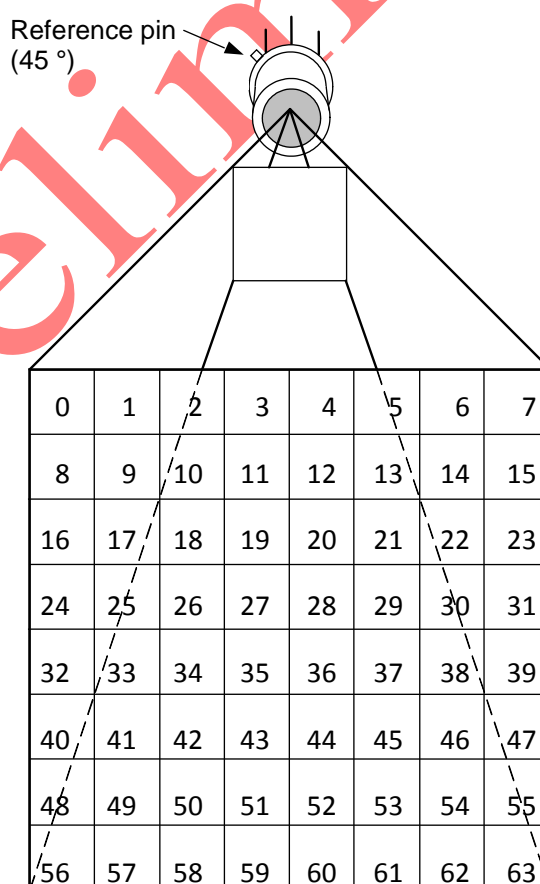


Figure 1: pin-allocation

Pin	Symbol	Description
1	SDA	Digital I/O, Open Drain, 100k PU, Serial Data
2	SCL	Digital I/O, Open Drain, 100k PU, Serial Clock
3	VDD	Positive supply voltage
4	VSS	Negative supply voltage / Ground (0V) (connected to housing)

3 Optical Orientation:



5 Characteristics:

5.1 Common Specifications:

Technology	n-poly/p-poly Si
Element Resistance	approx. 300 kOhms
Sensitivity	approx. 450 V/W without optics and filter
Thermal pixel time constant	<4 ms
Digital Interface	I ² C
Analog Output	No
selectable Clock	1 to 13 MHz
EEPROM size	256x16 Bit

Pitch	90 µm
Absorber size	44 µm
Max. Framerate	88 Hz
(maximum I ² C and sensor clock speed with full ADC-resolution)	
64 sensitive elements	

5.2 Optical characteristics:

Focal length:	2.1 mm ("L" equals the focal length of the lens)
F-Number:	0.8
Field of view:	23 x 23 deg
Lens coating:	LWP-coating 5.0
	Cut On (Tr. 5%): 5.0 µm ± 0.3 µm

5.3 Electric Specifications:

Absolute Maximum Ratings:

Parameter	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Supply Voltage	V _{DD}		-0.3		3.6	V
Voltage at All inputs and outputs	V _{IO}		-0.3		V _{DD} +0.3	V
Storage Temperature	T _{STG}		-40		85	Deg. C

Operating Conditions:

Parameter	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Supply Voltage	V _{DD}		3.3	3.35	3.6	V
Supply Current	I _{DD}			1.7	4.5	mA
Standby Current	I _{SBY}				10	μA
Operation Temperature	T _A		-20		85	Deg. C
ESD-Protection		Human body model	1.5			kV
		100pF + 1k50hm				

Electrical Characteristics

Parameter	Symbol	Condition	MIN.	TYP.	MAX.	Unit
-----------	--------	-----------	------	------	------	------

Digital Input

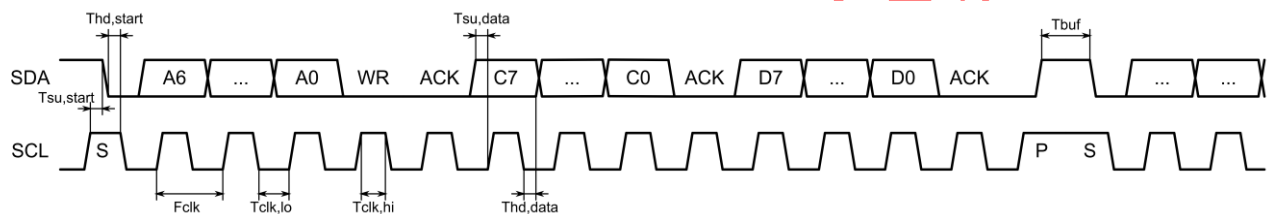
Internal Clock frequency	F _{CLK}		1	5	13	MHz
Internal I ² C Pull up	R _{PU}		1	100	100	kOhm
Bias current	I _{BIAS}		1	5	13	μA
BPA current	I _{BPA}		0.2	1.5	4.0	μA
Input voltage high	V _{IH}		0.7xV _{DD}			V
Input voltage low	V _{IL}				0.3xV _{DD}	V

PTAT

Temperature range			0		85	Deg. C
PTAT gradient			TBD	174	TBD	K/V

Preamplifier / ADC

Parameter	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Chopper frequency	F_{CHP}			20		kHz
Preamplifier Noise	N_{PA}	at 20 kHz		72		nV/HZ ^{1/2}
Frame rate	FR1		7.5	37	88	Hz
ADC pos. Reference	V_{REFP}			1.6		V
ADC neg. Reference	V_{REFN}			0.9		V
ADC resolution	ADC_{LSB}	at 16 Bit		21		μV

6 I²C Timings HTPA8x8d:

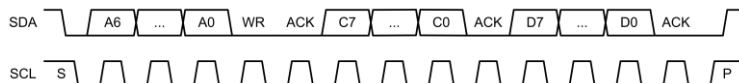
Parameter	Symbol	Condition	MIN.	TYP.	MAX.	Unit
I ² C clock frequency	F_{CLK}			400	1000	kHz
low pulse duration	$T_{\text{CLK,lo}}$		0.50			μs
high pulse duration	$T_{\text{CLK,hi}}$		0.26			μs
data set up time	$T_{\text{SU,data}}$		0.05			μs
data hold time	$T_{\text{hd,data}}$		0.00			μs
start setup time	$T_{\text{SU,start}}$		0.26			μs
start hold time	$T_{\text{hd,start}}$		0.26			μs
stop setup time	$T_{\text{SU,stop}}$		0.26			μs
stop hold time	$T_{\text{hd,stop}}$		0.26			μs
time between STOP / START	T_{buf}		0.50			μs

7 I²C Communication:

The chip uses the 7-bit I²C address 0x1A for configuration and sensor data and the address 0x1B to access the internal EEPROM followed by 1-bit of read/write command. The address byte is followed by an 8-bit command.

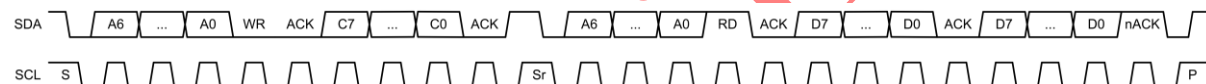
7.1 Write Command:

In case of a write access to an internal register the command is followed by the data byte. The chip acknowledges each byte with a low active ACK bit.



7.2 Read Command:

To read data from the chip first the address and command must be sent. After the last ACK a new start-bit (repeated start) and the address with a set read-flag initiates the read sequence. There can be bytes read as many as required. The last byte must be denoted by a not-acknowledge. The shown example below can be used e.g. to get the status register.



7.3 Sensor Commands:

The sensor has several registers that can be written and read, they are listed below.

Configuration register (write only)

Addr / CMD	0x1A / 0x01							
Config Reg	7	6	5	4	3	2	1	0
Name	RFU				START	RFU	BLIND	WAKEUP
Default	0	0	0	0	0	0	0	0

The WAKEUP bit is used to switch on / off the chip and must be set prior all other operations. After the START bit is set the chip starts a conversion of the array or blind elements and enters the idle state (not sleep!) when finished.

If the BLIND bit is set the electrical offsets are sampled instead of the active pixel.

RFU means reserved for future use and can be subject to change.

Status Register (read only)

Addr / CMD	0x1A / 0x02							
Status Reg	7	6	5	4	3	2	1	0
Name	RFU							EOC
Default	0	0	0	0	0	0	0	0

If the EOC flag is set a previous started conversion has been finished.

Trim Register 1 (write only)

Addr / CMD	0x1A / 0x03							
Trim Reg 1	7	6	5	4	3	2	1	0
Name	RFU				MBIT TRIM			
Default	0	0	0	0	1	1	0	0

MBIT_TRIM: m = 4 to 12 \Rightarrow (m+4) bit as ADC resolution (Default: m=12)

Trim Register 2 (write only)

Addr / CMD	0x1A / 0x04							
Trim Reg 2	7	6	5	4	3	2	1	0
Name	RFU				BIAS TRIM TOP			
Default	0	0	0	0	1	1	0	0

BIAS_TRIM_TOP: 0 to 31 \Rightarrow 1 μ A to 13 μ A (Default: 5 μ A)

This setting is used to adjust the bias current of the ADC. A faster clock frequency requires a higher bias current setting.

Trim Register 3 (write only)

Addr / CMD	0x1A / 0x05							
Trim Reg 3	7	6	5	4	3	2	1	0
Name	RFU				BIAS TRIM BOT			
Default	0	0	0	0	1	1	0	0

BIAS_TRIM_BOT: 0 to 31 \Rightarrow 1 μ A to 13 μ A (Default: 5 μ A)

This setting is used to adjust the bias current of the ADC. A faster clock frequency requires a higher bias current setting.

Trim Register 4 (write only)

Addr / CMD	0x1A / 0x06							
Trim Reg 4	7	6	5	4	3	2	1	0
Name	RFU				CLK TRIM			
Default	0	0	0	1	0	1	0	1

CLK_TRIM: 0 to 63 \Rightarrow 1MHz to 13MHz (Default: 5MHz)

NOTE: The measure time depends on the clock frequency settings. One quarter frame takes about:

$$t_{FR4} = \frac{32 \cdot (2^{MBIT} + 4)}{F_{CLK}} \approx 27ms @ 5MHz$$

Trim Register 5 (write only)

Addr / CMD	0x1A / 0x07							
Trim Reg 5	7	6	5	4	3	2	1	0
Name	RFU				BPA TRIM TOP			
Default	0	0	0	0	1	1	0	0

BPA_TRIM_TOP: 0 to 31 \Rightarrow 0.2 μ A to 4.0 μ A (Default: 1.5 μ A)

This setting is used to adjust the common mode voltage of the preamplifier.

Trim Register 6 (write only)

Addr / CMD	0x1A / 0x08							
Trim Reg 6	7	6	5	4	3	2	1	0
Name	RFU				BPA TRIM BOT			
Default	0	0	0	0	1	1	0	0

BPA_TRIM_BOT: 0 to 31 \Rightarrow 0.2 μ A to 4.0 μ A (Default: 1.5 μ A)

This setting is used to adjust the common mode voltage of the preamplifier.

Trim Register 7 (write only)

Addr / CMD	0x1A / 0x09							
Trim Reg 7	7	6	5	4	3	2	1	0
Name	PU SDA TRIM				PU SCL TRIM			
Default	1	0	0	0	1	0	0	0

PU_SDA_TRIM: select internal pull up resistor on SDA (Default: 100kOhm)

PU_SCL_TRIM: select internal pull up resistor on SCL (Default: 100kOhm)

“1000” = 100 kOhm; “0100” = 50 kOhm; “0010” = 10 kOhm; “0001” = 1 kOhm

Read Data 1 Command (Top Half of Array)

Addr / CMD	0x1A / 0x0A							
Read Data	7	6	5	4	3	2	1	0
1. Byte / 2. Byte	PTAT MSB / LSB							
3. Byte / 4. Byte	Pixel 0 MSB / LSB							
5. Byte / 6. Byte	Pixel 1 MSB / LSB							
...	...							
129. Byte / 130. Byte	Pixel 63 MSB / LSB							

The complete sensor data must be read at once. If the communication fails somewhere in between, all successive data will be corrupted. The readout can be stopped anywhere by pausing the clock. A new initialized readout proceeds at this stopped byte by continuing the clock, but the index is reset when a new conversion has been started.

If the bit for the electrical offsets (Bit 1 in Config 0x01) is set the electrical offsets are sampled and can be read similar to the active pixel:

Read Data electrical offsets (Top Half of Array)

Addr / CMD	0x1A / 0x0A							
Read Data	7	6	5	4	3	2	1	0
1. Byte / 2. Byte	PTAT MSB / LSB							
3. Byte / 4. Byte	electrical offset (0) MSB / LSB							
5. Byte / 6. Byte	electrical offset (1) MSB / LSB							
...	...							
129. Byte / 130. Byte	electrical offset (63) MSB / LSB							

The complete sensor data must be read at once. If the communication fails somewhere in between, all successive data will be corrupted. The readout can be stopped anywhere by pausing the clock. A new initialized readout proceeds at this stopped byte by continuing the clock, but the index is reset when a new conversion has been started.

7.4 EEPROM Commands:

To read/write data from/to the internal EEPROM the I2C address 0x1B is used.

EEPROM Commands

Name	CMD	Read / Write	Comment
Standby	0x00	W	
Active	0x01	W	releases all signals to default state wait for 15 μ s when wake up from standby
Normal Erase	0x02	W	program pulse width 5ms
Normal Write	0x03	W	program pulse width 5ms
Block Erase	0x04	W	program pulse width 5ms
Block Write	0x05	W	program pulse width 5ms
Normal Read	0x06	W	
Set Data	0x0A	W	16 bit data, MSB first
Get Data	0x0B	R	16 bit data, MSB first

Note: The EEPROM must be activated (wake up from standby) prior being used. The active command also initializes the EEPROM to its default state.

Note: Each word must be erased before it can be written, a write command stores only a “1” to the EEPROM cell.

Note: The commands “SET_DATA” / “GET_DATA” will increment the address pointer, except for the first execution after “SET_ADDR”.

7.5 I²C Example Sequences – EEPROM Wakeup / Standby

	ADDR	R/W	EEPROM_ACTIVE	
S	0x1B	0	0x01	P

	ADDR	R/W	EEPROM_STANDBY	
S	0x1B	0	0x00	P

7.6 I²C Example Sequences – EEPROM Block Erase / Block Write

	ADDR	R/W	BLOCK_ERASE	
S	0x1B	0	0x04	P

WAIT 5ms

	ADDR	R/W	EEPROM_ACTIVE	
S	0x1B	0	0x01	P

	ADDR	R/W	SET_DATA	DATA_MSB	DATA_LSB	
S	0x1B	0	0x0A	DATA	DATA	P

	ADDR	R/W	BLOCK_WRITE	
S	0x1B	0	0x05	P

WAIT 5ms

	ADDR	R/W	EEPROM_ACTIVE	
S	0x1B	0	0x01	P

7.7 I²C Example Sequences – EEPROM Sequential Erase / Write

	ADDR	R/W	SET_ADDR	EEP_ADDR	
S	0x1B	0	0x09	ADDR	P

	ADDR	R/W	NORMAL_ERASE	
S	0x1B	0	0x02	P

WAIT 5ms

	ADDR	R/W	SET_DATA	DATA_MSB	DATA_LSB	
S	0x1B	0	0x0A	DATA	DATA	P

	ADDR	R/W	NORMAL_WRITE	
S	0x1B	0	0x03	P

WAIT 5ms

	ADDR	R/W	SET_ADDR	EEP_ADDR	
S	0x1B	0	0x09	ADDR	P

	ADDR	R/W	NORMAL_ERASE	
S	0x1B	0	0x02	P

WAIT 5ms

	ADDR	R/W	SET_DATA	DATA_MSB	DATA_LSB	
S	0x1B	0	0x0A	DATA	DATA	P

	ADDR	R/W	NORMAL_WRITE	
S	0x1B	0	0x03	P

WAIT 5ms

	ADDR	R/W	SET_ADDR	EEP_ADDR	
S	0x1B	0	0x09	ADDR	P

	ADDR	R/W	NORMAL_ERASE	
S	0x1B	0	0x02	P

WAIT 5ms

	ADDR	R/W	SET_DATA	DATA_MSB	DATA_LSB	
S	0x1B	0	0x0A	DATA	DATA	P

	ADDR	R/W	NORMAL_WRITE	
S	0x1B	0	0x03	P

WAIT 5ms

	ADDR	R/W	EEPROM_ACTIVE	
S	0x1B	0	0x01	P

7.8 I2C Example Sequence – EEPROM Continuous Erase

	ADDR	R/W	SET_ADDR	EEP_ADDR	
S	0x1B	0	0x09	ADDR	P

	ADDR	R/W	NORMAL_ERASE	
S	0x1B	0	0x02	P

WAIT 5ms

	ADDR	R/W	SET_ADDR	EEP_ADDR	
S	0x1B	0	0x09	ADDR	P

	ADDR	R/W	NORMAL_ERASE	
S	0x1B	0	0x02	P

WAIT 5ms

	ADDR	R/W	EEPROM_ACTIVE	
S	0x1B	0	0x01	P

7.9 I2C Example Sequence – EEPROM Continuous Write

	ADDR	R/W	SET_ADDR	EEP_ADDR	
S	0x1B	0	0x09	ADDR	P

	ADDR	R/W	SET_DATA	DATA_MSB	DATA_LSB	
S	0x1B	0	0x0A	DATA	DATA	P

	ADDR	R/W	NORMAL_WRITE	
S	0x1B	0	0x03	P

WAIT 5ms

	ADDR	R/W	SET_DATA	DATA_MSB	DATA_LSB	
S	0x1B	0	0x0A	DATA	DATA	P

	ADDR	R/W	NORMAL_WRITE	
S	0x1B	0	0x03	P

WAIT 5ms

	ADDR	R/W	EEPROM_ACTIVE	
S	0x1B	0	0x01	P

7.10 I2C Example Sequence – EEPROM Sequential Read

	ADDR	R/W	SET_ADDR	EEP_ADDR	
S	0x1B	0	0x09	ADDR	P

	ADDR	R/W	NORMAL_READ	
S	0x1B	0	0x06	P

	ADDR	R/W	GET_DATA		ADDR	R/W	DATA_MSB	DATA_LSB	
S	0x1B	0	0x0B	Sr	0x1B	1	??	??	P

	ADDR	R/W	SET_ADDR	EEP_ADDR	
S	0x1B	0	0x09	ADDR	P

	ADDR	R/W	NORMAL_READ	
S	0x1B	0	0x06	P

	ADDR	R/W	GET_DATA		ADDR	R/W	DATA_MSB	DATA_LSB	
S	0x1B	0	0x0B	Sr	0x1B	1	??	??	P

	ADDR	R/W	EEPROM_ACTIVE	
S	0x1B	0	0x01	P

7.11 I2C Example Sequence – EEPROM Continuous Read

	ADDR	R/W	SET_ADDR	EEP_ADDR	
S	0x1B	0	0x09	ADDR	P

	ADDR	R/W	NORMAL_READ	
S	0x1B	0	0x06	P

	ADDR	R/W	GET_DATA		ADDR	R/W	DATA_MSB	DATA_LSB	
S	0x1B	0	0x0B	Sr	0x1B	1	??	??	P

	ADDR	R/W	GET_DATA		ADDR	R/W	DATA_MSB	DATA_LSB	
S	0x1B	0	0x0B	Sr	0x1B	1	??	??	P

	ADDR	R/W	GET_DATA		ADDR	R/W	DATA_MSB	DATA_LSB	
S	0x1B	0	0x0B	Sr	0x1B	1	??	??	P

	ADDR	R/W	GET_DATA		ADDR	R/W	DATA_MSB	DATA_LSB	
S	0x1B	0	0x0B	Sr	0x1B	1	??	??	P

	ADDR	R/W	EEPROM_ACTIVE	
S	0x1B	0	0x01	P

7.12 I2C Example Sequence – Init and Read Thermopile Array

	ADDR	R/W	CONFIG_REG	WAKEUP	
S	0x1A	0	0x01	0x01	P

	ADDR	R/W	TRIM_REG1	MBIT_TRIM	
S	0x1A	0	0x03	0x0C	P

	ADDR	R/W	TRIM_REG2	BIAS_TRIML	
S	0x1A	0	0x04	0x0C	P

	ADDR	R/W	TRIM_REG3	BIAS_TRIMR	
S	0x1A	0	0x05	0x0C	P

	ADDR	R/W	TRIM_REG4	CLK_TRIM	
S	0x1A	0	0x06	0x14	P

	ADDR	R/W	TRIM_REG5	BPA_TRIML	
S	0x1A	0	0x07	0x0C	P

	ADDR	R/W	TRIM_REG6	BPA_TRIMR	
S	0x1A	0	0x08	0x0C	P

	ADDR	R/W	TRIM_REG7	PU_TRIM	
S	0x1A	0	0x09	0x88	P

	ADDR	R/W	CONFIG_REG	START WAKEUP	
S	0x1A	0	0x01	0x09	P

	ADDR	R/W	STATUS_REG		ADDR	R/W	STATUS	
S	0x1A	0	0x02	Sr	0x1A	1	??	P

WAIT 30ms

	ADDR	R/W	STATUS_REG		ADDR	R/W	STATUS	
S	0x1A	0	0x02	Sr	0x1A	1	??	P

	ADDR	R/W	READ_DATA1		ADDR	R/W	PTAT1 MSB	PTAT1 LSB	P0,0 MSB	P0,0 LSB	...	Px,y MSB	Px,y LSB	
S	0x1A	0	0x0A	Sr	0x1A	1	??	??	??	??	...	??	??	P

	ADDR	R/W	CONFIG_REG	SLEEP	
S	0x1A	0	0x01	0x00	P

8 Temperature calculation:

The object and ambient temperature can be calculated from the sensor output and the stored calibration data. The table below is showing an overview of the EEPROM.

8x8d	0x00	0x01	0x02	0x03	0x04	0x05	0x06	0x07	0x08	0x09	0x0A	0x0B	0x0C	0x0D	0x0E	0x0F
0x00	PixC _{min} (float)		PixC _{max} (float)						gradScale	GlobalGain				TN	epsilon	
0x10											MBIT(PixC)	BIAS(PixC)	CLK(PixC)	BPA(PixC)	PU(PixC)	
0x20	MBIT(user)	BIAS(user)	CLK(user)	BPA(user)	PU(user)											
0x30					PTAT-gradient (float)	PTAT-offset (float)					Device ID [32 bit]					
0x40	ThGrad _i stored as 16 bit signed values															
0x50																
0x60																
0x70																
0x80	ThOffset _i stored as 16 bit signed values															
0x90																
0xA0																
0xB0																
0xC0	P _i stored as 16 bit unsigned values															
0xD0																
0xE0																
0xF0																

All values are stored as unsigned 16 bit values in the little endian format unless they are specified otherwise. Grey marked areas are used during calibration or for future use and are Heimann Sensor reserved.

MBIT(calib), BIAS(calib), CLK(calib), BPA(calib) and PU(calib) are the settings for the registers that have been used during calibration (see chapter 7.3 on how to set them).

MBIT(user), BIAS(user), CLK(user), BPA(user) and PU(user) are free to be set by the user.

The temperature calculation is only valid if the same settings are used that have been set during calibration!

8.1 Ambient Temperature:

The ambient temperature (T_a) is calculated from the measured PTAT value, the $PTAT_{gradient}$ and the $PTAT_{offset}$.

$$T_a = PTAT \cdot PTAT_{gradient} + PTAT_{offset} \quad (\text{Value is given back in dK})$$

where:

$PTAT_{gradient}$ is the gradient of the PTAT stored in the EEPROM as a float value

$PTAT_{offset}$ is the offset of the PTAT stored in the EEPROM as a float value

8.2 Thermal Offset:

The thermal offset of the sensor needs to be subtracted for each pixel to compensate for any thermal drifts.

$$V_{ij_Comp} = V_{ij} - \frac{ThGrad_{ij} \cdot T_a}{2^{gradScale}} - ThOffset_{ij}$$

where:

ij represents the row and column of the pixel

V_{ij_Comp} is the offset compensated voltage

V_{ij} is the raw pixel data (digital), readout from the RAM

$ThGrad_{ij}$ is the thermal gradient, stored in the EEPROM from 0x40 to 0x7F

$ThOffset_{ij}$ is the thermal offset, stored in the EEPROM from 0x80 to 0xBF

gradScale is the scaling coefficient for the thermal gradient

8.3 Electrical Offset

The electrical offset is used to compensate changes in the supply voltage. This compensation is only a subtraction so it can be done before or after the thermal offset compensation (here done afterwards).

The compensation for is done by using the following formula:

$$V_{ij_Comp}^* = V_{ij_Comp} - elOffset_{ij}$$

where:

ij represents the row (i) and column (j) of the pixel and electrical offset
*V_{ij_Comp}** is the electrical offset compensated voltage
V_{ij_Comp} is the thermal offset compensated voltage
elOffset_{ij} is the electrical offset belonging to Pixel ij and read from the RAM

8.4 Object Temperature:

The calculation of the object temperature is done by using a look-up table and doing a bi-linear interpolation, the matching table is given by the tablenumber (TN). The table is supplied in a separate file named "Table.c".

The sensitivity coefficients (*PixC_{ij}*) are calculated in the following way:

$$PixC_{ij} = \left(\frac{P_{ij} \cdot (PixC_{\max} - PixC_{\min})}{65535} + PixC_{\min} \right) \cdot \frac{\epsilon}{100} \cdot \frac{GlobalGain}{10000}$$

where:

PixC_{ij} is the sensitivity coefficient for each pixel
P_{ij} is the stored sensitivity coefficient scaled to 16 bit
PixC_{min} is the minimum sensitivity coefficient, used for scaling
PixC_{max} is the maximum sensitivity coefficient, used for scaling
epsilon is the emissivity factor

Leading to a compensation of the pixel voltage

$$V_{ij_PixC} = \frac{V_{ij_Comp}^* \cdot PCSCALEVAL}{PixC_{ij}}$$

where:

V_{ij_PixC} is the sensitivity compensated IR voltage
PCSCALEVAL is a scaling coefficient, typically $1 \cdot 10^8$

8.5 Example calculation:

Example values:

$$PTAT = 32357 \text{ Digits}$$

$$PTAT_{\text{gradient}} = 0.046 \text{ dK / Digit}$$

$$PTAT_{\text{offset}} = 1511.6 \text{ dK}$$

$$V_{00} = 34435 \text{ Digits}$$

$$\text{gradScale} = 15$$

$$ThGrad_{00} = 56693 \xrightarrow{\text{sign check}} -8842$$

$$ThOffset_{00} = 44$$

$$elOffset_{00} = 35000$$

$$PixC_{00} = 1.1 \cdot 10^8$$

$$PCSCALEVAL = 1 \cdot 10^8$$

Calculation of ambient temperature:

$$Ta = PTAT \cdot PTAT_{\text{gradient}} + PTAT_{\text{offset}} = 32357 \cdot 0.046 + 1511.6 \text{ dK} = 3000 \text{ dK}$$

Compensation of thermal offset:

$$V_{00_Comp} = V_{00} - \frac{ThGrad_{00} \cdot Ta}{2^{\text{gradScale}}} - ThOffset_{00} = 34435 - \frac{-8842 \cdot 3000}{2^{15}} - 44 = 35200$$

Compensation of electrical offset:

$$V_{00_Comp}^* = V_{00_Comp} - elOffset_{00} = 35200 - 35000 = 200$$

HTPA8x8dR1L2.1/0.8F5.0HiC[Si]

Thermopile Array With Lens Optics

Rev.5: 2017.06.12 Schnorr



Example look-up table:

TA[dK]/dig	2882	3032	3182	3332
-64	1494	2128	2491	2775
-32	2466	2692	2898	3091
0	2882	3032	3182	3332
32	3170	3285	3406	3530
64	3396	3491	3592	3699
96	3584	3665	3754	3848
128	3746	3818	3897	3981
160	3890	3954	4025	4102
192	4019	4078	4143	4214
224	4137	4191	4251	4317
256	4246	4296	4351	4413
288	4347	4393	4445	4503
320	4441	4485	4534	4588

$$V_{00_PixC} = \frac{200 \cdot 1 \cdot 10^8}{1.1 \cdot 10^8} = 182$$

Ta was calculated before to 3000 dK.

The matching region in the look-up table is already marked yellow, the bi-linear interpolation is leading to an object temperature of 3941 dK = 120.9 °C.

HTPA8x8dR1L2.1/0.8F5.0HiC[Si]

Thermopile Array With Lens Optics

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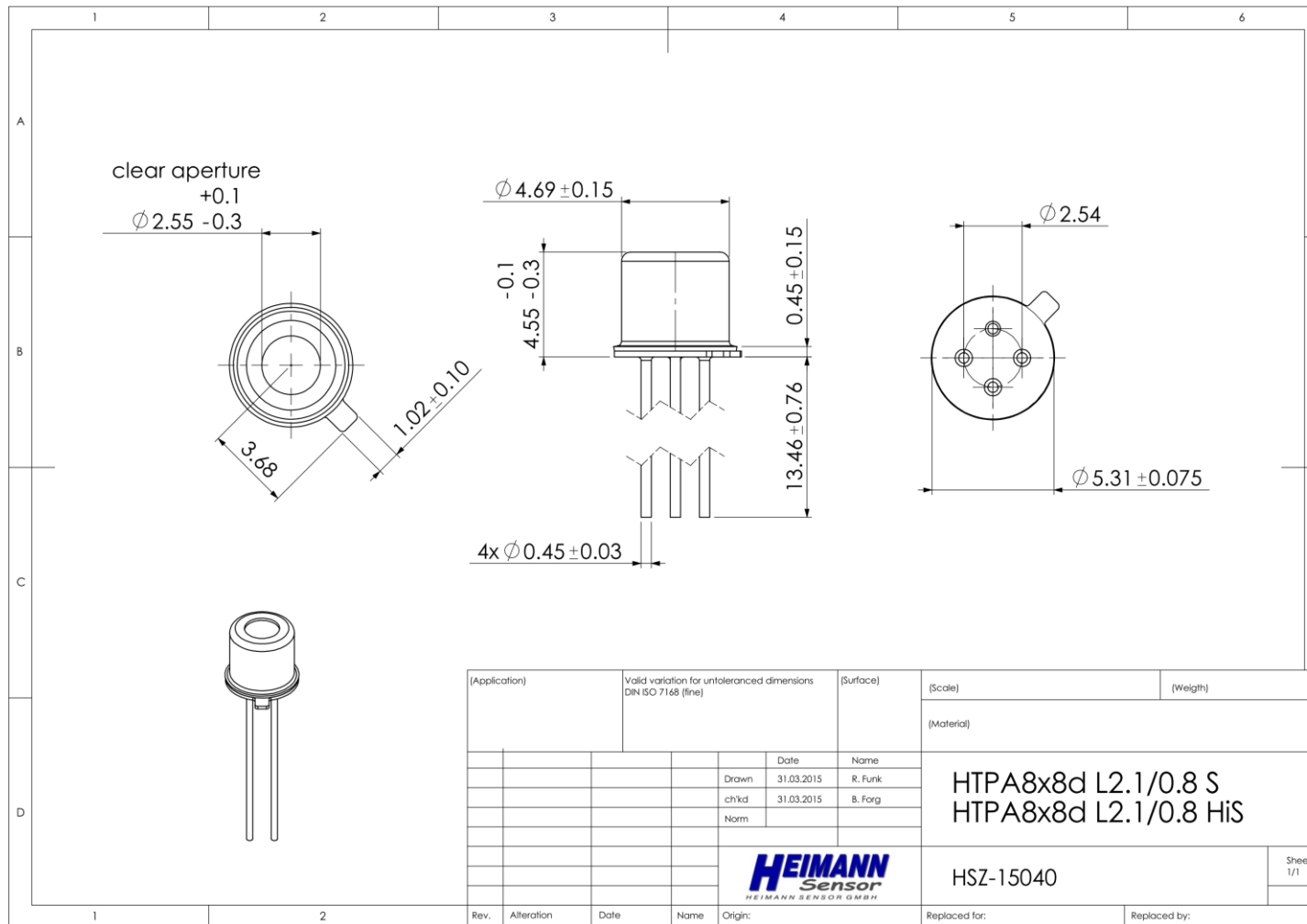
8.6 Look-up Table

The matching look-up table has to be taken from the Table.c file. Here is just shown an exemplary data for one optics.

dig \ Ta(dK)	2782	2882	2982	3082	3182	3282	3382
-512				1295	1742	2005	2202
-448				1848	2094	2284	2442
-384				2156	2340	2496	2634
-320				2381	2534	2677	2797
-256	2042	2244	2414	2562	2697	2822	2938
-192	2287	2445	2587	2717	2839	2954	3065
-128	2481	2612	2735	2852	2964	3073	3180
-64	2642	2755	2865	2972	3078	3182	3285
0	2782	2882	2982	3082	3182	3282	3382
64	2906	2996	3089	3183	3278	3375	3473
128	3019	3101	3187	3276	3368	3462	3558
192	3121	3197	3278	3363	3452	3544	3638
256	3216	3286	3363	3445	3531	3621	3715
320	3305	3370	3443	3522	3606	3695	3787
384	3387	3449	3519	3595	3677	3764	3856
448	3465	3524	3590	3664	3745	3831	3922
512	3539	3596	3659	3731	3810	3895	3986
576	3609	3662	3724	3794	3872	3957	4047
640	3676	3727	3787	3855	3932	4016	4106
704	3740	3788	3847	3914	3990	4073	4163
768	3802	3848	3904	3971	4046	4128	4218
832	3861	3905	3960	4025	4100	4182	4271
896	3918	3960	4014	4078	4152	4233	4322
960	3973	4014	4066	4129	4202	4284	4373
1024	4026	4065	4117	4179	4251	4332	4421
1088	4077	4115	4166	4227	4299	4380	4469
1152	4127	4164	4213	4274	4345	4426	4515
1216	4172	4210	4259	4319	4389	4469	4558
1280	4222	4257	4305	4364	4435	4515	4604
1344	4268	4302	4349	4408	4478	4558	4647
1408	4312	4345	4391	4450	4520	4600	4689
1472	4355	4388	4433	4491	4561	4641	4730
1536	4398	4429	4474	4532	4601	4681	4770
1600	4439	4470	4514	4571	4640	4720	4809
1664	4480	4509	4553	4610	4679	4758	4848
1728	4519	4548	4591	4648	4716	4796	4885
1792	4558	4586	4629	4685	4753	4833	4922
1856	4595	4623	4666	4721	4790	4869	4958
1920	4633	4660	4702	4757	4825	4905	4995
1984	4669	4696	4737	4792	4860	4940	5030
2048	4705	4731	4772	4826	4894	4974	5064
2112	4740	4765	4806	4860	4928	5008	5098
2176	4774	4799	4839	4894	4961	5041	5131
2240	4808	4832	4872	4926	4994	5074	5164
2304	4841	4865	4904	4958	5026	5106	5197
2368	4873	4897	4936	4990	5058	5137	5228
2432	4906	4929	4968	5021	5089	5169	5260
2496	4937	4960	4998	5052	5119	5199	5291
2560	4968	4991	5029	5082	5149	5230	5321
2624	4999	5021	5059	5112	5179	5259	5351
2688	5029	5050	5088	5141	5208	5289	5381
2752	5059	5080	5117	5170	5237	5318	5410
2816	5088	5109	5146	5199	5266	5346	5438
2880	5117	5137	5174	5227	5294	5375	5467
2944	5145	5165	5202	5255	5322	5402	5495
3008	5173	5193	5230	5282	5349	5430	5523
3072	5201	5220	5257	5309	5378	5457	5550
3136	5228	5247	5284	5336	5403	5484	5577
3200	5255	5274	5310	5362	5429	5510	5604
3264	5282	5300	5336	5388	5455	5537	5630
3328	5308	5326	5362	5414	5481	5563	5656
3392	5334	5352	5388	5439	5507	5588	5682
3456	5360	5377	5413	5465	5532	5613	5707
3520	5385	5403	5438	5489	5557	5638	5733
3584	5410	5427	5462	5514	5581	5662	5757
3648	5435	5452	5487	5538	5606	5688	5783
3712	5459	5476	5511	5562	5630	5712	5807
3776	5483	5500	5535	5586	5654	5736	5831
3840	5507	5524	5558	5610	5677	5760	5855
3904	5531	5547	5582	5633	5701	5783	5879
3968	5554	5571	5605	5656	5724	5806	5902
4032	5578	5594	5628	5679	5747	5829	5925
4096	5601	5616	5650	5702	5769	5852	5948
4160	5623	5639	5673	5724	5792	5875	5971
4224	5646	5661	5695	5746	5814	5897	5994
4288	5668	5683	5717	5768	5836	5919	6016
4352	5690	5705	5739	5790	5858	5941	6038
4416	5712	5727	5760	5811	5879	5963	6060
4480	5734	5748	5782	5833	5901	5984	6082
4544	5755	5770	5803	5854	5922	6006	6103
4608	5776	5791	5824	5875	5943	6027	6125
4672	5797	5811	5844	5895	5964	6048	6146
4736	5818	5832	5865	5916	5984	6069	6167
4800	5839	5853	5886	5937	6005	6089	6188
4864	5859	5873	5906	5957	6025	6109	6208
4928	5880	5893	5926	5977	6045	6130	6229
4992	5900	5913	5946	5997	6065	6150	6249
5056	5920	5933	5965	6017	6085	6170	6269
5120	5940	5953	5985	6036	6105	6190	6289
5184	5959	5972	6005	6056	6124	6209	6309
5248	5979	5991	6024	6075	6144	6229	6329
5312	5998	6011	6043	6094	6163	6248	6348
5376	6017	6030	6062	6113	6182	6267	6368
5440	6036	6049	6081	6132	6201	6286	6387
5504	6055	6067	6099	6150	6220	6305	6406
5568	6074	6086	6118	6169	6238	6324	6425
5632	6092	6104	6136	6187	6257	6343	6444
5696	6111	6123	6155	6206	6275	6361	6462
5760	6129	6141	6173	6224	6293	6379	6480
5824	6147	6159	6191	6242	6311	6398	6499
5888	6165	6177	6209	6260	6329	6416	6517
5952	6183	6195	6226	6277	6347	6434	6536
6016	6201	6212	6244	6295	6365	6451	6554
6080	6219	6230	6261	6313	6382	6469	6571
6144	6236	6247	6279	6330	6400	6487	6589
6208	6253	6264	6296	6347	6417	6504	6607
6272	6271	6282	6313	6364	6434	6522	6624
6336	6288	6299	6330	6381	6451	6539	6642
6400	6305	6316	6347	6398	6468	6556	6659
6464	6322	6332	6364	6415	6485	6573	6676
6528	6339	6349	6380	6432	6502	6590	6693
6592	6356	6366	6397	6448	6518	6607	6710
6656	6372	6382	6413	6465	6535	6623	6727
6720	6388	6399	6430	6481	6552	6640	6744
6784	6405	6415	6446	6497	6568	6656	6761
6848	6421	6431	6462	6514	6584	6673	6777
6912	6437	6447	6478	6530	6600	6689	6794
6976	6453	6463	6494	6546	6616	6705	6810
7040	6469	6479	6510	6562	6632	6721	6826
7104	6485	6495	6526	6577	6648	6737	6842
7168	6501	6511	6542	6593	6664	6753	6858
7232	6517	6526	6557	6608	6679	6768	6873
7296	6532	6542	6573	6624	6695	6784	6889
7360	6548	6557	6588	6640	6711	6800	6906
7424	6563	6572	6603	6655	6726	6815	6922
7488	6578	6588	6618	6670	6741	6830	6937
7552	6594	6603	6634	6685	6756	6845	6953
7616	6609	6618	6649	6700	6771	6860	6968
7680	6624	6633	6664	6715	6786	6875	6981
7744	6639	6648	6679	6730	6801	6890	6997
7808	6654	6663	6694	6745	6816	6905	7015
7872	6669	6677	6708	6759	6830	6919	7029
7936	6683	6692	6723	6774	6845	6934	7044
8000	6697	6707	6737	6788	6859	6948	7059
8064	6712	6721	6752	6803	6874	6963	7074
8128	6727	6735	6766	6817	6888	6977	7088
8192	6741	6750	6780	6831	6902	6991	7102
8256	6756	6764	6795	6846	6917	7006	7117
8320	6770	6778	6809	6860	6931	7020	7131
8384	6784	6793	6823	6874	6945	7034	7145
8448	6798	6806	6				

9 Outer Dimensions:

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