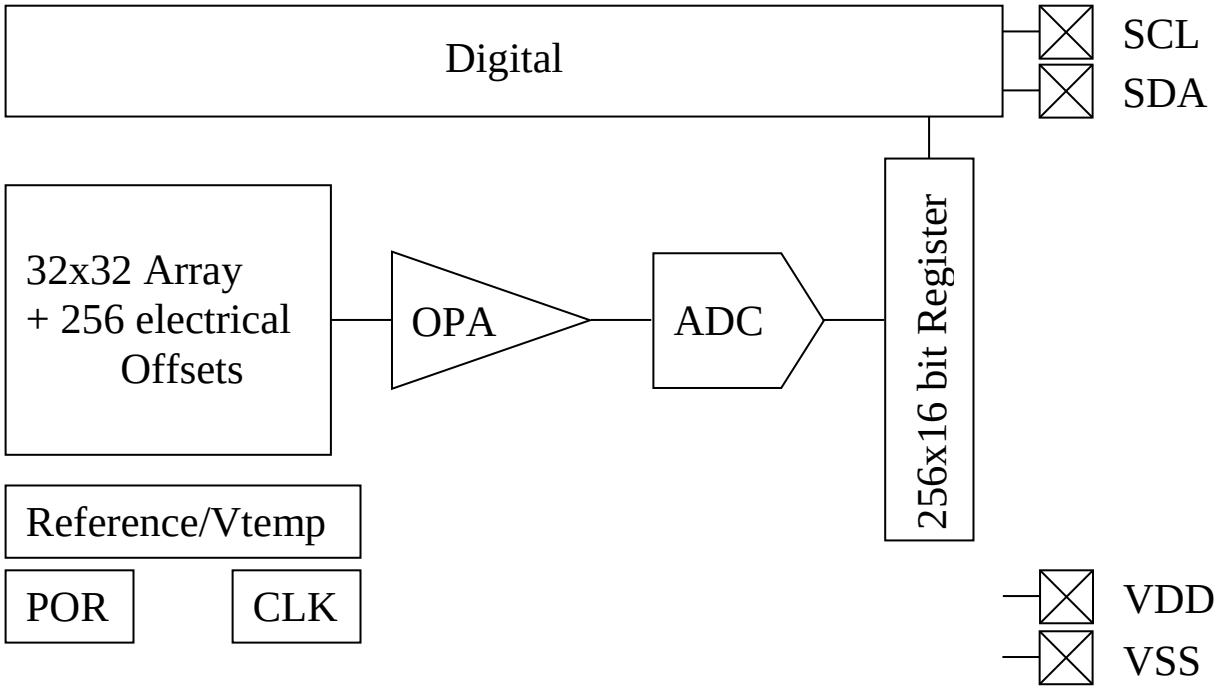
1 Principal Schematic for HTPA32x32d:



2 Pin Assignment– Bottom View:

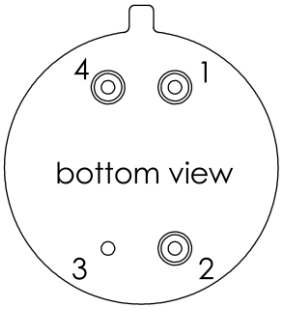


Figure 1: pin-allocation

Pin	Symbol	Description
1	SCL	Digital I/O, Open Drain, 100k PU, Serial Clock
2	VDD	Positive supply voltage
3	VSS	Negative supply voltage / Ground (0V) (connected to housing)
4	SDA	Digital I/O, Open Drain, 100k PU, Serial Data

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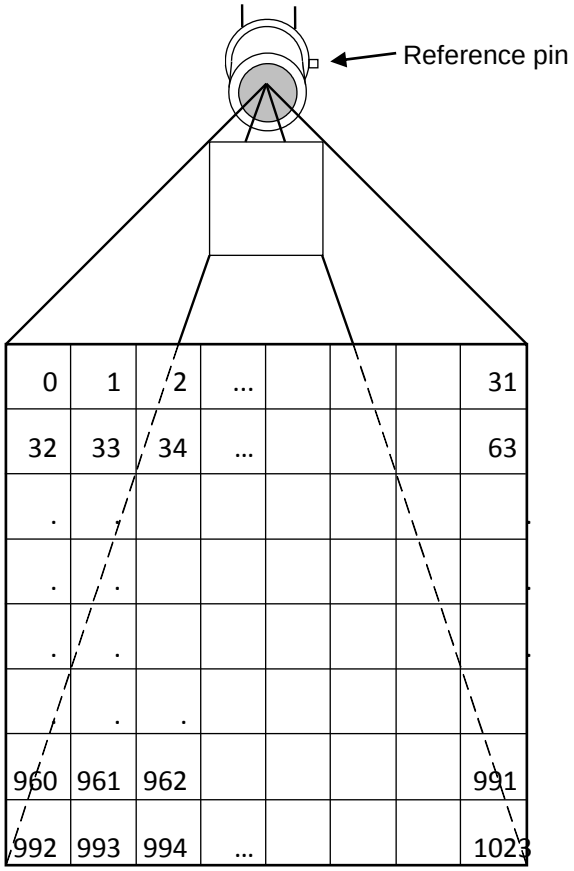
3 Optical Orientation:



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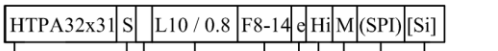
4 Order Code Example



Type:

HTPA32x31	Please contact support for all available HTPA and module combinations.

Package:

S	SMD variant
Not declared	TO can variant

Output:

d	HTPA sensor with digital output
Not declared	HTPA sensor with analogous output

Optics:

L	Focal length: In example L10 = 10.0 mm focal length
/	F-Number: In example /0.8
	For optics see also “HTPA standard optics”

Filter:

F	Filter characteristics. In example F8-14 (µm, Bandpass)
Not declared	Broad band ARC

External aperture:

Not declared	without external aperture
e	with external aperture

Sensitivity:

Hi	Increased sensitivity
Not declared	Standard sensitivity

Version:

A	Application set: comes with GUI, housing, power supply. Always UDP Interface.
C	Calibrated sensor (only digital). Carries calibration constants on internal EEPROM
M	Module: HTPA sensor soldered to PCB, calibrated stream
S	HTPA sensor only. Raw voltage output, not calibrated

Interface:

SPI	SPI device; Three variants: HTPA82x62: 16bit ADC all other analogous HTPAs: 14bit ADC Digital HTPA: 12bit ADC
LC	SPI,Only Analogous HTPA, 12bit ADC low speed, external processing required
UDP	Ethernet, CAT5 cable connection
PoE	Power over Ethernet, CAT5 connection, UDP protocol

Lens Material:

Si	Silicon
Not declared	Germanium

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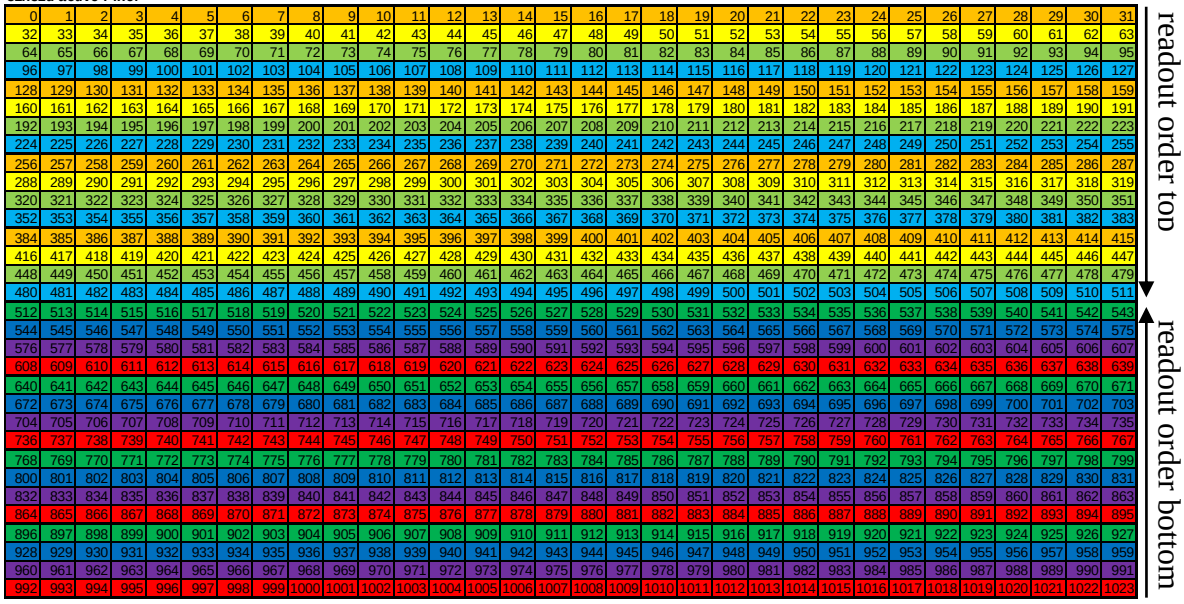
5 Serial Order of Frame

The sensor is divided into two parts (top and bottom half) which are again separated into 4 blocks. The readout order is shown below for the different blocks.

Block 0 (top)
Block 1 (top)
Block 2 (top)
Block 3 (top)
Block 3 (bottom)
Block 2 (bottom)
Block 1 (bottom)
Block 0 (bottom)

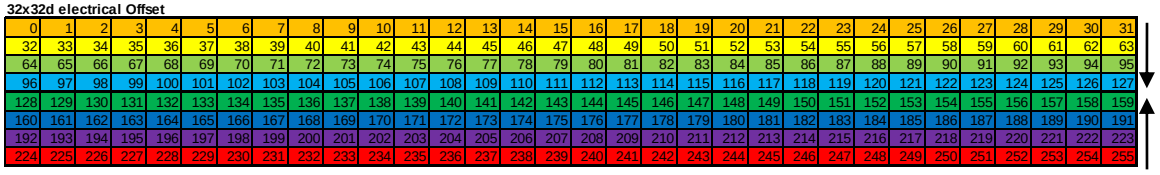
Whenever a conversion is started the Block x of the top and bottom half are measured at the same time. Each block consists of 128 Pixel that are sampled fully parallel. The readout order on the bottom half is mirrored compared to the top half so that the central lines are always read last.

32x32d active Pixel



The electrical offsets are sampled in parallel for the top and bottom half. The matching rows for the corresponding electrical offsets and active Pixel are marked with the same color. The conversion of the electrical offsets is started by sending the command for the BLIND bit during the start command, see 8.3.

32x32d electrical Offset



6 Characteristics:

6.1 Common Specifications:

Technology	n-poly/p-poly Si
Element Resistance	approx. 300 kOhms
Sensitivity	approx. 450 V/W without optics and filter
Thermal pixel time constant	<4 ms
Digital Interface	I²C
Analog Output	No
selectable Clock	1 to 13 MHz
EEPROM size	64 kBit

Pitch	90 µm
Absorber size	44 µm
Max. Framerate	60 Hz

(complete frame with maximum I²C and sensor clock speed and reduced ADC resolution)
1024 sensitive elements

6.2 Optical characteristics:

Focal length:	3.6 mm (“L” equals the focal length of the lens)
F-Number:	0.9
Field of view:	43 x 43 deg
Lens coating:	AR-Coating; average reflectance per surface < 3% for 8µm < λ < 11.5 µm Environment acc. for MIL-C-48497

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6.3 Electric Specifications:

Absolute Maximum Ratings:

Parameter	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Supply Voltage	V_{DD}		-0.3		3.6	V
Voltage at All inputs and outputs	V_{IO}		-0.3		V_{DD}+0.3	V
Storage Temperature	T_{STG}		-40		85	Deg. C

Operating Conditions:

Parameter	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Supply Voltage	V_{DD}		3.3	3.35	3.6	V
Supply Current (sensor running)	I_{DD}		5.0	5.5	6.4	mA
Supply Current (sensor in idle state)	I_{DD}		tbd	5.2	tbd	mA
Standby Current (sensor in sleep state)	I_{SBY}		2.0	2.1	2.5	µA
Operation Temperature	T_A		-20		65	Deg. C
ESD-Protection		Human body model 100pF + 1k5Ohm	2.0			kV

Electrical Characteristics

Parameter	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Digital Input						
Internal Clock frequency	F_{CLK}		1	5	13	MHz
Internal I²C Pull up	R_{PU}		1	100	100	kOhm
Bias current	I_{BIAS}		1	3	13	µA
BPA current	I_{BPA}		0.2	1.5	4.0	µA
Input voltage high	V_{IH}		0.7xV_{DD}			V
Input voltage low	V_{IL}				0.3xV_{DD}	V
PTAT						
Temperature range			TBD		TBD	Deg. C
PTAT gradient			170	174	178	K/V

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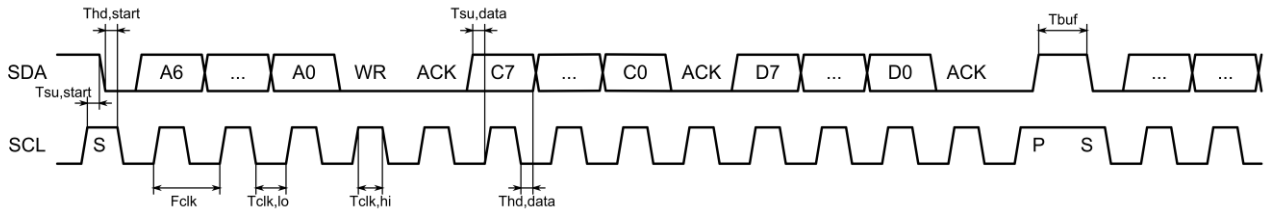
Preamplifier / ADC

Parameter	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Chopper frequency	F_{CHP}			20		kHz
Preamplifier Noise	N_{PA}	at 20 kHz		72		$nV/HZ^{1/2}$
Frame rate (Full Array)	FR1		2	9	60	Hz
Frame rate (Quarter Array)	FR4		8	36	240	HZ
ADC pos. Reference	V_{REFP}	REF_CAL 00		1.529		V
		REF_CAL 01		1.442		
		REF_CAL 10		1.355		
		REF_CAL 11		1.268		
ADC neg. Reference	V_{REFN}	REF_CAL 00		0.850		V
		REF_CAL 01		0.901		
		REF_CAL 10		0.968		
		REF_CAL 11		1.056		
ADC resolution	ADC_{LSB}	at 16 Bit	6.5		20.7	µV

7 I²C Timings HTPA32x32d:



Parameter	Symbol	Condition	MIN.	TYP.	MAX.	Unit
I²C clock frequency	F_{CLK}			400	1000	kHz
low pulse duration	$T_{CLK,lo}$		0.50			µs
high pulse duration	$T_{CLK,hi}$		0.26			µs
data set up time	$T_{SU,data}$		0.05			µs
data hold time	$T_{hd,data}$		0.00			µs
start setup time	$T_{SU,start}$		0.26			µs
start hold time	$T_{hd,start}$		0.26			µs
stop setup time	$T_{SU,stop}$		0.26			µs
stop hold time	$T_{hd,stop}$		0.26			µs
time between STOP / START	T_{buf}		0.50			µs

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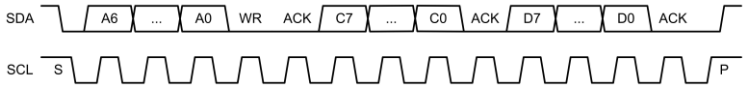
8 I²C Communication:

The chip uses the **7-bit I²C address 0x1A** for configuration and **sensor** data and the **7-bit I²C address 0x50** to access the internal **EEPROM**. The address byte is followed by a W/R bit and an 8-bit command.

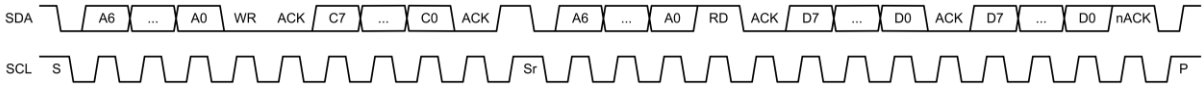
8.1 Write Command:

In case of a write access to an internal register the command is followed by the data byte. The chip acknowledges each byte with a low active ACK bit.



8.2 Read Command:

To read data from the chip first the address and command must be sent. After the last ACK a new start-bit (repeated start) and the address with a set read-flag initiates the read sequence. There can be bytes read as many as required. The last byte must be denoted by a not-acknowledge. The shown example below can be used e.g. to get the status register.



8.3 Sensor Commands:

The sensor has several registers that can be written and read, they are listed below.

Configuration register (write only)

Addr / CMD	0x1A (7 Bit!) / 0x01							
Config Reg	7	6	5	4	3	2	1	0
Name	RFU		BLOCK		START	VDD_MEAS	BLIND	WAKEUP
Default	0	0	0	0	0	0	0	0

The WAKEUP bit is used to switch on / off the chip and must be set prior all other operations. After the START bit is set the chip starts a conversion of the array or blind elements and enters the idle state (not sleep!) when finished. The BLOCK selects one of the four multiplexed array blocks.
If the BLIND bit is set the electrical offsets are sampled instead of the active pixel and the setting of the BLOCK is ignored.
If VDD_MEAS bit is set the VDD voltage is measured instead of the PTAT value.
RFU means reserved for future use and can be subject to change.

Status Register (read only)

Addr / CMD	0x1A (7 Bit!) / 0x02							
Status Reg	7	6	5	4	3	2	1	0
Name	RFU		BLOCK		RFU	VDD_MEAS	BLIND	EOC
Default	0	0	0	0	0	0	0	0

If the EOC flag is set a previous started conversion has been finished.

Trim Register 1 (write only)

Addr / CMD	0x1A (7 Bit!) / 0x03							
Trim Reg 1	7	6	5	4	3	2	1	0
Name	RFU		REF_CAL		MBIT TRIM			
Default	0	0	1	0	1	1	0	0

REF_CAL: selectable amplification, see 6.3 for more detail
MBIT_TRIM: m = 4 to 12 ⇒ (m+4) bit as ADC resolution (Default: m=12)

Trim Register 2 (write only)

Addr / CMD	0x1A (7 Bit!) / 0x04							
Trim Reg 2	7	6	5	4	3	2	1	0
Name	RFU			BIAS TRIM TOP				
Default	0	0	0	0	0	1	0	1

BIAS_TRIM_TOP: 0 to 31 ⇒ 1µA to 13µA (Default: 3µA)
This setting is used to adjust the bias current of the ADC. A faster clock frequency requires a higher bias current setting.

Trim Register 3 (write only)

Addr / CMD	0x1A (7 Bit!) / 0x05							
Trim Reg 3	7	6	5	4	3	2	1	0
Name	RFU			BIAS TRIM BOT				
Default	0	0	0	0	0	1	0	1

BIAS_TRIM_BOT: 0 to 31 ⇒ 1µA to 13µA (Default: 3µA)
This setting is used to adjust the bias current of the ADC. A faster clock frequency requires a higher bias current setting.

Trim Register 4 (write only)

Addr / CMD	0x1A (7 Bit!) / 0x06							
Trim Reg 4	7	6	5	4	3	2	1	0
Name	RFU		CLK TRIM					
Default	0	0	0	1	0	1	0	1

CLK_TRIM: 0 to 63 ⇒ 1MHz to 13MHz (Default: 5MHz)
NOTE: The measure time depends on the clock frequency settings. One quarter frame takes about:

$$t_{FR4} = \frac{32 \cdot \left(2^{MBIT} + 4\right)}{F_{CLK}} \approx 27ms@5MHz$$

Trim Register 5 (write only)

Addr / CMD	0x1A (7 Bit!) / 0x07							
Trim Reg 5	7	6	5	4	3	2	1	0
Name	RFU			BPA TRIM TOP				
Default	0	0	0	0	1	1	0	0

BPA_TRIM_TOP: 0 to 31 ⇒ 0.2µA to 4.0µA (Default: 1.5µA)
This setting is used to adjust the common mode voltage of the preamplifier.

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Trim Register 6 (write only)

Addr / CMD	0x1A (7 Bit!) / 0x08							
Trim Reg 6	7	6	5	4	3	2	1	0
Name	RFU			BPA TRIM BOT				
Default	0	0	0	0	1	1	0	0

BPA_TRIM_BOT: 0 to 31 ⇒ 0.2µA to 4.0µA (Default: 1.5µA)
This setting is used to adjust the common mode voltage of the preamplifier.

Trim Register 7 (write only)

Addr / CMD	0x1A (7 Bit!) / 0x09							
Trim Reg 7	7	6	5	4	3	2	1	0
Name	PU SDA TRIM				PU SCL TRIM			
Default	1	0	0	0	1	0	0	0

PU_SDA_TRIM: select internal pull up resistor on SDA (Default: 100kOhm)
PU_SCL_TRIM: select internal pull up resistor on SCL (Default: 100kOhm)
"1000" = 100 kOhm; "0100" = 50 kOhm; "0010" = 10 kOhm; "0001" = 1 kOhm

Read Data 1 Command (Top Half of Array)

Addr / CMD	0x1A (7 Bit!) / 0x0A							
Read Data	7	6	5	4	3	2	1	0
1. Byte / 2. Byte	PTAT 1 MSB / LSB or Vdd 1 MSB / LSB							
3. Byte / 4. Byte	Pixel (0+BLOCK*128) MSB / LSB							
5. Byte / 6. Byte	Pixel (1+BLOCK*128) MSB / LSB							
…	…							
257. Byte / 258. Byte	Pixel (127+BLOCK*128) MSB / LSB							

Read Data 2 Command (Bottom Half of Array)

Addr / CMD	0x1A (7 Bit!) / 0x0B							
Read Data	7	6	5	4	3	2	1	0
1. Byte / 2. Byte	PTAT 2 MSB / LSB or Vdd 2 MSB / LSB							
3. Byte / 4. Byte	Pixel (992-BLOCK*128) MSB / LSB							
5. Byte / 6. Byte	Pixel (993-BLOCK*128) MSB / LSB							
…								
65. Byte / 66. Byte	Pixel (1023-BLOCK*128) MSB / LSB							
67. Byte / 68. Byte	Pixel (960-BLOCK*128) MSB / LSB							
69. Byte / 70. Byte	Pixel (961-BLOCK*128) MSB / LSB							
…								
129. Byte / 130. Byte	Pixel (991-BLOCK*128) MSB / LSB							
131. Byte / 132. Byte	Pixel (928-BLOCK*128) MSB / LSB							
…								
257. Byte / 258. Byte	Pixel (927-BLOCK*128) MSB / LSB							

The complete sensor data must be read at once. If the communication fails somewhere in between, all successive data will be corrupted. The readout can be stopped anywhere by pausing the clock. A new initialized readout proceeds at this stopped byte by continuing the clock, but the index is reset when a new conversion has been started.

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If the bit for the electrical offsets (Bit 1 in Config 0x01) is set the electrical offsets are sampled and can be read similar to the active pixel:

Read Data electrical offsets (Top Half of Array)

Addr / CMD	0x1A (7 Bit!) / 0x0A							
Read Data	7	6	5	4	3	2	1	0
1. Byte / 2. Byte	PTAT 1 MSB / LSB or Vdd 1 MSB / LSB							
3. Byte / 4. Byte	electrical offset (0) MSB / LSB							
5. Byte / 6. Byte	electrical offset (1) MSB / LSB							
…	…							
257. Byte / 258. Byte	electrical offset (127) MSB / LSB							

Read Data electrical offsets (Bottom Half of Array)

Addr / CMD	0x1A (7 Bit!) / 0x0B							
Read Data	7	6	5	4	3	2	1	0
1. Byte / 2. Byte	PTAT 2 MSB / LSB or Vdd 2 MSB / LSB							
3. Byte / 4. Byte	electrical offset (224) MSB / LSB							
5. Byte / 6. Byte	electrical offset (225) MSB / LSB							
…	…							
65. Byte / 66. Byte	electrical offset (255) MSB / LSB							
67. Byte / 68. Byte	electrical offset (192) MSB / LSB							
…	…							
257. Byte / 258. Byte	electrical offset (159) MSB / LSB							

The complete sensor data must be read at once. If the communication fails somewhere in between, all successive data will be corrupted. The readout can be stopped anywhere by pausing the clock. A new initialized readout proceeds at this stopped byte by continuing the clock, but the index is reset when a new conversion has been started.
Depending on the setting of VDD_MEAS the PTAT or the VDD is transmitted.

8.4 EEPROM communication

The built-in EEPROM (24AA64 from Microchip) consists of 8 blocks of 1K x 8-bit. The chip select of the EEPROM is set to 000 (A2 to A0). For further information please see the corresponding datasheet:
http://ww1.microchip.com/downloads/en/DeviceDoc/21189f.pdf

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8.5 I²C Example Sequences – Init and Read Thermopile Array

(There should be a delay of at least 5 ms between the write of each Configuration Register)

	ADDR	W/R	CONFIG_REG	WAKEUP	
S	0x1A	0	0x01	0x01	P

	ADDR	W/R	TRIM_REG1	MBIT_TRIM	
S	0x1A	0	0x03	0x0C	P

	ADDR	W/R	TRIM_REG2	BIAS_TRIML	
S	0x1A	0	0x04	0x0C	P

	ADDR	W/R	TRIM_REG3	BIAS_TRIMR	
S	0x1A	0	0x05	0x0C	P

	ADDR	W/R	TRIM_REG4	CLK_TRIM	
S	0x1A	0	0x06	0x14	P

	ADDR	W/R	TRIM_REG5	BPA_TRIML	
S	0x1A	0	0x07	0x0C	P

	ADDR	W/R	TRIM_REG6	BPA_TRIMR	
S	0x1A	0	0x08	0x0C	P

	ADDR	W/R	TRIM_REG7	PU_TRIM	
S	0x1A	0	0x09	0x88	P

	ADDR	W/R	CONFIG_REG	START|WAKEUP	
S	0x1A	0	0x01	0x09	P

	ADDR	W/R	STATUS_REG		ADDR	W/R	STATUS	
S	0x1A	0	0x02	Sr	0x1A	1	??	P

Wait 30 ms

	ADDR	W/R	STATUS_REG		ADDR	W/R	STATUS	
S	0x1A	0	0x02	Sr	0x1A	1	??	P

	ADDR	W/R	READ_DATA 1		ADDR	W/R	PTAT1 MSB	PTAT1 LSB	P0,0 MSB	P0,0 LSB	…	Px,y MSB	Px,y LSB	
S	0x1A	0	0x0A	Sr	0x1A	1	??	??	??	??	…	??	??	P

	ADDR	W/R	READ_DATA 2		ADDR	W/R	PTAT2 MSB	PTAT2 LSB	P0,0 MSB	P0,0 LSB	…	Px,y MSB	Px,y LSB	
S	0x1A	0	0x0B	Sr	0x1A	1	??	??	??	??	…	??	??	P

	ADDR	W/R	CONFIG_REG	SLEEP	
S	0x1A	0	0x01	0x00	P

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9 Temperature calculation

The object and ambient temperature can be calculated from the sensor output and the stored calibration data. The table below is showing an overview of the EEPROM.

32x32d	0x00	0x01	0x02	0x03	0x04	0x05	0x06	0x07	0x08	0x09	0x0A	0x0B	0x0C	0x0D	0x0E	0x0F
0x0000	PixCmin [float]				PixCmax [float]				gradScale			TN as 16 bit unsigned		epsilon		
0x0010											MBIT(calib)	BIAS(calib)	CLK(calib)	BPA(calib)	PU(calib)	
0x0020			Arraytype				VddCalib									
0x0030					PTAT-gradient (float)				PTAT-offset (float)							
0x0040															VddScGrad	VddScOff
0x0050					GlobalOff	GlobalGain										
0x0060	MBIT(user)	BIAS(user)	CLK(user)	BPA(user)	PU(user)											
0x0070	DeviceID															NrOfDefPix
0x0080 0x0090 0x00A0	DeadPixAdr as 16 bit unsigned values															
0x00B0	DeadPixMask															
0x00C0	DeadPixMask								free to use							
0x00D0 ... 0x0330	free to use															
0x0340 ... 0x0530	$VddCompGrad_{ij}$ stored as 16 bit signed values															
0x0540 ... 0x0730	$VddCompOff_{ij}$ stored as 16 bit signed values															
0x0740 ... 0x0F30	$ThGrad_{ij}$ stored as 16 bit signed values															
0x0F40 ... 0x1730	$ThOffset_{ij}$ stored as 16 bit signed values															
0x1740 ... 0x1F30	P_{ij} stored as 16 bit unsigned values															

All values are stored as unsigned 8 bit values unless they are specified otherwise. The little endian format is used for larger values. Grey marked areas are used during calibration or for future use and are Heimann Sensor reserved.
MBIT(calib), BIAS(calib), CLK(calib), BPA(calib) and PU(calib) are the settings for the registers that have been used during calibration (see chapter 8.3 on how to set them).
MBIT(user), BIAS(user), CLK(user), BPA(user) and PU(user) are free to be set by the user.
The temperature calculation is only valid if the same settings are used that have been set during calibration!
TN is the tablenumber and has to match the given tablenumber in the sample code.
GlobalOff is stored as an 8 bit signed value, GlobalGain and VddCalib are both stored as 16 bit unsigned.
VddCalib is the used supply voltage during calibration measured by the sensor itself and stored in Digits.
The corresponding order of $ThGrad_{ij}$, $ThOffset_{ij}$ and P_{ij} to the Pixelnumber is given by the following overview:

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$ThGrad_{0,0}$	→ Pixel 0	$ThGrad_{0,1}$	→ Pixel 1	…	$ThGrad_{0,31}$	→ Pixel 31
$ThGrad_{1,0}$	→ Pixel 32	$ThGrad_{1,1}$	→ Pixel 33	…	$ThGrad_{1,31}$	→ Pixel 63
.						
.						
.						
$ThGrad_{15,0}$	→ Pixel 480	$ThGrad_{15,1}$	→ Pixel 481	…	$ThGrad_{15,31}$	→ Pixel 511
$ThGrad_{16,0}$	→ Pixel 992	$ThGrad_{16,1}$	→ Pixel 993	…	$ThGrad_{16,31}$	→ Pixel 1023
$ThGrad_{17,0}$	→ Pixel 960	$ThGrad_{17,1}$	→ Pixel 961	…	$ThGrad_{17,31}$	→ Pixel 991
.						
.						
.						
$ThGrad_{31,0}$	→ Pixel 512	$ThGrad_{31,1}$	→ Pixel 513	…	$ThGrad_{31,31}$	→ Pixel 543

The order of $VddCompGrad_{ij}$ and $VddCompOff_{ij}$ is similar to the electrical Offsets and have to be used block by block.

$VddCompGrad_{0,0}$	→ Pixel 0	$VddCompGrad_{0,1}$	→ Pixel 1	…	$VddCompGrad_{0,31}$	→ Pixel 31	top half
$VddCompGrad_{1,0}$	→ Pixel 32	$VddCompGrad_{1,1}$	→ Pixel 33	…	$VddCompGrad_{1,31}$	→ Pixel 63	
$VddCompGrad_{2,0}$	→ Pixel 64	$VddCompGrad_{2,1}$	→ Pixel 65	…	$VddCompGrad_{2,31}$	→ Pixel 95	
$VddCompGrad_{3,0}$	→ Pixel 96	$VddCompGrad_{3,1}$	→ Pixel 97	…	$VddCompGrad_{3,31}$	→ Pixel 127	
$VddCompGrad_{0,0}$	→ Pixel 128	$VddCompGrad_{0,1}$	→ Pixel 129	…	$VddCompGrad_{0,31}$	→ Pixel 159	
.							
.							
.							
$VddCompGrad_{3,0}$	→ Pixel 480	$VddCompGrad_{3,1}$	→ Pixel 481	…	$VddCompGrad_{3,31}$	→ Pixel 511	
$VddCompGrad_{4,0}$	→ Pixel 992	$VddCompGrad_{4,1}$	→ Pixel 993	…	$VddCompGrad_{4,31}$	→ Pixel 1023	bottom half
$VddCompGrad_{5,0}$	→ Pixel 960	$VddCompGrad_{5,1}$	→ Pixel 961	…	$VddCompGrad_{5,31}$	→ Pixel 991	
$VddCompGrad_{6,0}$	→ Pixel 928	$VddCompGrad_{6,1}$	→ Pixel 929	…	$VddCompGrad_{6,31}$	→ Pixel 959	
$VddCompGrad_{7,0}$	→ Pixel 896	$VddCompGrad_{7,1}$	→ Pixel 897	…	$VddCompGrad_{7,31}$	→ Pixel 927	
$VddCompGrad_{4,0}$	→ Pixel 864	$VddCompGrad_{4,1}$	→ Pixel 865	…	$VddCompGrad_{4,31}$	→ Pixel 895	
.							
.							
.							
$VddCompGrad_{4,0}$	→ Pixel 512	$VddCompGrad_{4,1}$	→ Pixel 513	…	$VddCompGrad_{4,31}$	→ Pixel 543	

9.1 Ambient Temperature:

The ambient temperature (Ta) is calculated from the average measured PTAT value, the $PTAT_{gradient}$ and the $PTAT_{offset}$.

$$Ta = PTAT_{av} \cdot PTAT_{gradient} + PTAT_{offset}$$ (Value is given back in dK)

where:

$PTAT_{gradient}$ is the gradient of the PTAT stored in the EEPROM as a float value

$PTAT_{offset}$ is the offset of the PTAT stored in the EEPROM as a float value

$PTAT_{av} = \frac{\sum_{i=0}^{7} PTAT_i}{8}$ is the average measured PTAT value

9.2 Thermal Offset:

The thermal offset of the sensor needs to be substracted for each pixel to compensate for any thermal drifts.

$$V_{ij\_Comp} = V_{ij} - \frac{ThGrad_{ij} \cdot PTAT_{av}}{2^{gradScale}} - ThOffset_{ij}$$

where:

ij	represents the row (i) and column (j) of the pixel
V_{ij_Comp}	is the thermal offset compensated voltage
V_{ij}	is the raw pixel data (digital), readout from the RAM
$ThGrad_{ij}$	is the thermal gradient, stored in the EEPROM from 0x740 to 0xF3F
$ThOffset_{ij}$	is the thermal offset, stored in the EEPROM from 0xF40 to 0x173F
$gradScale$	is the scaling coefficient for the thermal gradient stored in the EEPROM

9.3 Electrical Offset

The electrical offset is used to compensate changes in the supply voltage. This compensation is only a substraction so it can be done before or after the thermal offset compensation (here done afterwards).
The compensation for the top half is done by using the following formula:

$$V_{ij\_Comp}* = V_{ij\_Comp} - elOffset\left[(j + i \cdot 32):128\right]$$

and the bottom half analogue with this formula:

$$V_{ij\_Comp}* = V_{ij\_Comp} - elOffset\left[(j + i \cdot 32):128 + 128\right]$$

where:

ij	represents the row (i) and column (j) of the pixel and electrical offset
$V_{ij\_Comp}*$	is the thermal and electrical offset compensated voltage
V_{ij_Comp}	is the thermal offset compensated voltage
$elOffset\left[ij\right]$	is the electrical offset belonging to Pixel ij
i:128	is the rest of the integer division of i by 128 (e.g. 130:128=2)

Please see chapter 5 for the serial order.

9.4 Vdd Compensation

A supply voltage compensation called VddComp is used to take care of supply voltage changes. In order to use this compensation the supply voltage of the sensor (Vdd) has to be measured by the sensor from time to time by setting the configuration register and the average of Vdd 1 and Vdd 2 is resulting in Vdd (similar like $PTAT_{av}$).
The compensation for the top half is done by using the following formula:

$$V_{ij\_VDDComp} = V_{ij\_Comp}* - \frac{\left(\frac{VddCompGrad\left[(j + i \cdot 32):128\right] \cdot PTAT_{av}}{2^{VddScGrad}} + VddCompOff\left[(j + i \cdot 32):128\right]\right) \cdot (Vdd - VddCalib)}{2^{VddScOff}}$$

and the bottom half analogue with this formula:

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$$V_{ij\_VDDComp} = V_{ij\_Comp}^{*} - \frac{\left(\frac{VddCompGrad\left[\left(j+i\cdot 32\right):128+128\right]\cdot PTAT_{av}}{2^{VddScGrad}} + VddCompOff\left[\left(j+i\cdot 32\right):128+128\right]\right)\cdot\left(Vdd - VddCalib\right)}{2^{VddScOff}}$$

where:

ij	represents the row (i) and column (j) of the pixel
$V_{ij\_VDDComp}$	is the Vdd compensated voltage
$V_{ij\_Comp}^{*}$	is the thermal and electrical offset compensated voltage
$VddCompGrad[ij]$	is the VddComp gradient belonging to Pixel ij
$VddCompOff[ij]$	is the VddComp offset belonging to Pixel ij
$i:128$	is the rest of the integer division of i by 128 (e.g. 130:128=2)
Vdd	is the average measured supply voltage of the sensor in Digits
$VddCalib$	is the supply voltage during calibration stored in the EEPROM 0x26 & 0x27
$VddScGrad$	is a scaling coefficient and stored in the EEPROM
$VddScOff$	is a scaling coefficient and stored in the EEPROM

9.5 Object Temperature:

The calculation of the object temperature is done by using a look-up table and doing a bi-linear interpolation, the matching table is given by the tablenumber (TN). The table is supplied in a separate file named “Table.c”. If you do not have the file, please ask Heimann Sensor for support.

The sensitivity coefficients ($PixC_{ij}$) are calculated in the following way:

$$PixC_{ij} = \left(\frac{P_{ij}\cdot\left(PixC_{\max} - PixC_{\min}\right)}{65535} + PixC_{\min}\right)\cdot\frac{epsilon}{100}\cdot\frac{GlobalGain}{10000}$$

where:

$PixC_{ij}$	is the sensitivity coefficient for each pixel
P_{ij}	is the stored sensitivity coefficient scaled to 16 bit
$PixC_{\min}$	is the minimum sensitivity coefficient, used for scaling
$PixC_{\max}$	is the maximum sensitivity coefficient, used for scaling
$epsilon$	is the emissivity factor
$GlobalGain$	is a factor for fine tuning of the sensitivity for all Pixel

Leading to a compensation of the pixel voltage

$$V_{ij\_PixC} = \frac{V_{ij\_VDDComp}\cdot \mathrm{PCSCALEVAL}}{PixC_{ij}}$$

where:

V_{ij_PixC}	is the sensitivity compensated IR voltage
PCSCALEVAL	is a defined scaling coefficient, typically set to $1\cdot 10^{8}$

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9.6 Example calculation:

Example values:

$$PTAT_{av} = \frac{\sum_{i=0}^{7} PTAT_i}{8} = 38152\ Digits$$

$$PTAT_{gradient} = 0.0211\ dK / Digit$$

$$PTAT_{offset} = 2195.0\ dK$$

$$V_{00} = 34435\ Digits$$

$$elOffset[0] = 34240$$

$$gradScale = 24$$

$$ThGrad_{00} = 11137 \xrightarrow[signcheck]{} 11137$$

$$ThOffset_{00} = 65506 \xrightarrow[signcheck]{} -30$$

$$Vdd = 35000$$

$$VddCalib = 33942$$

$$VddCompGrad[0] = 10356 \xrightarrow[signcheck]{} 10356$$

$$VddCompOff[0] = 51390 \xrightarrow[signcheck]{} -14146$$

$$VddScGrad = 16$$

$$VddScOff = 23$$

$$PixC_{00} = 1.1 \cdot 10^8$$

$$\text{PCSCALEVAL} = 1 \cdot 10^8$$

Calculation of ambient temperature:

$$Ta = PTAT_{av} \cdot PTAT_{gradient} + PTAT_{offset} = 38152 \cdot 0.0211 + 2195.0\ dK = 3000\ dK$$

Compensation of thermal offset:

$$V_{00\_Comp} = V_{00} - \frac{ThGrad_{00} \cdot PTAT_{av}}{2^{gradScale}} - ThOffset_{00} = 34435 - \frac{11137 \cdot 38152}{2^{15}} - (-30) = 34439$$

Compensation of electrical offset:

$$V_{00\_Comp}{}^* = V_{00\_Comp} - elOffset[0] = 34439 - 34240 = 199$$

Compensation of supply voltage:

$$V_{ij\_VDDComp} = V_{ij\_Comp}{}^* - \frac{\left(\frac{VddCompGrad[0] \cdot PTAT_{av}}{2^{VddScGrad}} + VddCompOff[0]\right) \cdot (Vdd - VddCalib)}{2^{VddScOff}}$$

$$= 199 - \frac{\left(\frac{10356 \cdot 38152}{2^{16}} - 14146\right) \cdot (35000 - 33942)}{2^{23}} = 199 - (-1) = 200$$

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Example look-up table:

TA[dK]/dig	2882	3032	3182	3332
-64	1494	2128	2491	2775
-32	2466	2692	2898	3091
0	2882	3032	3182	3332
32	3170	3285	3406	3530
64	3396	3491	3592	3699
96	3584	3665	3754	3848
128	3746	3818	3897	3981
160	3890	3954	4025	4102
192	4019	4078	4143	4214
224	4137	4191	4251	4317
256	4246	4296	4351	4413
288	4347	4393	4445	4503
320	4441	4485	4534	4588

$$V_{00\_PixC} = \frac{200 \cdot 1 \cdot 10^8}{1.1 \cdot 10^8} = 182$$

Ta was calculated before to 3000 dK.
The matching region in the look-up table is already marked yellow, the bi-linear interpolation is leading to an object temperature of 3941 dK = 120.9 °C.
A global Offset (GlobalOff) is used for fine tuning of the measured object temperature and has to be added to the object temperature. This value is stored in the EEPROM.

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9.7 Pixel Masking

A maximum of 5 defect Pixels are allowed on the complete array, this means that at least 99.5 % of the Pixels are working correctly. The amount of defect Pixels is given in the EEPROM at address 0x007F and is named *NrOfDefPix* . *DeadPixAdr* is the address of the defect Pixels and *DeadPixMask* determines the neighbours that should be used for masking the pixel. A simple averaging of all selected nearest neighbours is done to overwrite the temperature value of these Pixel.
The order of the top and bottom half is the same as the readout order that is stated in 5. The neighbours to use is given in a binary format and the order is shown in the overview below in decimal and binary values for the top and bottom half.

top half

128	1	2
64	DeadPix	4
32	16	8

0b1000 0000	0b0000 0001	0b0000 0010
0b0100 0000	DeadPix	0b0000 0100
0b0010 0000	0b0001 0000	0b0000 1000

bottom half

32	16	8
64	DeadPix	4
128	1	2

0b0010 0000	0b0001 0000	0b0000 1000
0b0100 0000	DeadPix	0b0000 0100
0b1000 0000	0b0000 0001	0b0000 0010

Example values for the masking:

$NrOfDefPix = 0x03$
$DeadPixAdr[0] = 0x000F \rightarrow \text{Pixel } 15$
$DeadPixAdr[1] = 0x012C \rightarrow \text{Pixel } 300$
$DeadPixAdr[2] = 0x0295 \rightarrow \text{Pixel } 661$
$DeadPixMask[0] = 0x7C \rightarrow 0b01111100\,(\text{top})$
$DeadPixMask[1] = 0x8F \rightarrow 0b10001111\,(\text{top})$
$DeadPixMask[2] = 0xFE \rightarrow 0b11111110\,(\text{bot})$

According to the sample values 3 Pixels are defect and need to be interpolated. 2 Pixels are on the top and 1 Pixel on the bottom half. Assuming that the neighbouring Pixels are having the temperature data stated below and the green marked cells are used for averaging (according to DeadPixMask) then the interpolated temperature will be the following:

$$\text{Pixel } 15 = \frac{3007+3008+3008+3011+3009}{5} dK = \frac{15043}{5} dK \approx 3009 dK$$

$$\text{Pixel } 300 = \frac{3010+3012+3005+3008+3009}{5} dK = \frac{15044}{5} dK \approx 3009 dK$$

$$\text{Pixel } 661 = \frac{3010+3012+3005+3007+3008+3008+3009}{7} dK = \frac{21059}{7} dK \approx 3008 dK$$

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All values are given in dK

3007	Pixel 15	3008
3008	3011	3009

Pixel 14	Pixel 15	Pixel 16
Pixel 46	Pixel 47	Pixel 48

3010	3012	3005
3007	Pixel 300	3008
3008	3011	3009

Pixel 267	Pixel 268	Pixel 269
Pixel 299	Pixel 300	Pixel 301
Pixel 331	Pixel 332	Pixel 333

3010	3012	3005
3007	Pixel 661	3008
3008	3011	3009

Pixel 628	Pixel 629	Pixel 630
Pixel 660	Pixel 661	Pixel 662
Pixel 692	Pixel 693	Pixel 694

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9.8 Look-up Table

The matching look-up table has to be taken from the Table.c file. Here is just shown an exemplary data for one optics.

dig \ Ta[dK]	2782	2882	2982	3082	3182	3282	3382
-512				1295	1742	2005	2202
-448		To in dK		1848	2094	2284	2442
-384				2156	2340	2496	2634
-320				2381	2534	2671	2797
-256	2042	2244	2414	2562	2697	2822	2938
-192	2287	2445	2587	2717	2839	2954	3065
-128	2481	2612	2735	2852	2964	3073	3180
-64	2642	2755	2865	2972	3078	3182	3285
0	2782	2882	2982	3082	3182	3282	3382
64	2906	2996	3089	3183	3278	3375	3473
128	3019	3101	3187	3276	3368	3462	3558
192	3121	3197	3278	3363	3452	3544	3638
256	3216	3286	3363	3445	3531	3621	3715
320	3305	3370	3443	3522	3606	3695	3787
384	3387	3449	3519	3595	3677	3764	3856
448	3465	3524	3590	3664	3745	3831	3922
512	3539	3595	3659	3731	3810	3895	3986
576	3609	3662	3724	3794	3872	3957	4047
640	3676	3727	3787	3855	3932	4016	4106
704	3740	3788	3847	3914	3990	4073	4163
768	3802	3848	3904	3971	4046	4128	4218
832	3861	3905	3960	4025	4100	4182	4271
896	3918	3960	4014	4078	4152	4233	4322
960	3973	4014	4066	4129	4202	4284	4372
1024	4026	4065	4117	4179	4251	4332	4421
1088	4077	4115	4166	4227	4299	4380	4469
1152	4127	4164	4213	4274	4345	4426	4515
1216	4175	4211	4260	4320	4391	4471	4560
1280	4222	4257	4305	4364	4435	4515	4604
1344	4268	4302	4349	4408	4478	4558	4647
1408	4312	4345	4391	4450	4520	4600	4689
1472	4355	4388	4433	4491	4561	4641	4730
1536	4398	4429	4474	4532	4601	4681	4770
1600	4439	4470	4514	4571	4640	4720	4809
1664	4480	4509	4553	4610	4679	4758	4848
1728	4519	4548	4591	4648	4716	4796	4885
1792	4558	4586	4629	4685	4753	4833	4922
1856	4595	4623	4666	4721	4790	4869	4959
1920	4633	4660	4702	4757	4825	4905	4995
1984	4669	4696	4737	4792	4860	4940	5030
2048	4705	4731	4772	4826	4894	4974	5064
2112	4740	4765	4806	4860	4928	5008	5098
2176	4774	4799	4839	4894	4961	5041	5131
2240	4808	4832	4872	4926	4994	5074	5164
2304	4841	4865	4904	4958	5026	5106	5197
2368	4873	4897	4936	4990	5058	5137	5228
2432	4906	4929	4968	5021	5089	5169	5260
2496	4937	4960	4998	5052	5119	5199	5291
2560	4968	4991	5029	5082	5149	5230	5321
2624	4999	5021	5059	5112	5179	5259	5351
2688	5029	5050	5088	5141	5208	5289	5381
2752	5059	5080	5117	5170	5237	5318	5410
2816	5088	5109	5146	5199	5266	5346	5439
2880	5117	5137	5174	5227	5294	5375	5467
2944	5145	5165	5202	5255	5322	5402	5495
3008	5173	5193	5230	5282	5349	5430	5523
3072	5201	5220	5257	5309	5376	5457	5550
3136	5228	5247	5284	5336	5403	5484	5577
3200	5255	5274	5310	5362	5429	5510	5604
3264	5282	5300	5336	5388	5455	5537	5630
3328	5308	5326	5362	5414	5481	5563	5656
3392	5334	5352	5388	5439	5507	5588	5682
3456	5360	5377	5413	5465	5532	5613	5708
3520	5385	5403	5438	5489	5557	5638	5733
3584	5410	5427	5462	5514	5581	5663	5758
3648	5435	5452	5487	5538	5606	5688	5783
3712	5459	5476	5511	5562	5630	5712	5807
3776	5483	5500	5535	5586	5654	5736	5831
3840	5507	5524	5558	5610	5677	5760	5855
3904	5531	5547	5582	5633	5701	5783	5879
3968	5554	5571	5605	5656	5724	5806	5902
4032	5578	5594	5628	5679	5747	5829	5925
4096	5601	5616	5650	5702	5769	5852	5948
4160	5623	5639	5673	5724	5792	5875	5971
4224	5646	5661	5695	5746	5814	5897	5994
4288	5668	5683	5717	5768	5836	5919	6016
4352	5690	5705	5739	5790	5858	5941	6038
4416	5712	5727	5760	5811	5879	5963	6060
4480	5734	5748	5782	5833	5901	5984	6082
4544	5755	5770	5803	5854	5922	6006	6103
4608	5776	5791	5824	5875	5943	6027	6125
4672	5797	5811	5844	5896	5964	6048	6146
4736	5818	5832	5865	5916	5984	6069	6167
4800	5839	5853	5886	5937	6005	6089	6188
4864	5859	5873	5906	5957	6025	6110	6208
4928	5880	5893	5926	5977	6045	6130	6229
4992	5900	5913	5946	5997	6065	6150	6249
5056	5920	5933	5965	6017	6085	6170	6269
5120	5940	5953	5985	6036	6105	6190	6289
5184	5959	5972	6005	6056	6124	6209	6309
5248	5979	5991	6024	6075	6144	6229	6329
5312	5998	6011	6043	6094	6163	6248	6348
5376	6017	6030	6062	6113	6182	6267	6368
5440	6036	6049	6081	6132	6201	6286	6387
5504	6055	6067	6099	6150	6220	6305	6406
5568	6074	6086	6118	6169	6238	6324	6425
5632	6092	6104	6136	6187	6257	6343	6444
5696	6111	6123	6155	6206	6275	6361	6462
5760	6129	6141	6173	6224	6293	6379	6481
5824	6147	6159	6191	6242	6311	6398	6499
5888	6165	6177	6209	6260	6329	6416	6517
5952	6183	6195	6226	6277	6347	6434	6536
6016	6201	6212	6244	6295	6365	6451	6554
6080	6219	6230	6261	6313	6382	6469	6571
6144	6236	6247	6279	6330	6400	6487	6589
6208	6253	6264	6296	6347	6417	6504	6607
6272	6271	6282	6313	6364	6434	6522	6624
6336	6288	6299	6330	6381	6451	6539	6642
6400	6305	6316	6347	6398	6468	6556	6659
6464	6322	6332	6364	6415	6485	6573	6676
6528	6339	6349	6380	6432	6502	6590	6693
6592	6355	6366	6397	6448	6519	6607	6710
6656	6372	6382	6413	6465	6535	6623	6727
6720	6388	6399	6430	6481	6552	6640	6744
6784	6405	6415	6446	6497	6568	6656	6761

6848	6421	6431	6462	6514	6584	6673	6777
6912	6437	6447	6478	6530	6600	6689	6794
6976	6453	6463	6494	6546	6616	6705	6810
7040	6469	6479	6510	6562	6632	6721	6826
7104	6485	6495	6526	6577	6648	6737	6842
7168	6501	6511	6542	6593	6664	6753	6858
7232	6517	6526	6557	6609	6680	6769	6874
7296	6532	6542	6573	6624	6695	6785	6890
7360	6548	6557	6588	6640	6711	6800	6906
7424	6563	6572	6603	6655	6726	6816	6922
7488	6578	6588	6618	6670	6742	6831	6937
7552	6594	6603	6634	6685	6757	6847	6953
7616	6609	6618	6649	6700	6772	6862	6968
7680	6624	6633	6664	6715	6787	6877	6984
7744	6639	6648	6678	6730	6802	6892	6999
7808	6654	6663	6693	6745	6817	6907	7014
7872	6669	6677	6708	6760	6832	6922	7029
7936	6683	6692	6723	6774	6846	6937	7044
8000	6698	6707	6737	6789	6861	6952	7059
8064	6712	6721	6752	6803	6876	6966	7074
8128	6727	6735	6766	6818	6890	6981	7089
8192	6741	6750	6780	6832	6905	6996	7104
8256	6756	6764	6795	6847	6919	7010	7118
8320	6770	6778	6809	6861	6933	7024	7133
8384	6784	6792	6823	6875	6947	7039	7147
8448	6798	6806	6837	6889	6961	7053	7162
8512	6812	6820	6851	6903	6976	7067	7176
8576	6826	6834	6865	6917	6990	7081	7190
8640	6840	6848	6878	6931	7003	7095	7205
8704	6854	6862	6892	6944	7017	7109	7219
8768	6868	6875	6906	6958	7031	7123	7233
8832	6881	6889	6919	6972	7045	7137	7247
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8960	6908	6916	6946	6999	7072	7164	7275
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9088	6935	6943	6973	7026	7099	7192	7302
9152	6949	6956	6987	7039	7112	7205	7316
9216	6962	6969	7000	7052	7126	7219	7329
9280	6975	6983	7013	7065	7139	7232	7343
9344	6988	6996	7026	7079	7152	7245	7356
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9536	7028	7035	7065	7118	7191	7285	7396
9600	7040	7048	7078	7130	7204	7298	7410
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9792	7079	7086	7116	7169	7243	7337	7449
9856	7092	7098	7129	7181	7256	7350	7462
9920	7104	7111	7141	7194	7268	7363	7475
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10048	7129	7136	7166	7219	7294	7388	7501
10112	7142	7148	7179	7231	7306	7401	7513
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11520	7403	7409	7439	7493	7569	7666	7782
11584	7414	7420	7450	7504	7580	7677	7793
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11840	7459	7465	7495	7549	7625	7723	7840
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12096	7504	7509	7539	7593	7670	7768	7885
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12736	7612	7617	7647	7701	7779	7878	7996
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13056	7664	7669	7699	7754	7832	7931	8050
13120	7675	7679	7709	7764	7842	7942	8061
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13312	7706	7710	7740	7795	7873	7973	8093
13376	7716	7720	7751	7805	7884	7984	8104
13440	7726	7731	7761	7816	7894	7994	8114
13504	7736	7741	7771	7826	7904	8004	8125
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13632	7757	7761	7791	7846	7925	8025	8145
13696	7767	7771	7801	7856	7935	8035	8156
13760	7777	7781	7811	7866	7945	8046	8166
13824	7787	7791	7821	7876	7955	8056	8177
13888	7797	7801	7831	7886	7965	8066	8187
13952	7807	7811	7841	7896	7975	8076	8197
14016	7817	7821	7851	7906	7985	8086	8207
14080	7827	7831	7861	7916	7995	8096	8218
14144	7836	7841	7871	7926	8005	8106	8228
14208	7846	7851	7881	7936	8015	8116	8238

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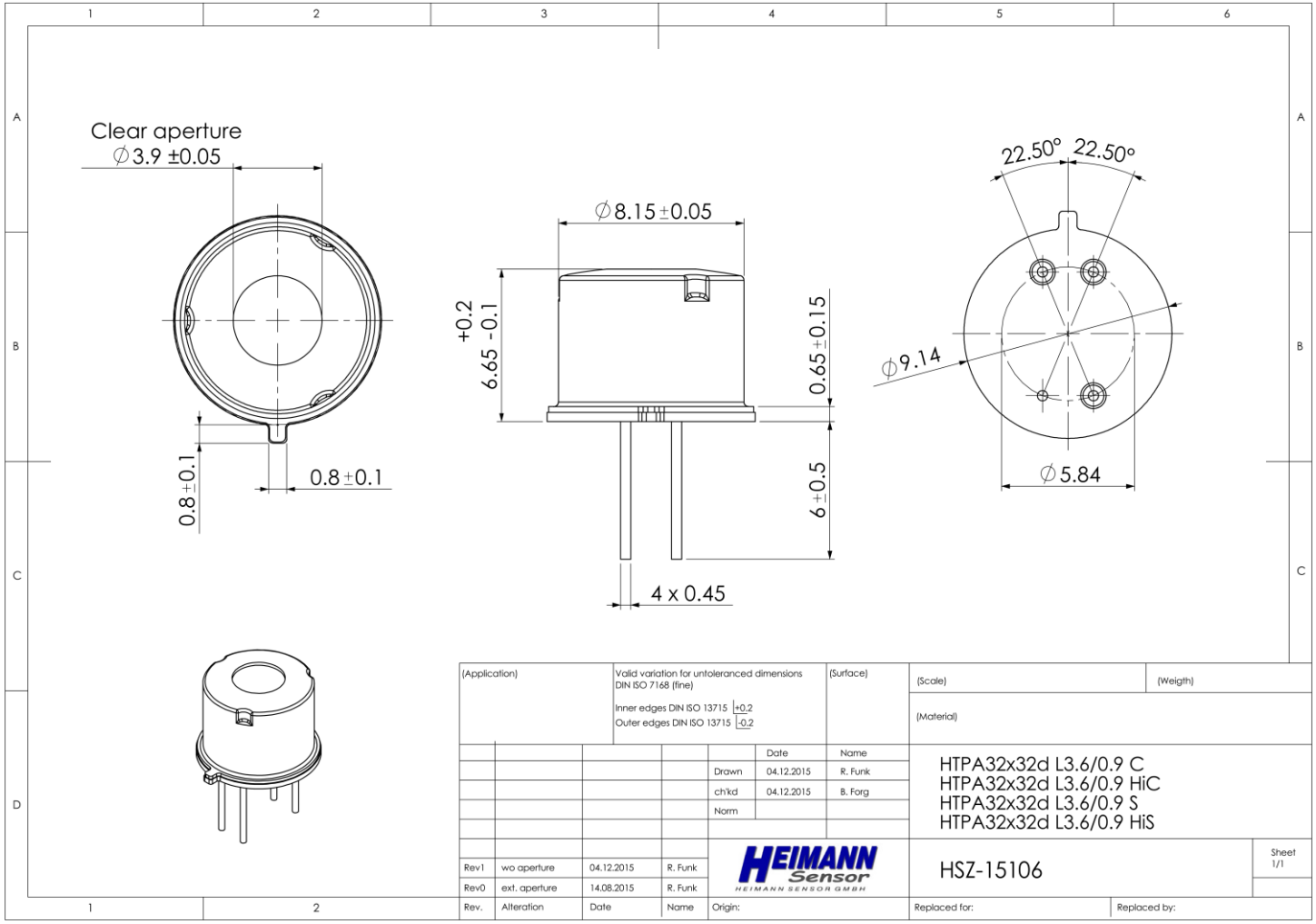
10 Outer Dimensions:



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