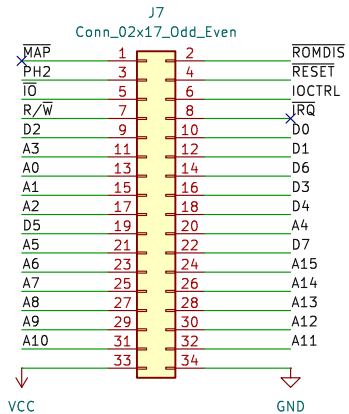
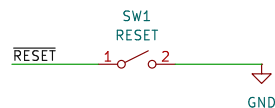


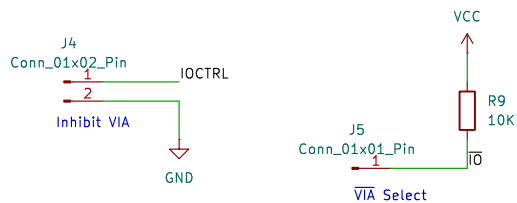
Expansion Connector



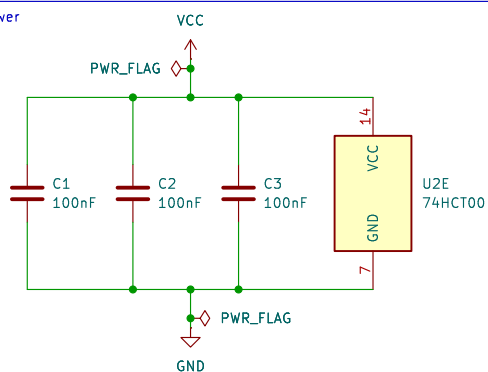
Reset



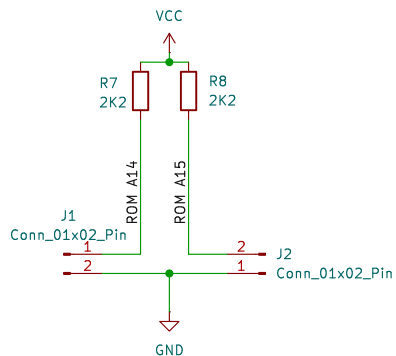
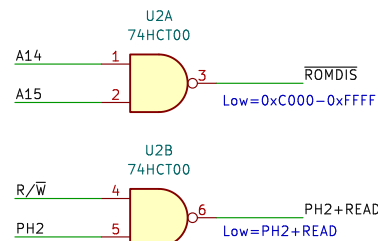
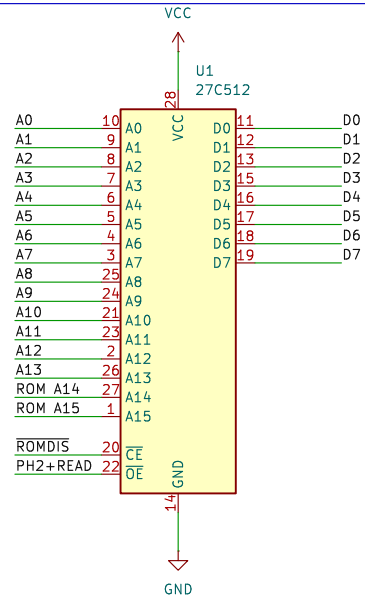
Test Points



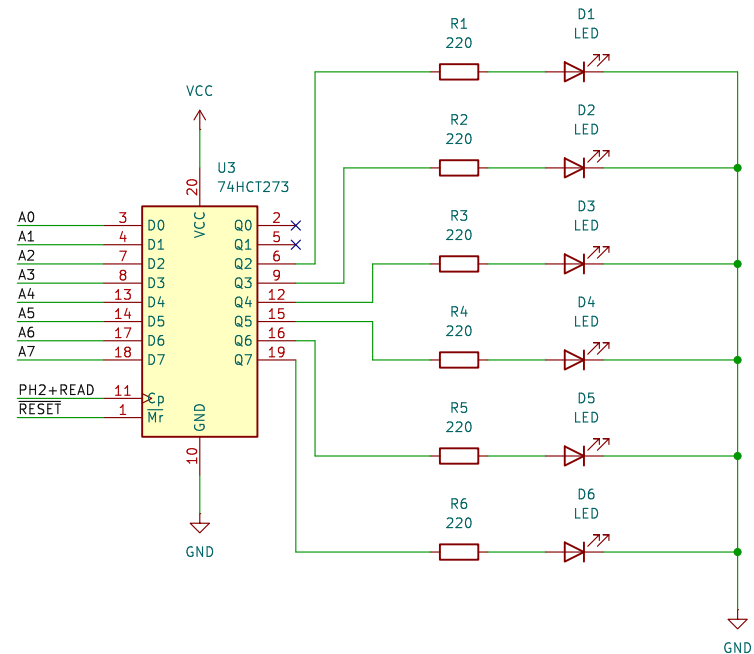
Power



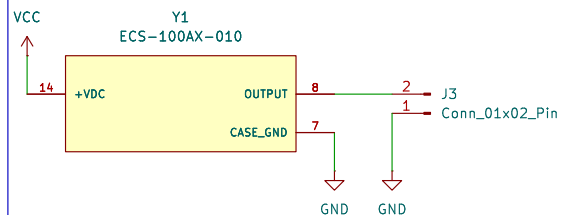
ROM



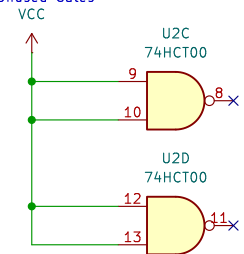
Status



1Mhz Test Oscillator



Unused Gates



This is a redrawn Schematic of Mike Brown's Oric Diag Board

**** NOT VERIFIED YET ****
**** DO NOT USE ****

Sheet: /
File: Oric_Diag.kicad_sch

Title: Oric Diag Board

Size: A4 Date: 2025-09-26

KiCad E.D.A. 9.0.4

Rev: 1

Id: 1/1