Americas

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Asia/Pacific

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China - Wuhan - 86-27-5980-5300 China - Xiamen - 86-592-2388138 China - Xian - 86-29-8833-7252 China - Zhuhai - 86-756-3210040 India - Bangalore - 91-80-3090-4444 India - New Delhi - 91-11-4160-8631 India - Pune - 91-20-2566-1512 Japan - Yokohama - 81-45-471-6166 Korea - Daegu - 82-53-744-4301 Korea - Seoul - 82-2-554-7200 Malaysia - Kuala Lumpur - 60-3-6201-9857

Malaysia - Penang - 60-4-227-8870 Philippines - Manila - 63-2-634-9065 Singapore - 65-6334-8870 Taiwan - Hsin Chu - 886-3-6578-300

Taiwan - Kaohsiung - 886-7-536-4818 Taiwan - Taipei - 886-2-2500-6610 Thailand - Bangkok - 66-2-694-1351

Europe

Austria - Weis - 43-7242-2244-39 Denmark - Copenhagen - 45-4450-2828 France - Paris - 33-1-69-53-63-20 Germany - Munich - 49-89-627-144-0 Italy - Milan - 39-0331-742611 Netherlands - Drunen - 31-416-690399 Spain - Madrid - 34-91-708-08-90 UK - Wokingham - 44-118-921-5869

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DS51912A

MRF24WB0MA PICtail™/PICtail Plus Daughter Board

Overview

The MRF24WB0MA PICtail/PICtail Plus Daughter Board is a development and demonstration board for evaluating Wi-Fi® connectivity using microcontrollers and the MRF24WB0MA module.

Features

- MRF24WB0MA Wi-Fi module with 802.11 connectivity
- Daughter board supports PICtail and PICtail Plus connectors (See reverse side for schematic).

Getting Started

To get started, an Explorer 16 or PICDEM.net™ 2 development board is required. Alternatively, a PIC32 Starter Kit with a PIC32 I/O Expansion Board can be used. With the appropriate hardware available, download the Microchip TCP/IP Stack and follow the Getting Started chapter of the TCP/IP Stack Help.

TCP/IP Stack and Firmware

The Microchip TCP/IP stack and other firmware examples can be downloaded with the Microchip Application Libraries at http://www.microchip.com/mal. Support for the MRF24WB0MA PICtail/PICtail Plus Daughter Board is in TCP/IP Stack version 5.25 and later (Microchip Application Libraries v2010-04-28 or later). Once installed, a link to the TCP/IP Stack Help will be placed in your Start Menu.

TABLE 1: SIGNAL INTERFACE

Function	I/O	PICtail™ Pin	PICtail Plus Pin	Description
CS	I	RC2	RB2	SPI Chip Select
SCK	ı	RC3/SCK	RF6/SCK1	SPI Clock
SDO	0	RC4/SDI	RF7/SDI1	SPI Data Out from MRF24WB0MA
SDI	ı	RC5/SDO	RF8/SDO1	SPI Data In to MRF24WB0MA
ĪNT	0	RB0INT0	RE8/INT1	Open Drain Interrupt Signal to Microcontroller
RESET	I	RB1	RF0	Reset Signal to MRF24WB0MA
HIBERNATE	I	RB2	RF1	Hibernate Signal to MRF24WB0MA

Wireless Router Configuration

The Microchip TCP/IP stack examples are written to interface to a wireless router with the following configuration:

SSID: MicrochipDemoAP

Channel: 1, 6, or 11 Security: Disabled

These can be changed in the file WF_Config.h.

Media Access Control (MAC) Address

The TCP/IP stack is preconfigured to use the preprogrammed MAC address in the MRF24WB0MA. This address is also printed on the MRF24WB0MA module label.

MRF24WB0MA PICtail™/PICtail Plus Daughter Board

Board Schematic

