1.7 Section See Charles





Lit Instruction Centre Warp Scheduler (12 Proced/Uk) Dispatch Unit (32 thread/clk) Register File (16,364 x 32-bit)						
FPSA FPSA FPSA FPSA FPSA FPSA FPSA FPSA	FPSA INT INT FFSE FFSE					

Warp Scheduler (32 threedick) Dispatch Unit (32 threedick)							
Register File (15,364 x 32-bit)							
FPS4	1917 1917	PH PH					
PP\$4	167 167	eraz eraz					
FPS4	197 107	FP10 FP11	TENSOR CORE	TENSOR CORE			
FPSA	INT INT	res res					
FPHA	100	rem rem					
FFEE	INT INT	FF12 FF12					
FPSA	1917 1917	F713 FF11					
FP64	1617 INT	**12 1113					
MY M	LIR LIB ST ST	110 110 51 87	LES LOS	sru			

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