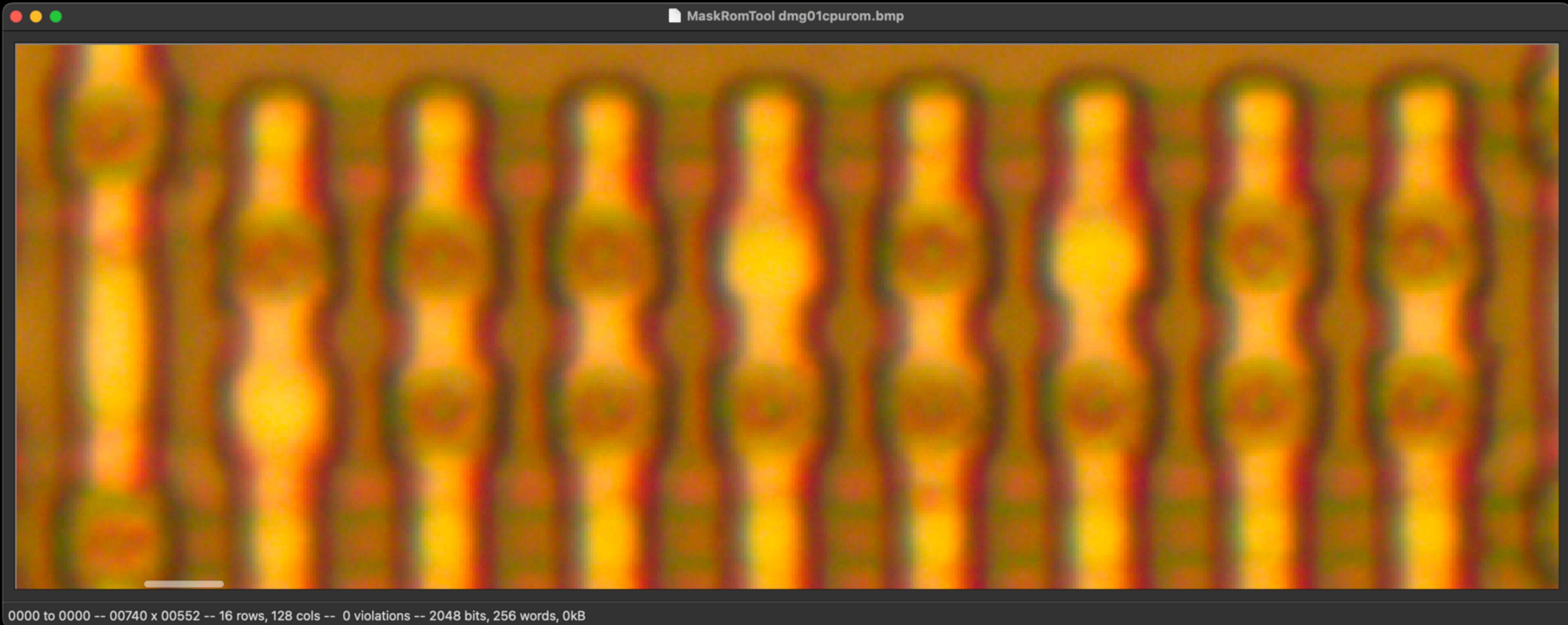
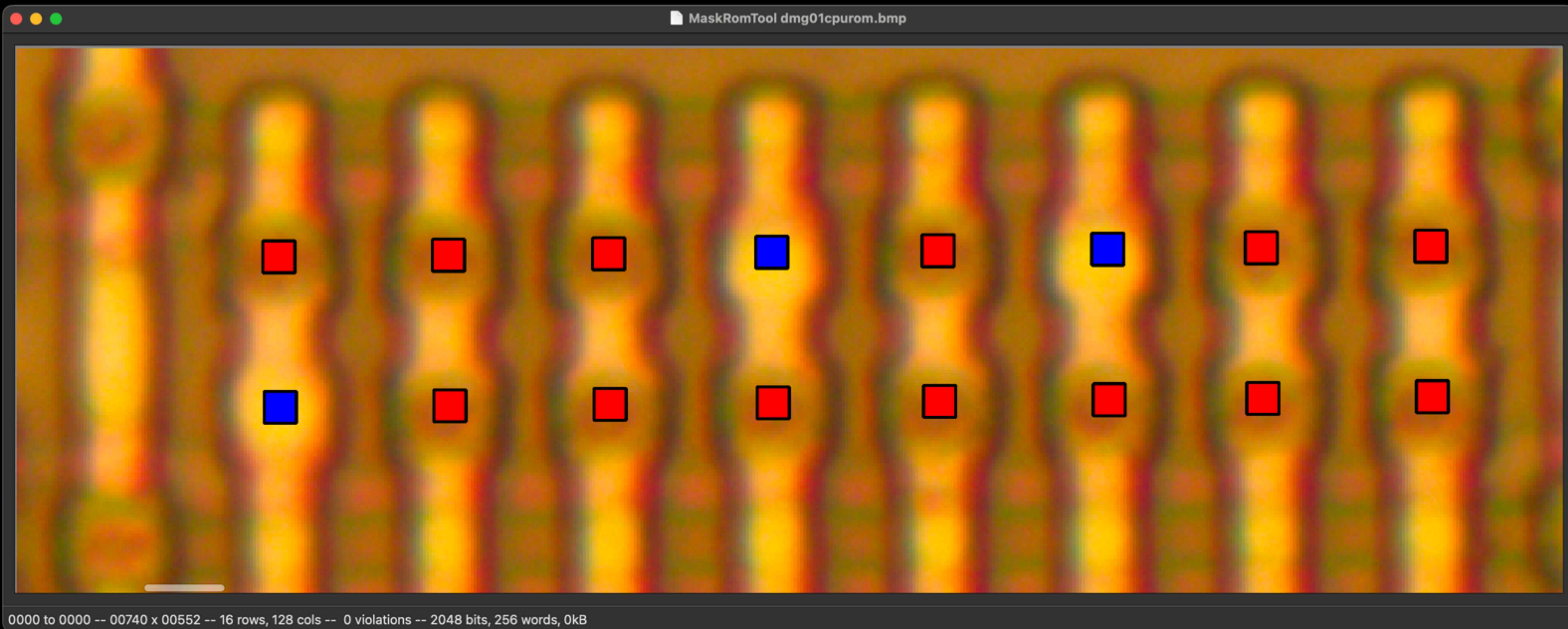


# GameBoy ROM Extraction!



# GameBoy ROM Extraction!



# GameBoy ROM Extraction

- First, some theory:
  - What's a mask ROM?
  - How can we identify thousands of bits?
  - How can we convert those bits to bytes?
- Second, some practice:
  - Nintendo's Game Boy ROM: 2048 bits.

# Software and target!

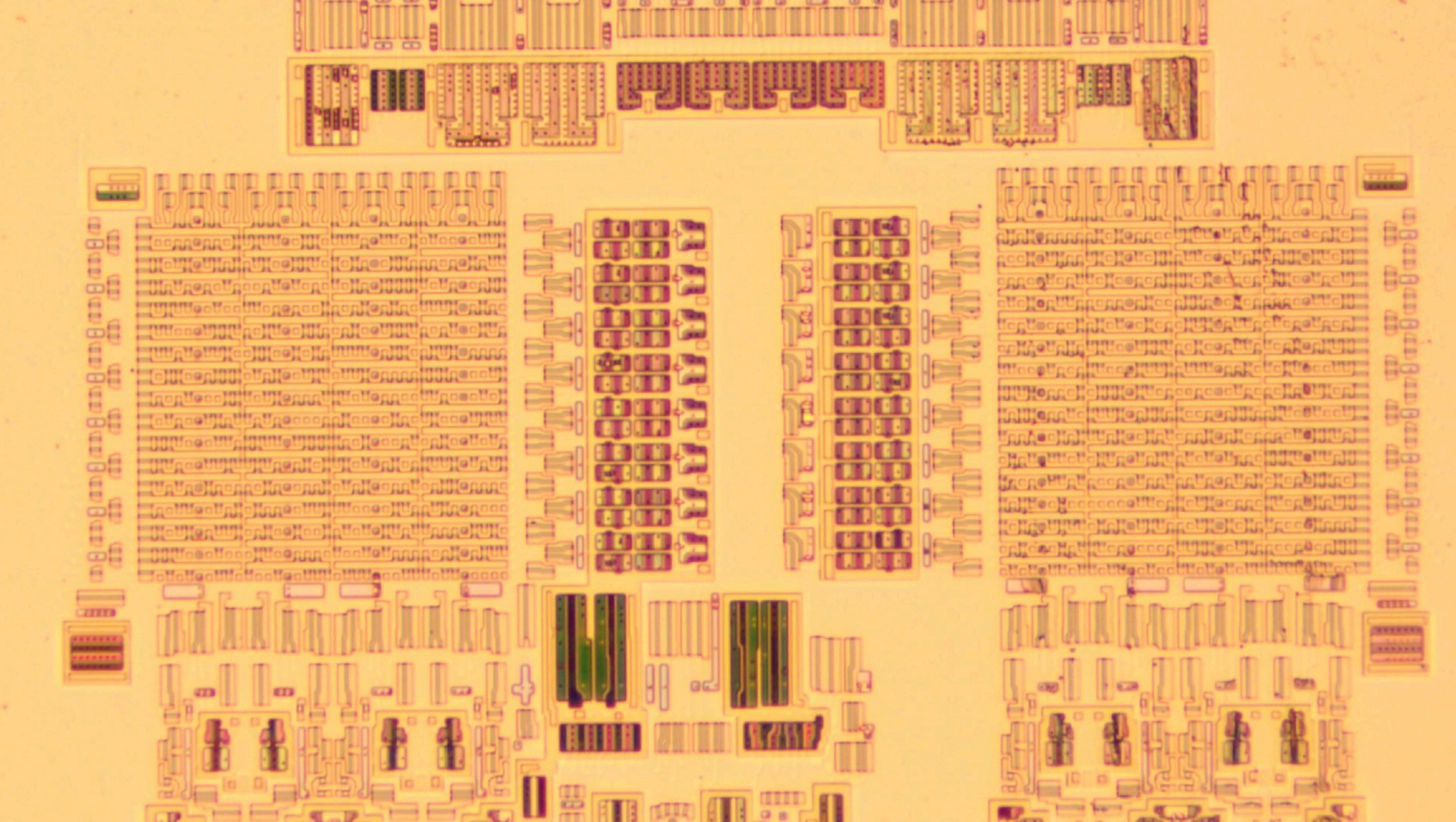
- <https://github.com/travisgoodspeed/maskromtool/>
  - Latest release for Windows or macOS.
  - Build from source with Qt6 for Linux.
- <https://github.com/travisgoodspeed/gbrom-tutorial>
  - Clone this locally, follow instructions in README.
- WIFI: UPCguest

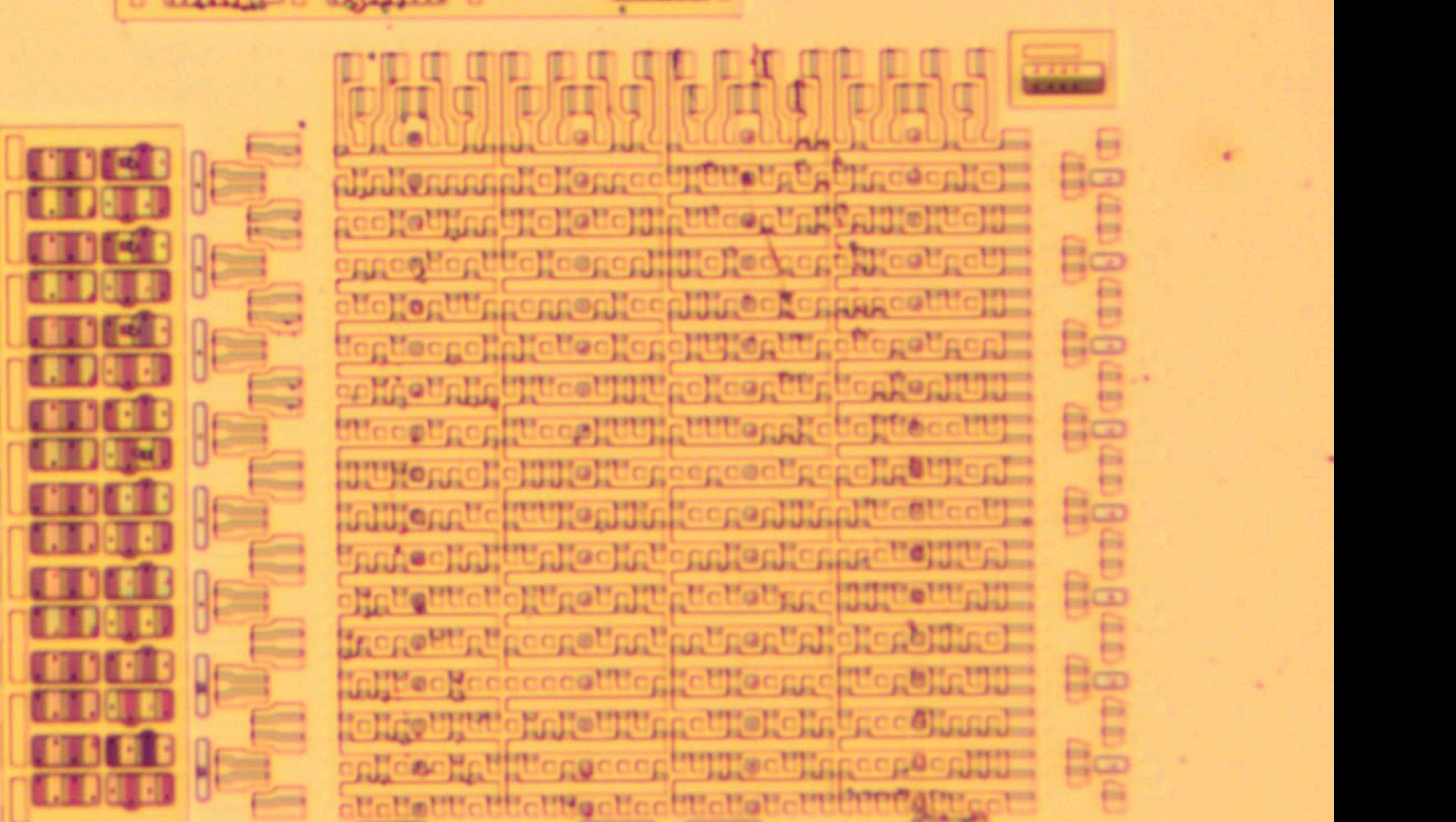
# Flash ROM, EEPROM, and Mask ROM

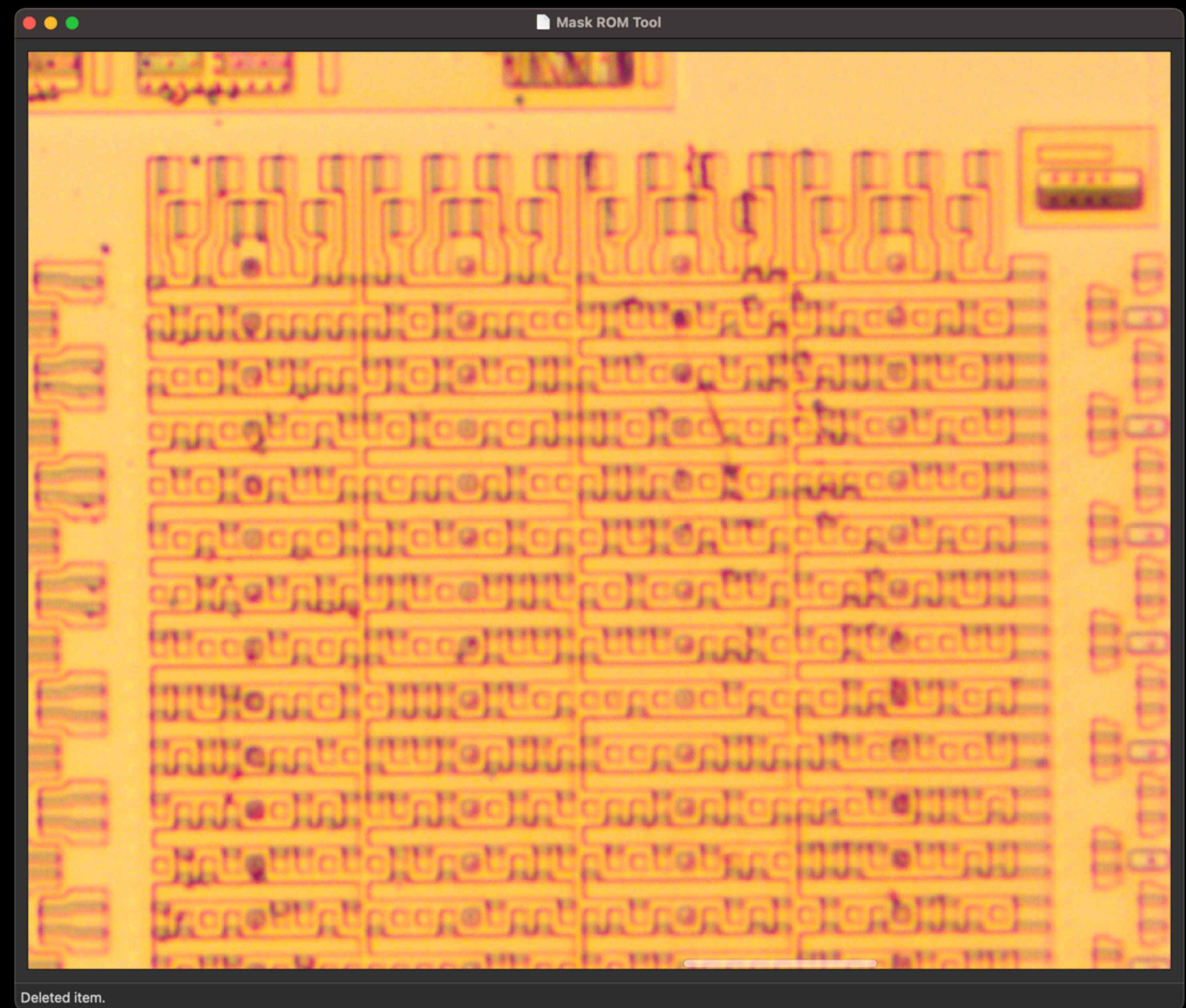
- Flash ROM and EEPROM
  - Electrically, Individually Programmable
  - Great for small quantities of code.
- Mask ROM
  - Mask Programmed
  - Only for very large quantites.

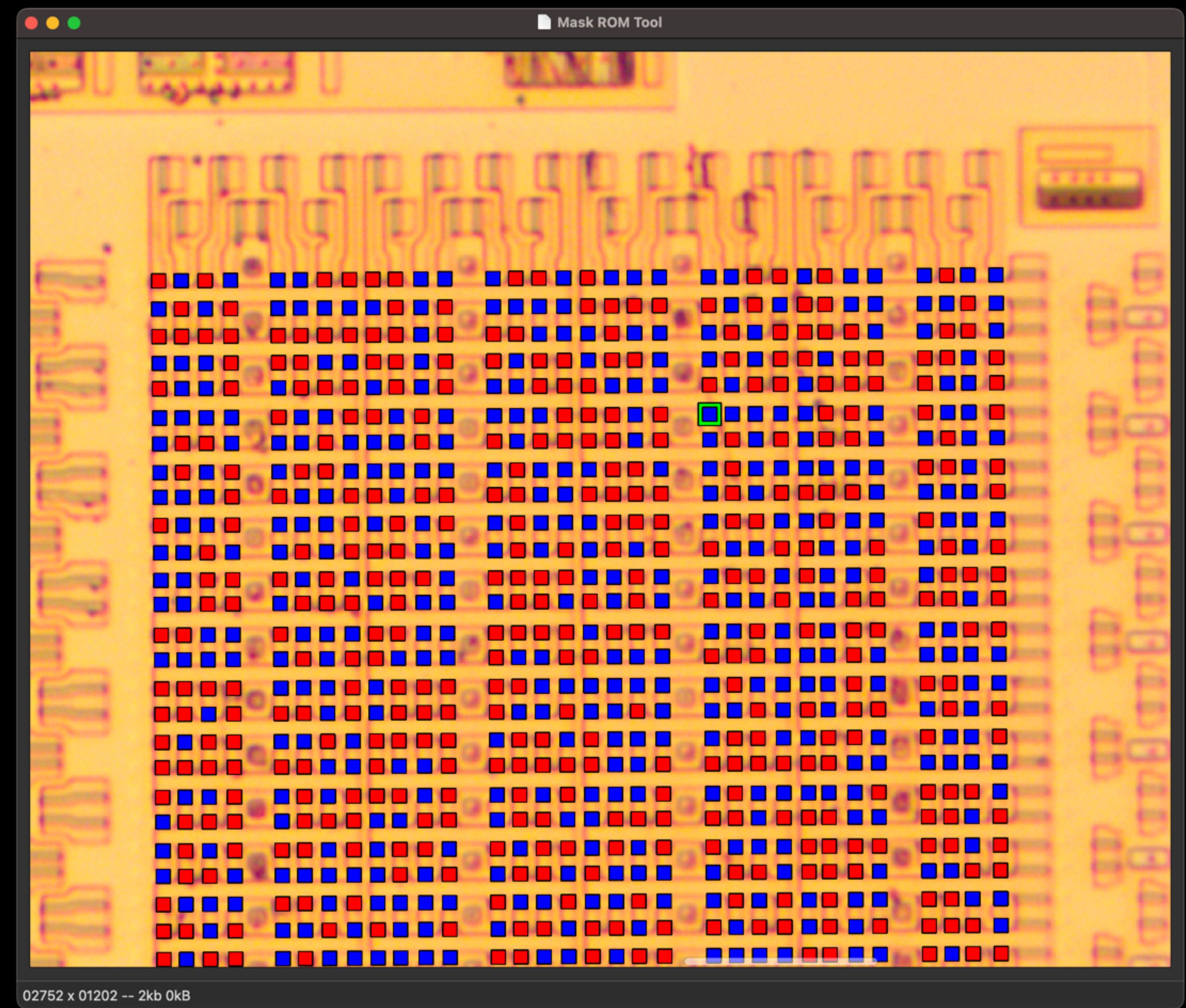
# Mask ROMS

- Mask Programmed at the Factory
- Contain code, data, or microcode.
- Often require chemical processing.
- Good targets for reverse engineering:
  - Video Games, Copy Protection
  - Cryptography









# A Sidebar into Chemistry

- Depackage the chip with 65% HNO<sub>3</sub> and heat.
- Delayer the chip with HF.
- Stain the ROM with a Dash Etch.



OFF  
0  
100%  
LIGHT INTENSITY CONTROL











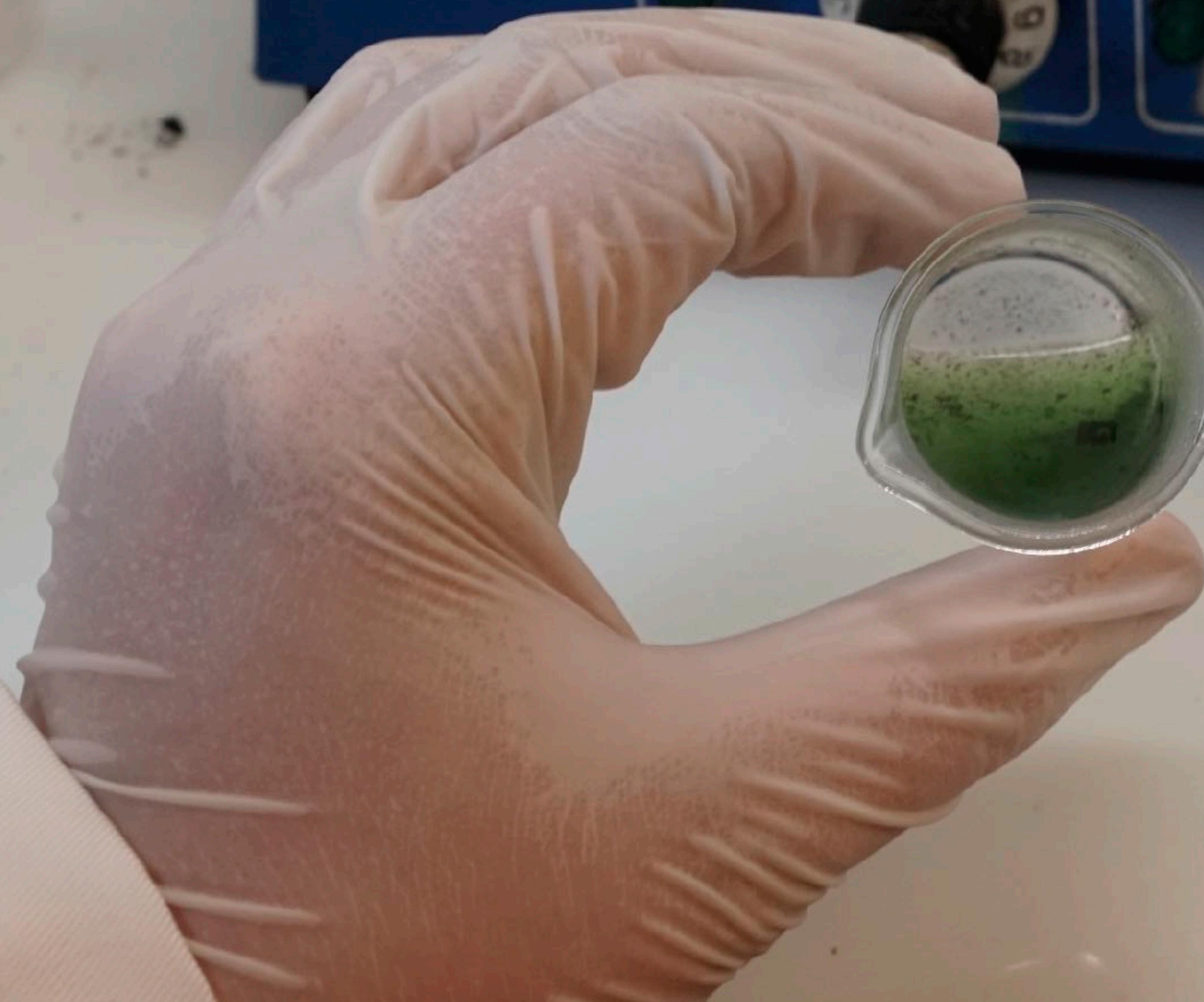
VWR  
MODEL 320

STIR

10  
0  
10  
9  
8  
7  
6  
5  
4  
3  
2  
1

HI

6 5 4  
3 2 1

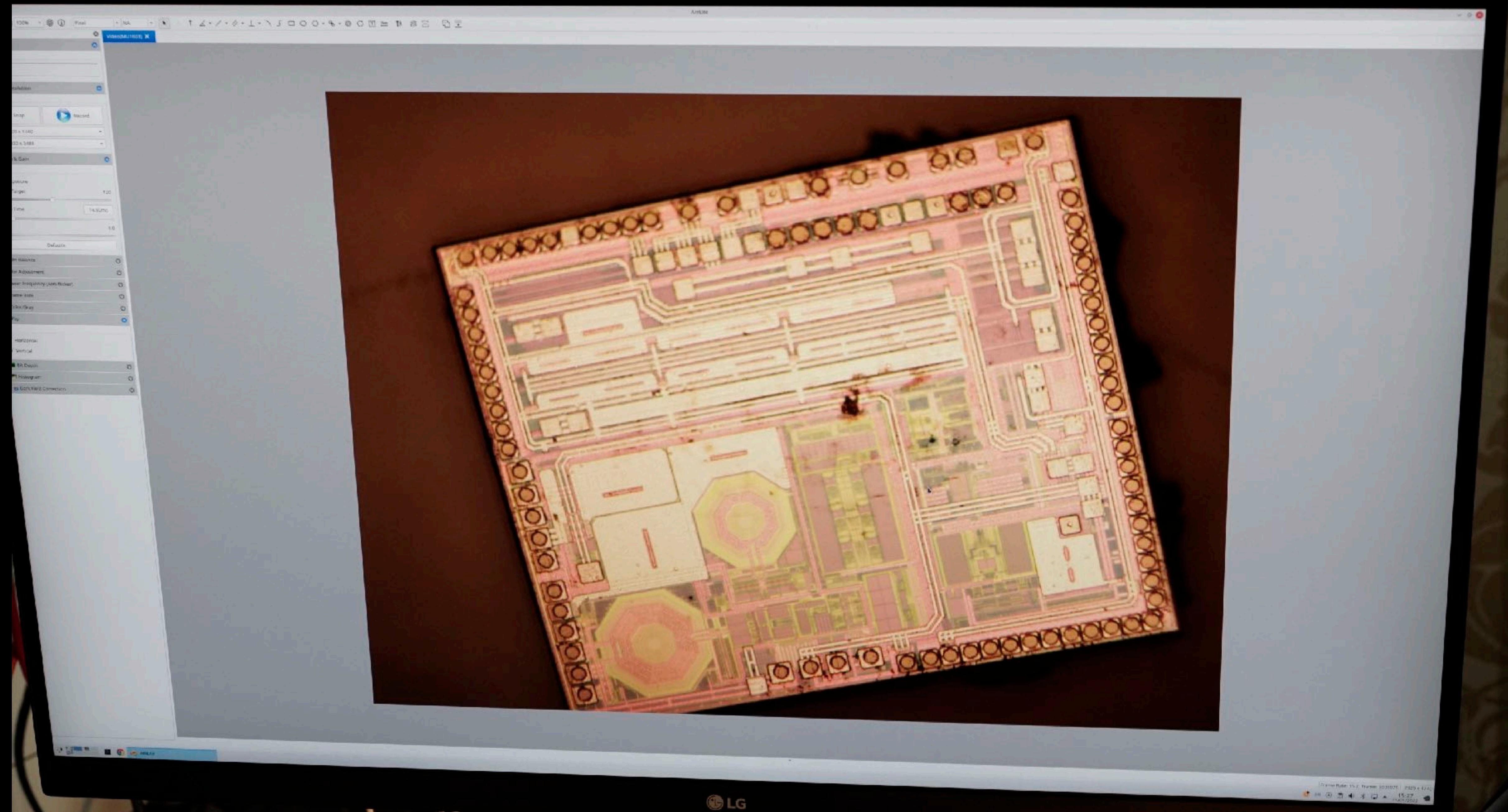


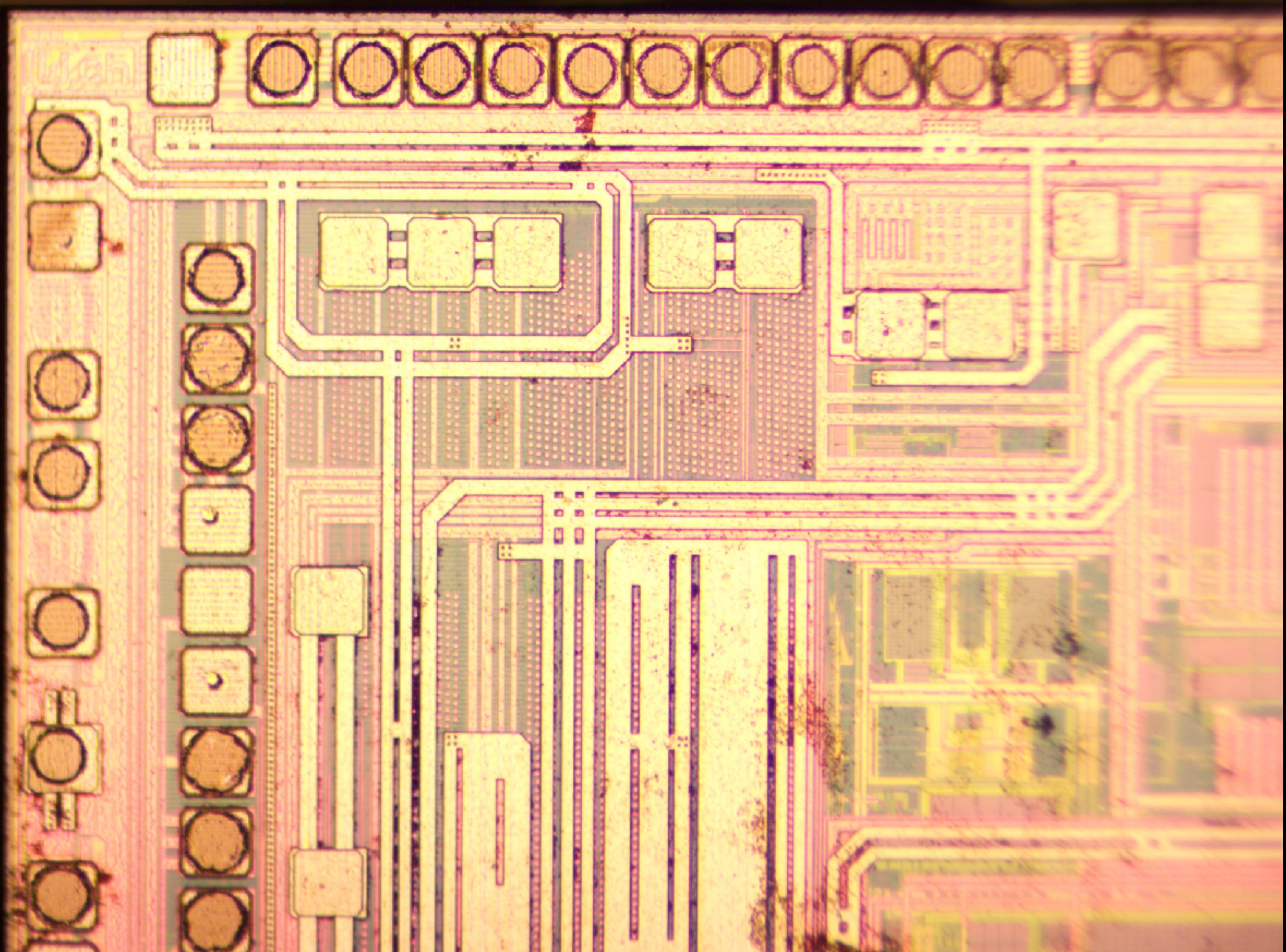


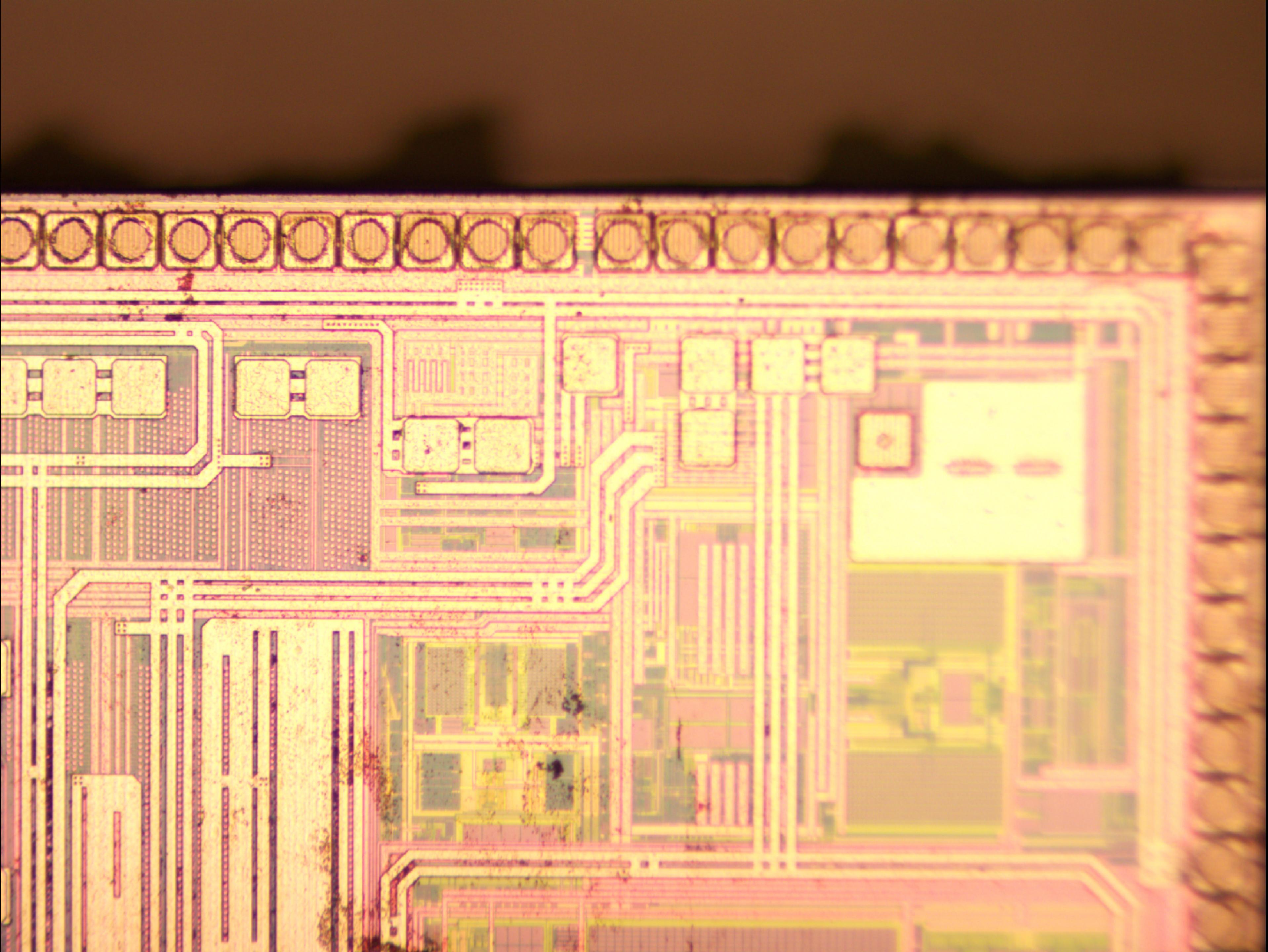


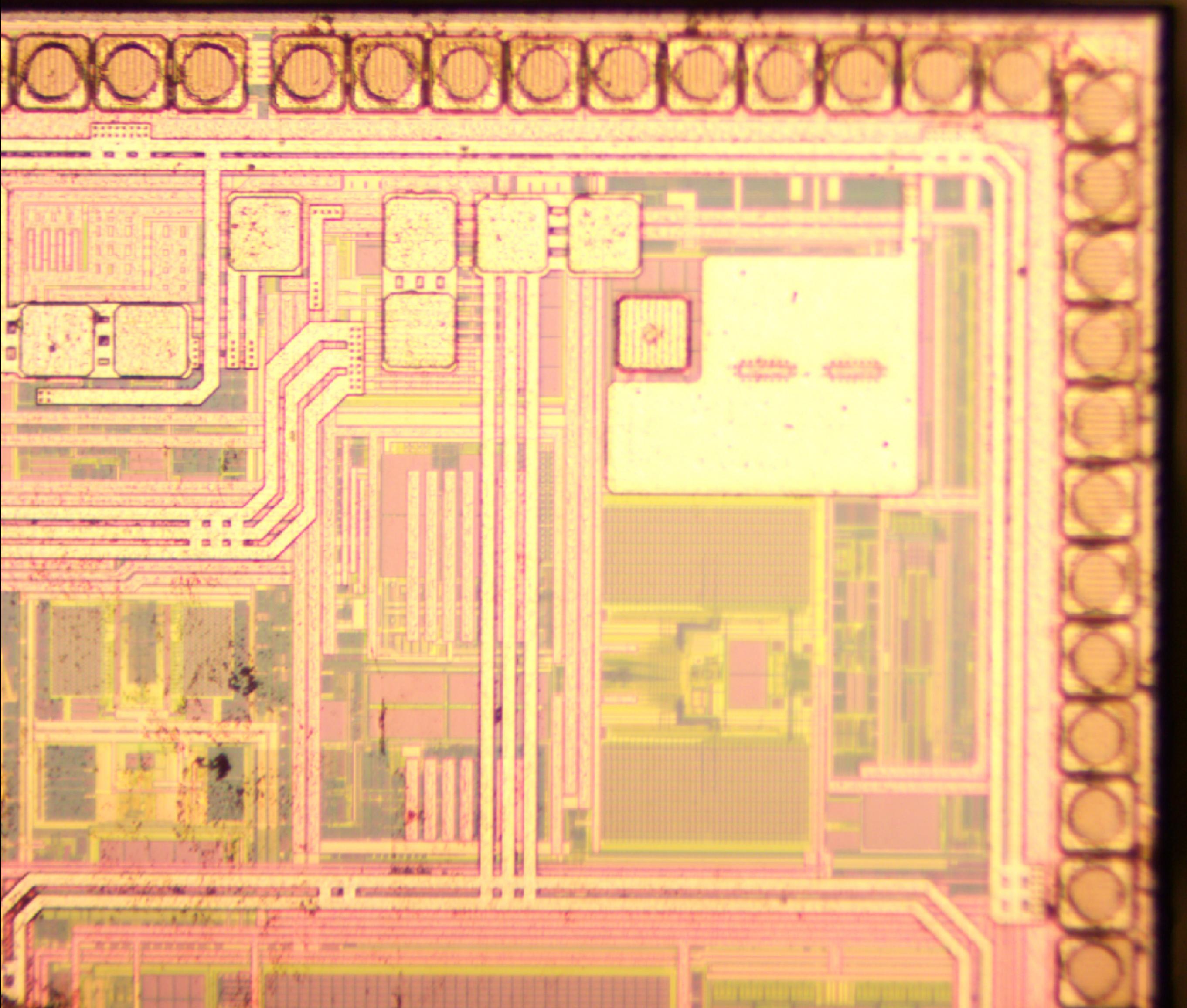


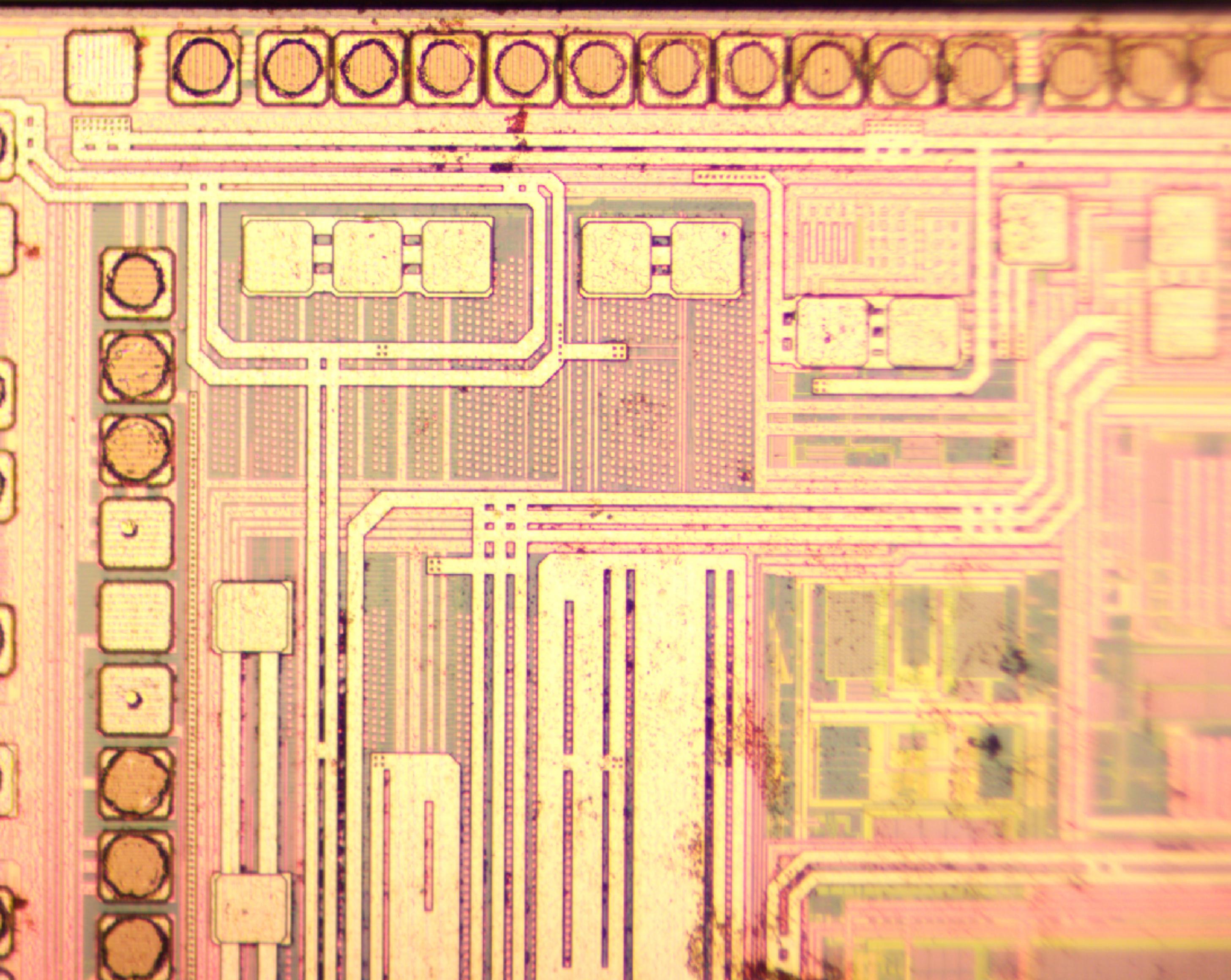


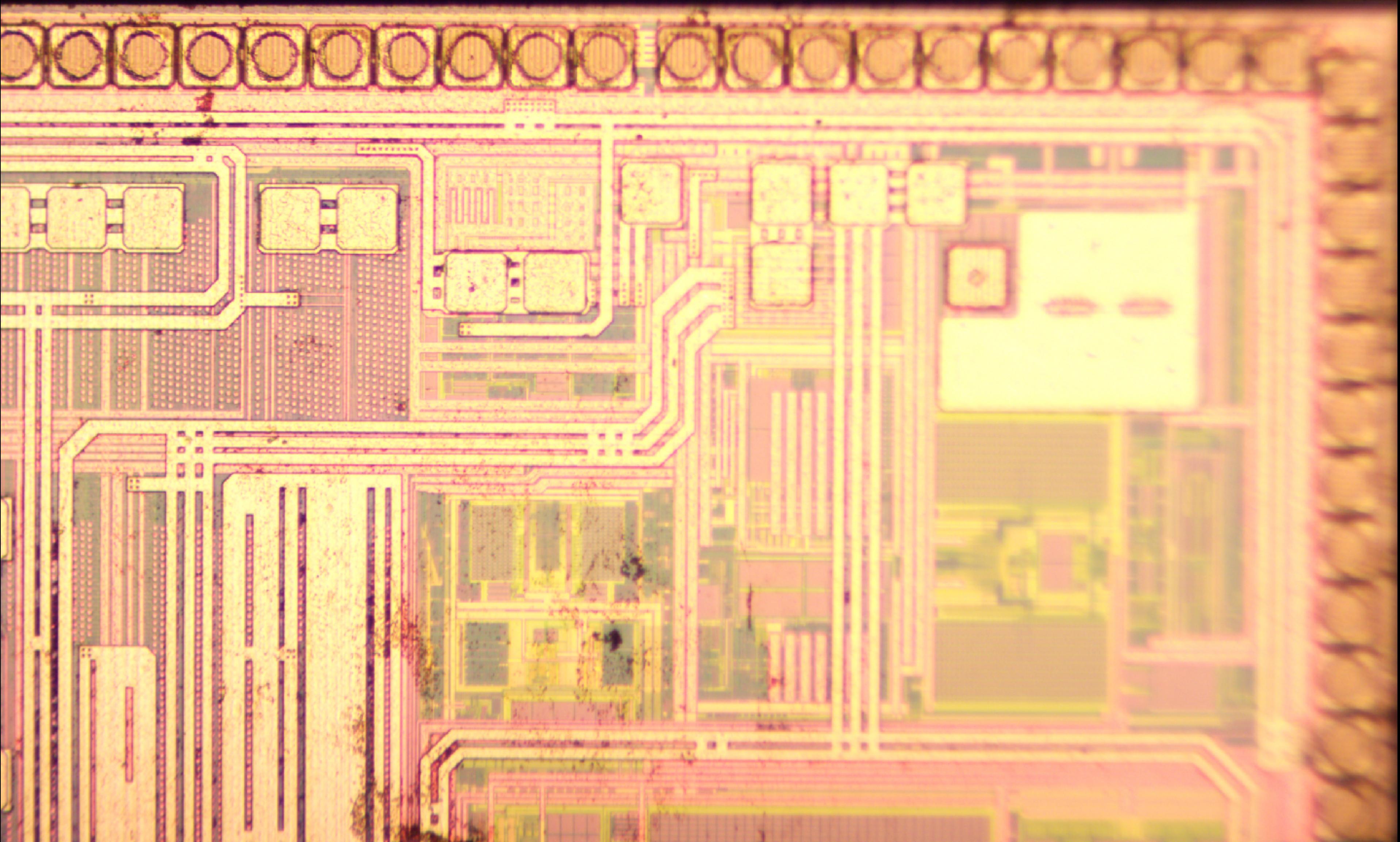


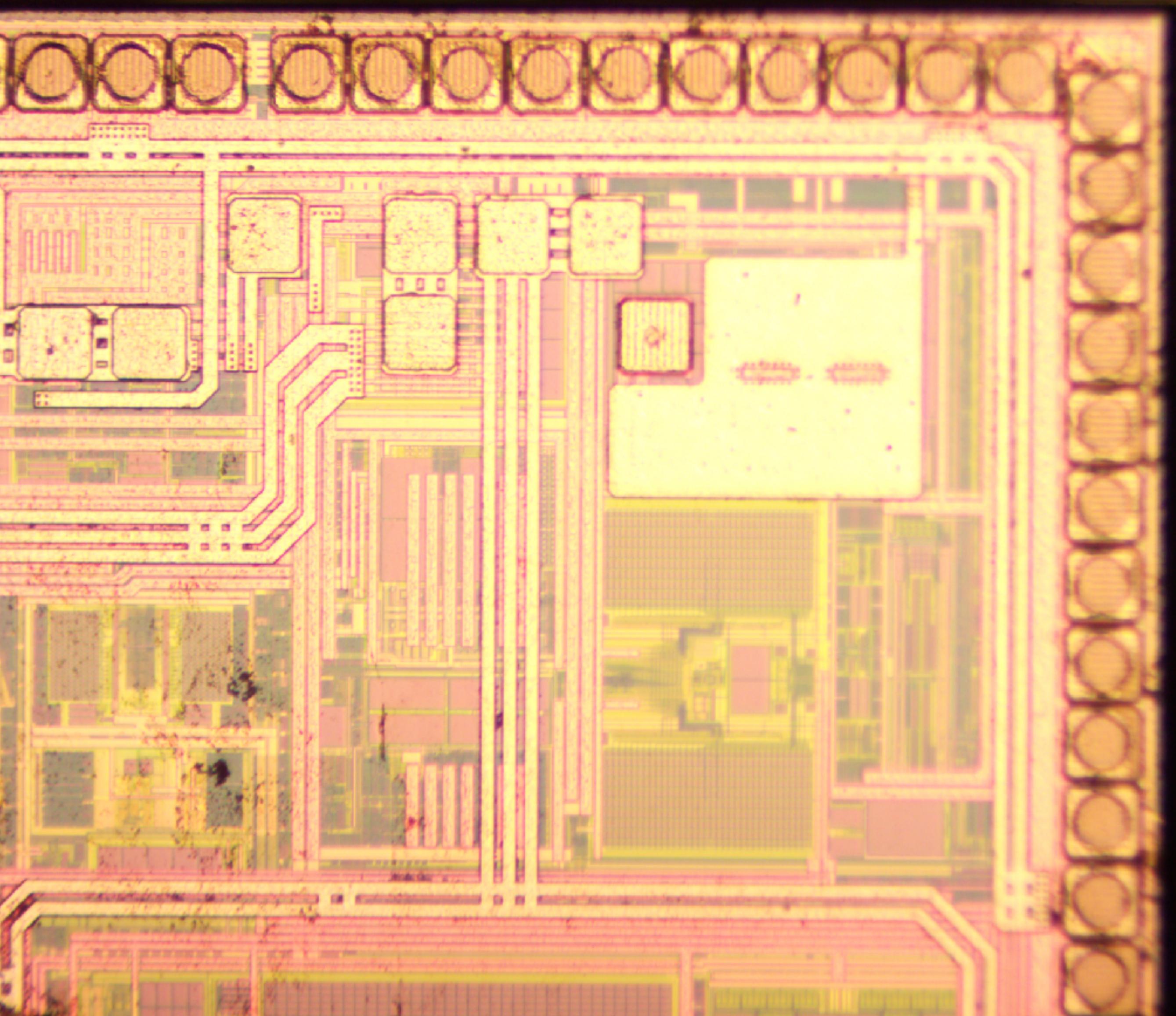


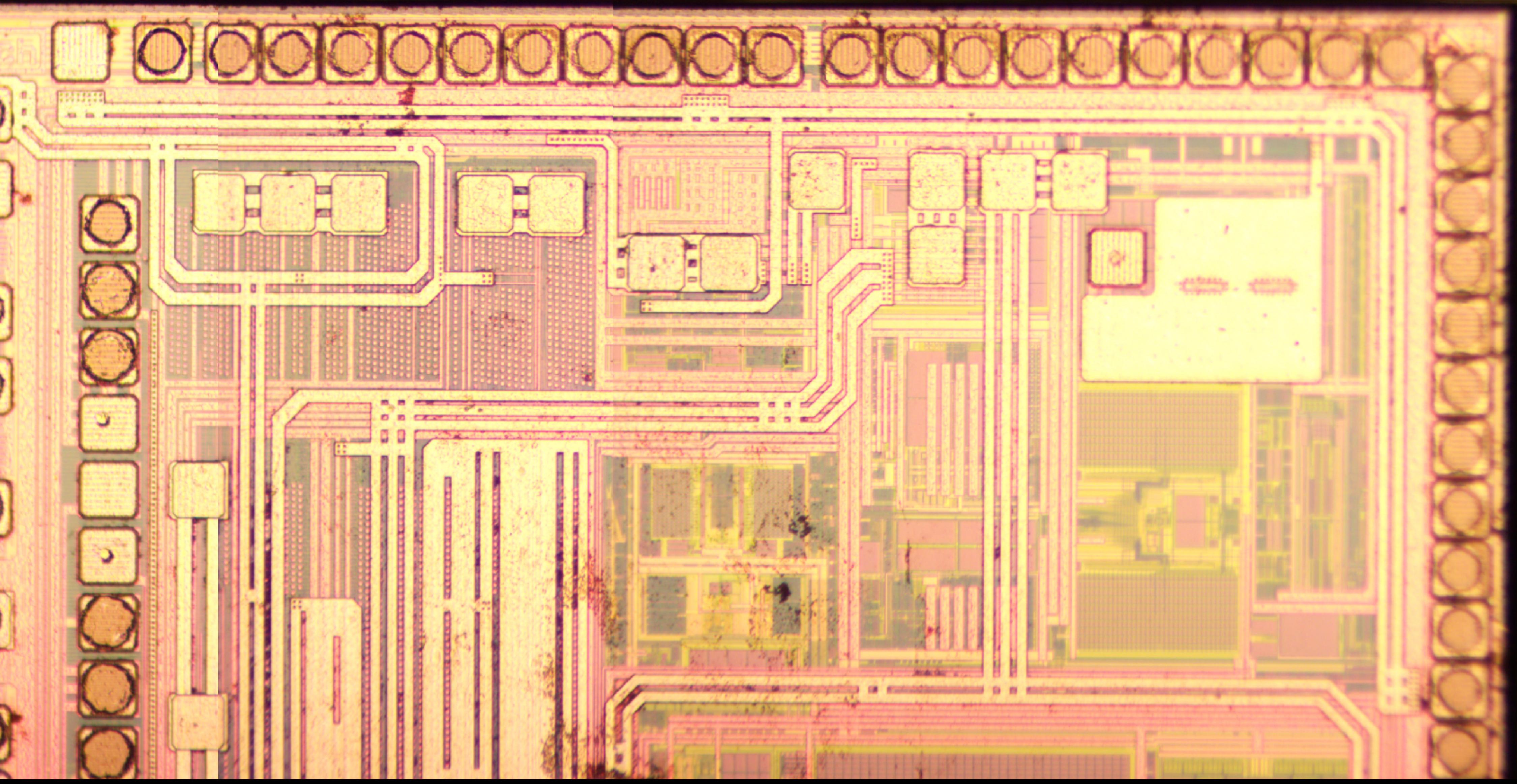


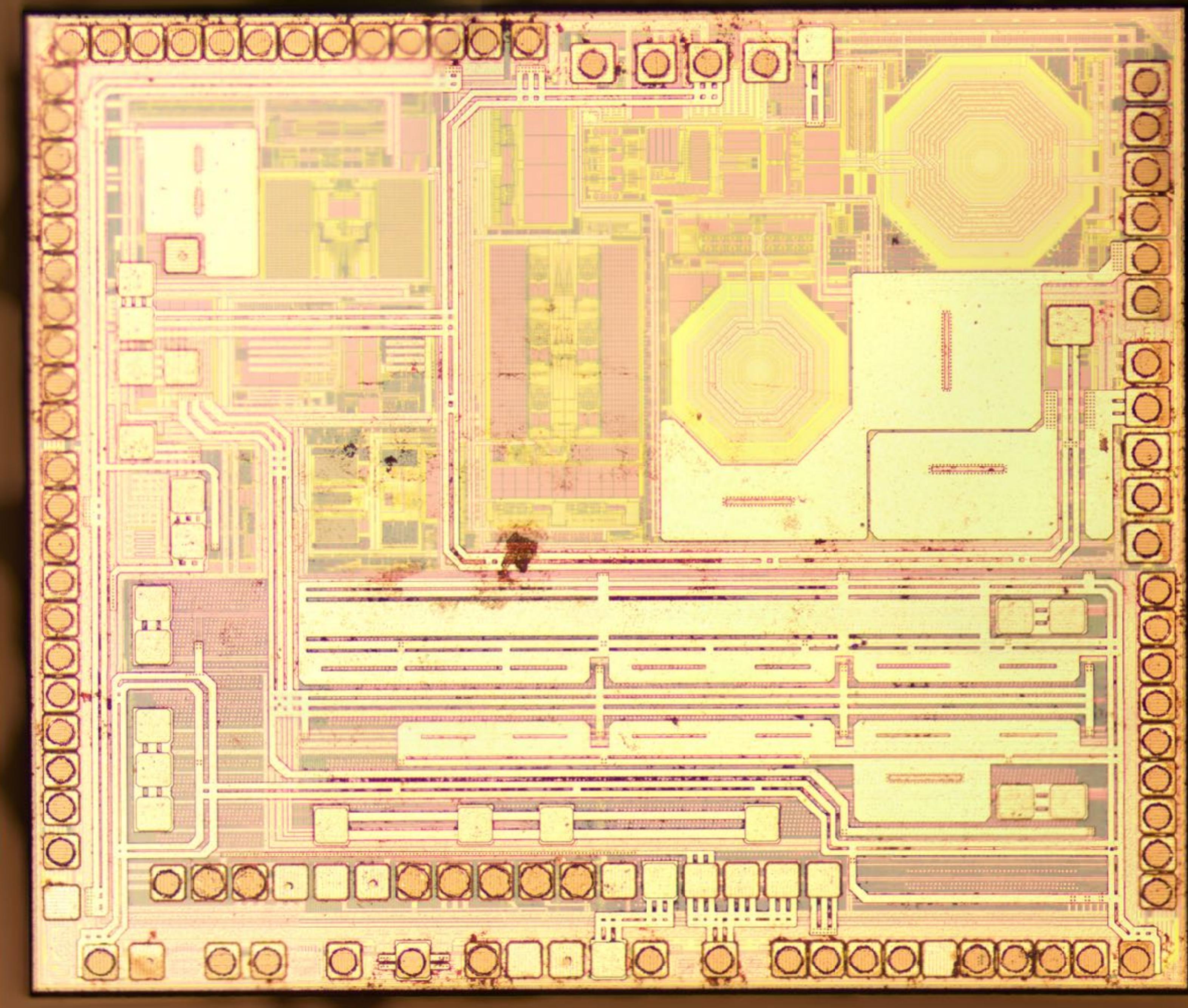










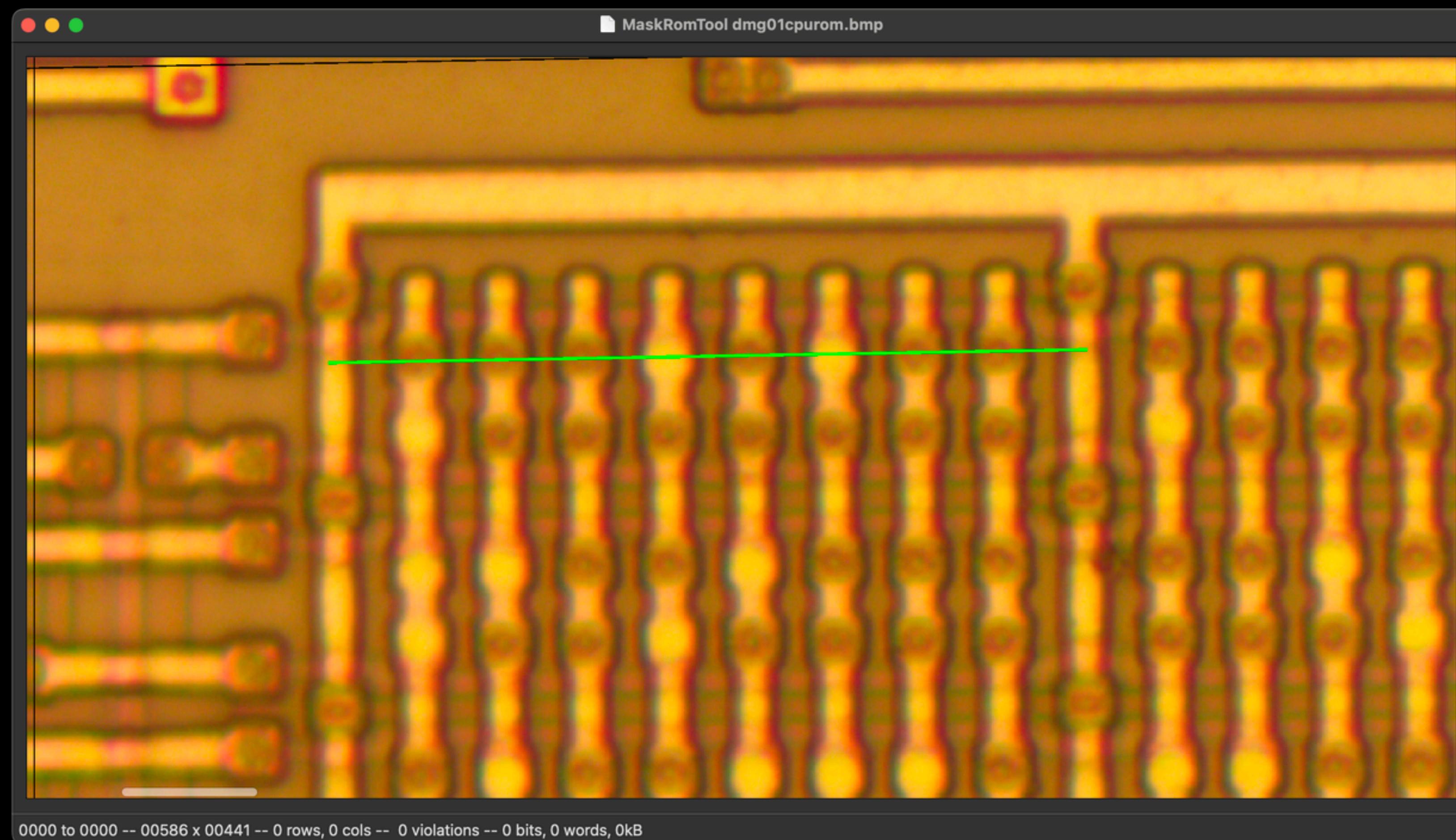


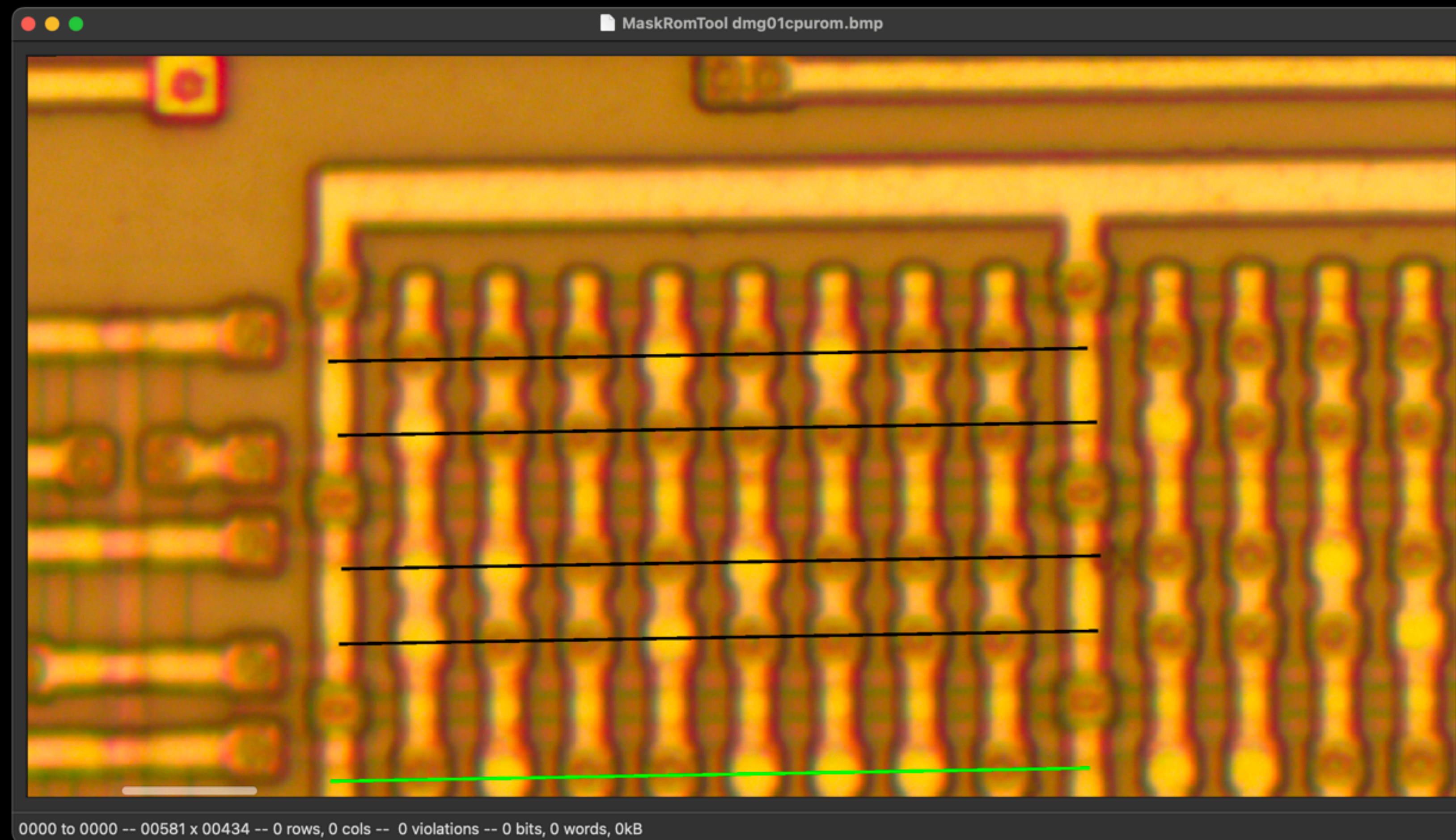
# Delayering

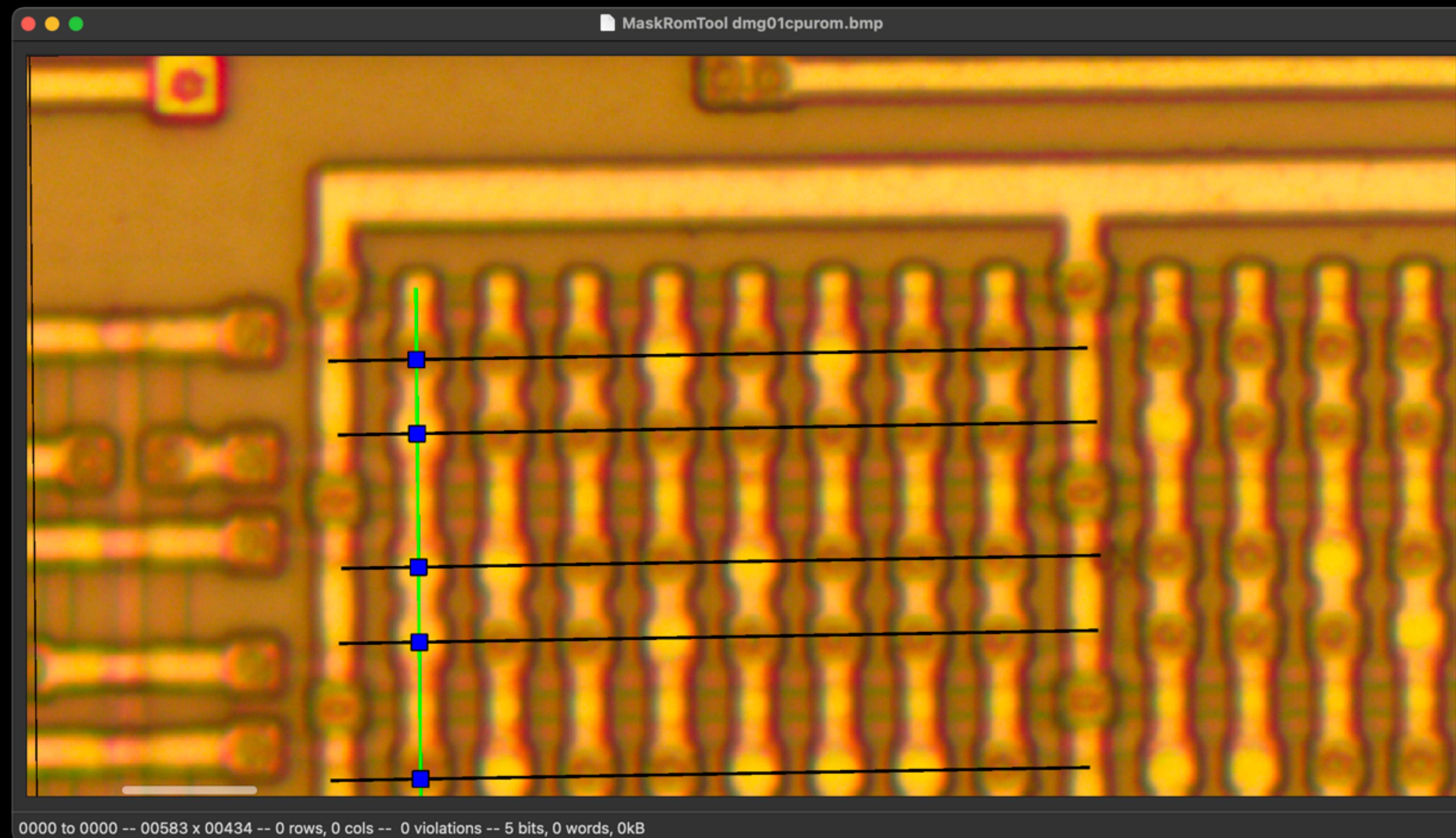
- HNO<sub>3</sub> or H<sub>2</sub>SO<sub>4</sub> gets us to the top metal layer.
- Many ROMs are lower in the chip:
  - Diffusion ROMs require HF delayering.
  - Implant ROMs require Dash Etch staining.

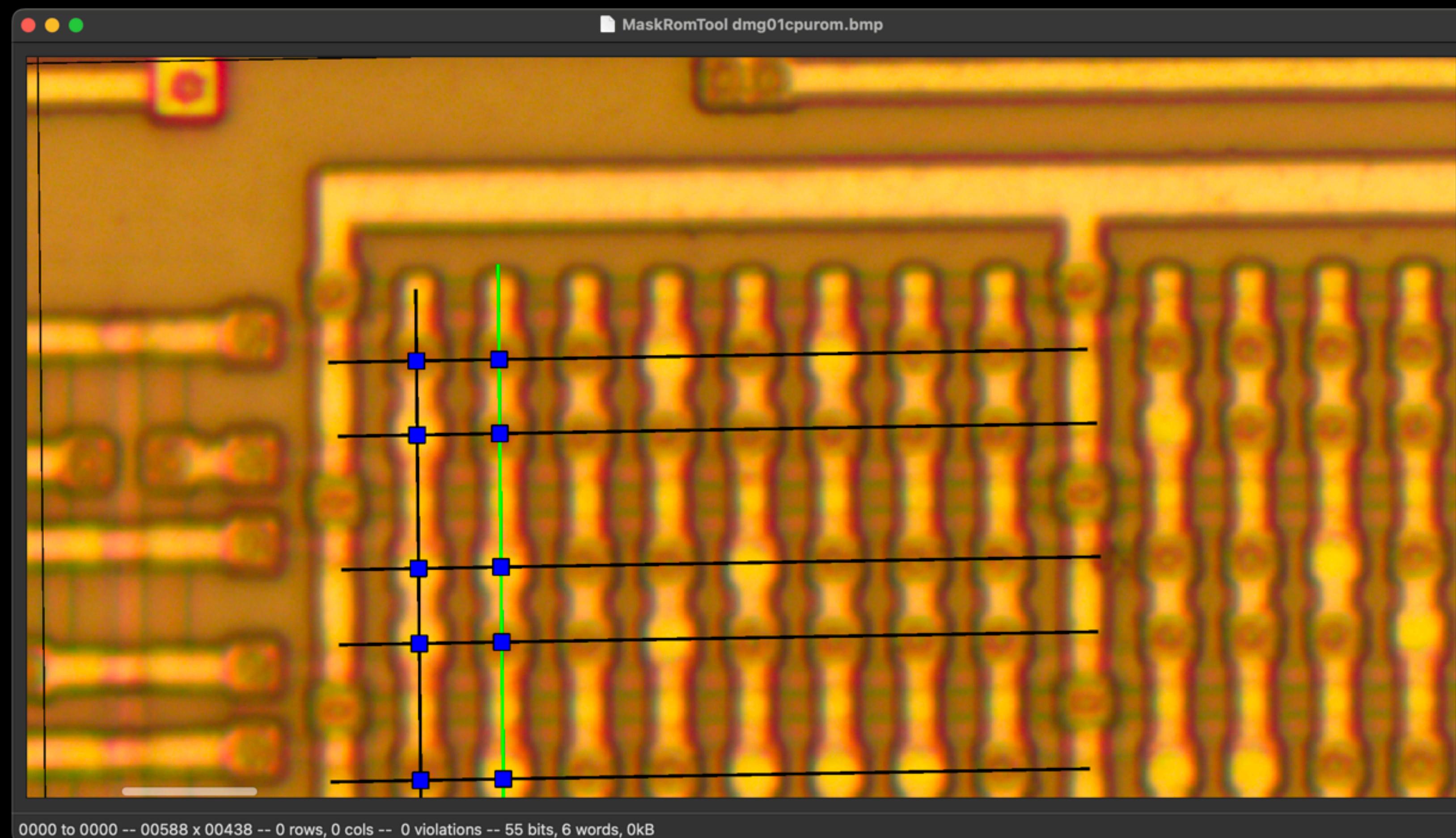
# A Quick Demo of Extraction

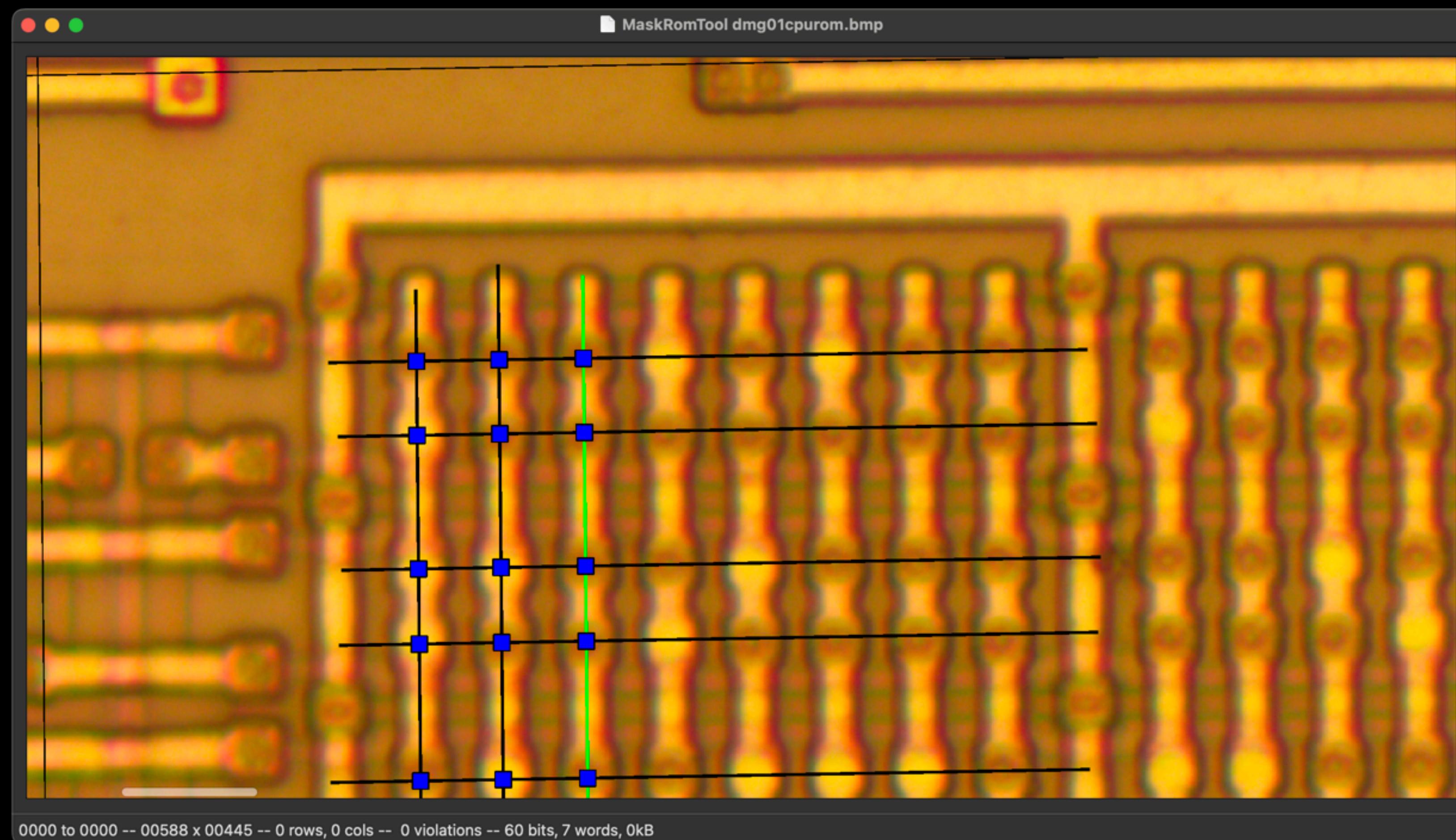
- We annotate some features:
  - Row and Column lines.
  - Threshold between One and Zero.
  - Forced bits.
- Tool helps out:
  - Bits marked at Row/Column intersections.
  - Design Rule Checks (DRC) reduce bit errors.
  - Importing diffs, Exporting to other formats.

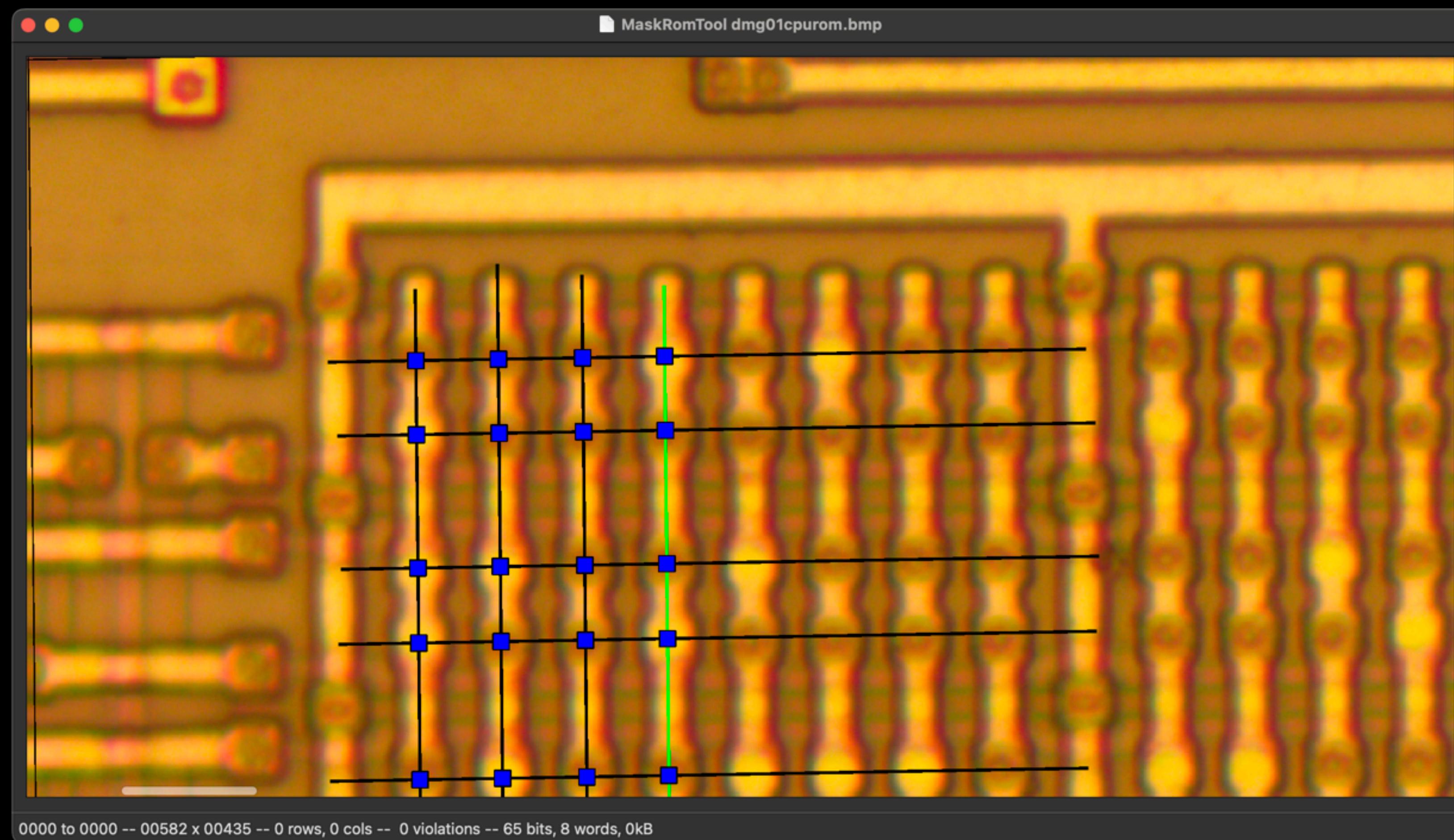


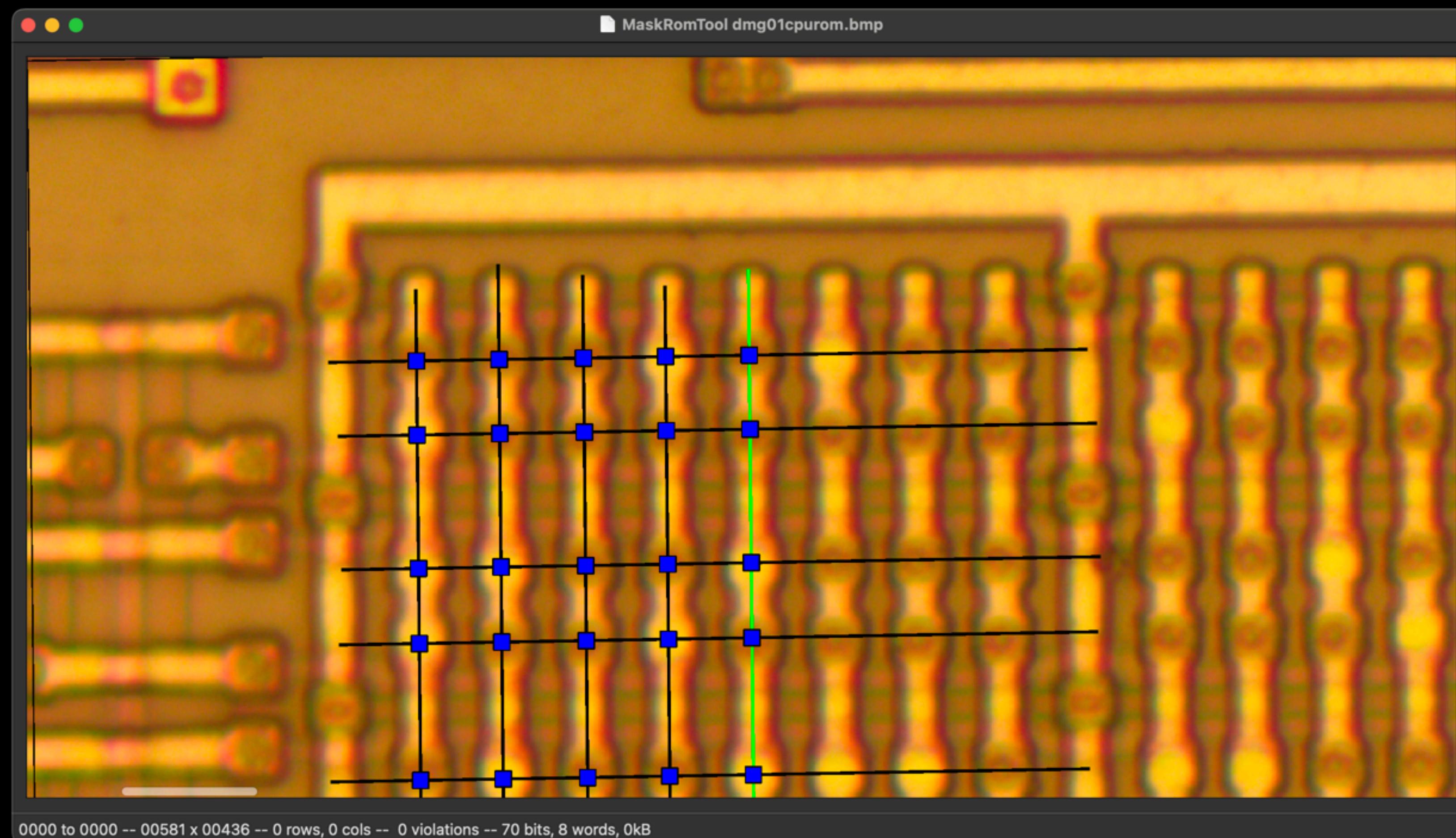


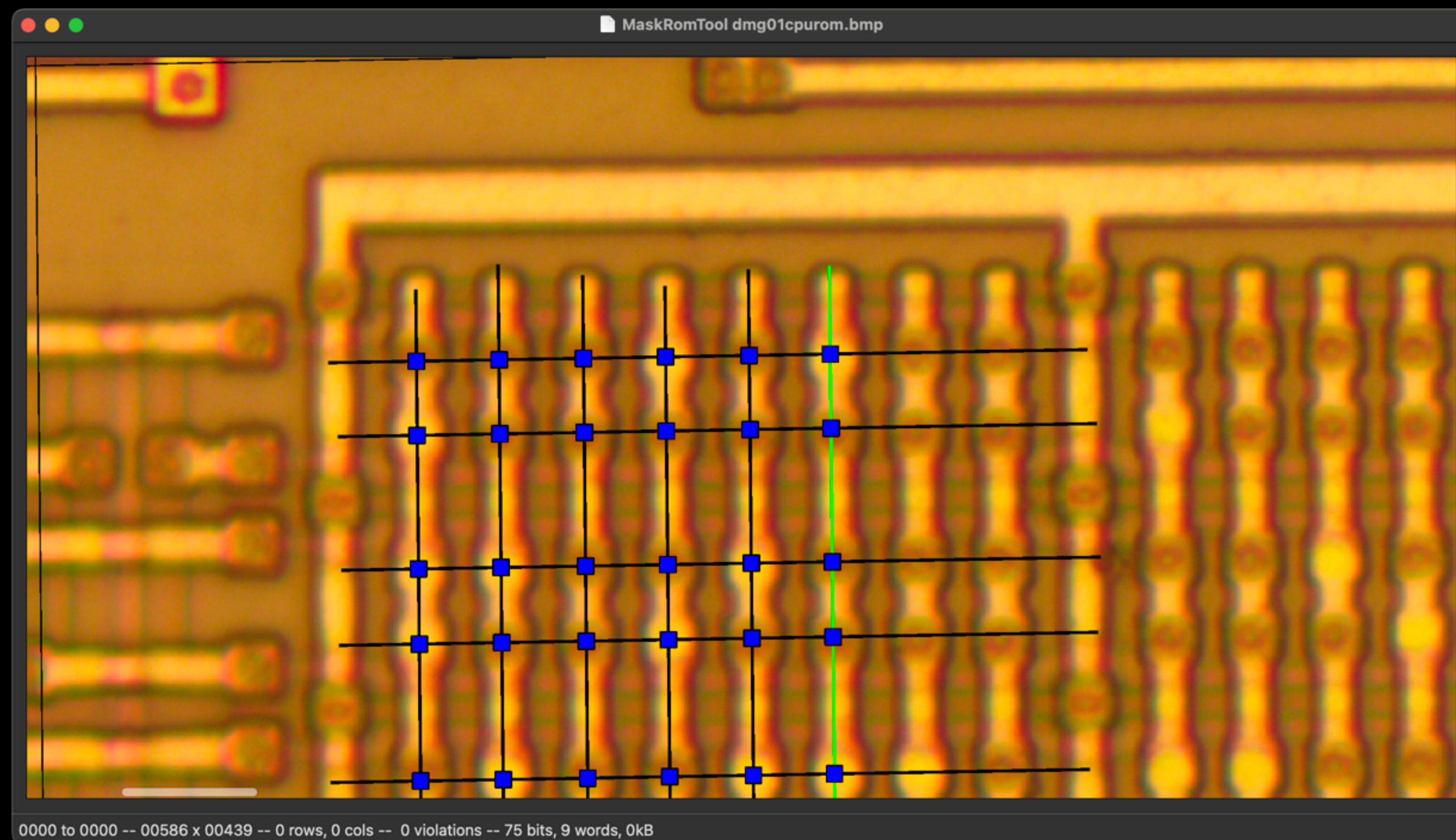


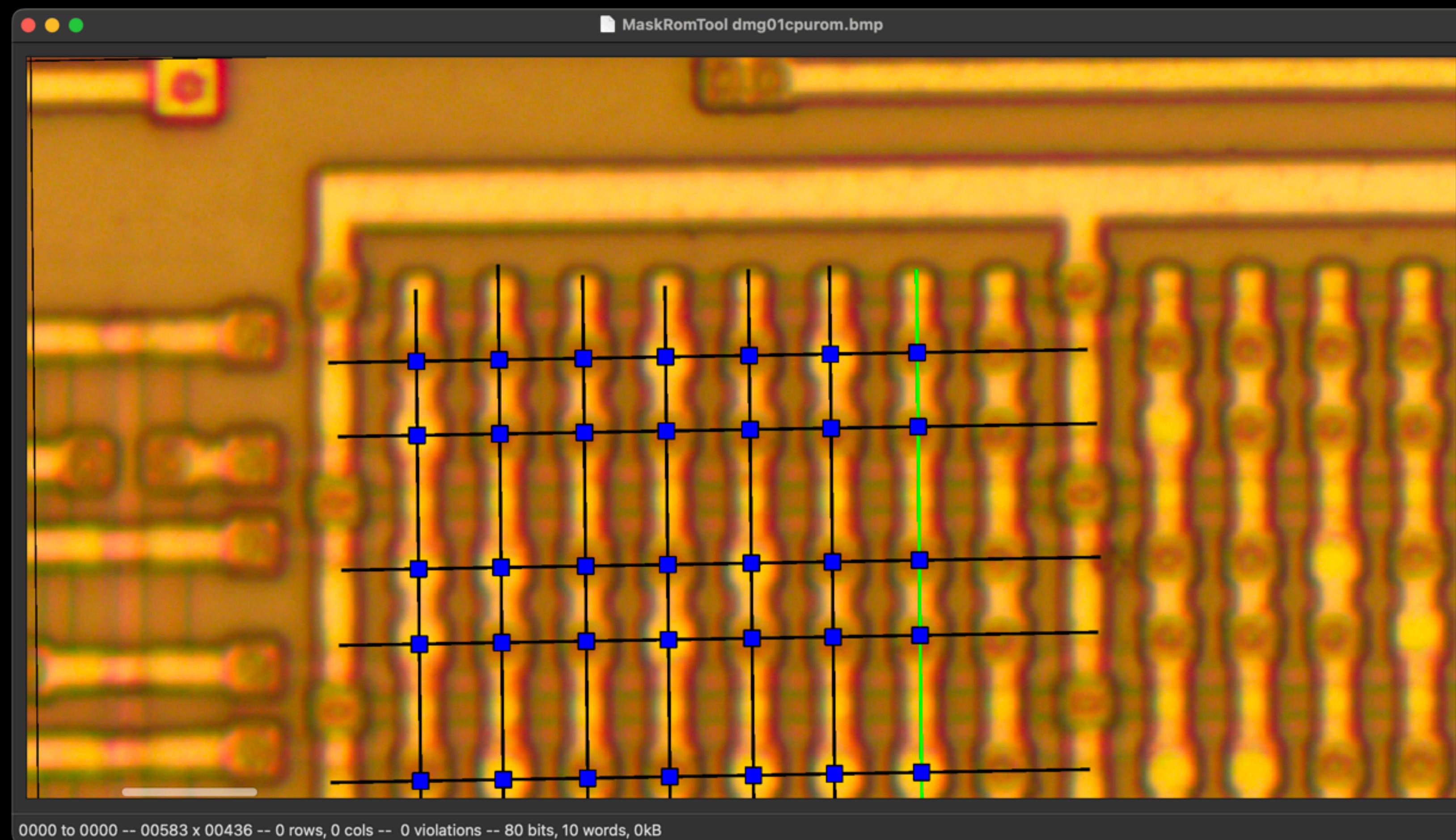


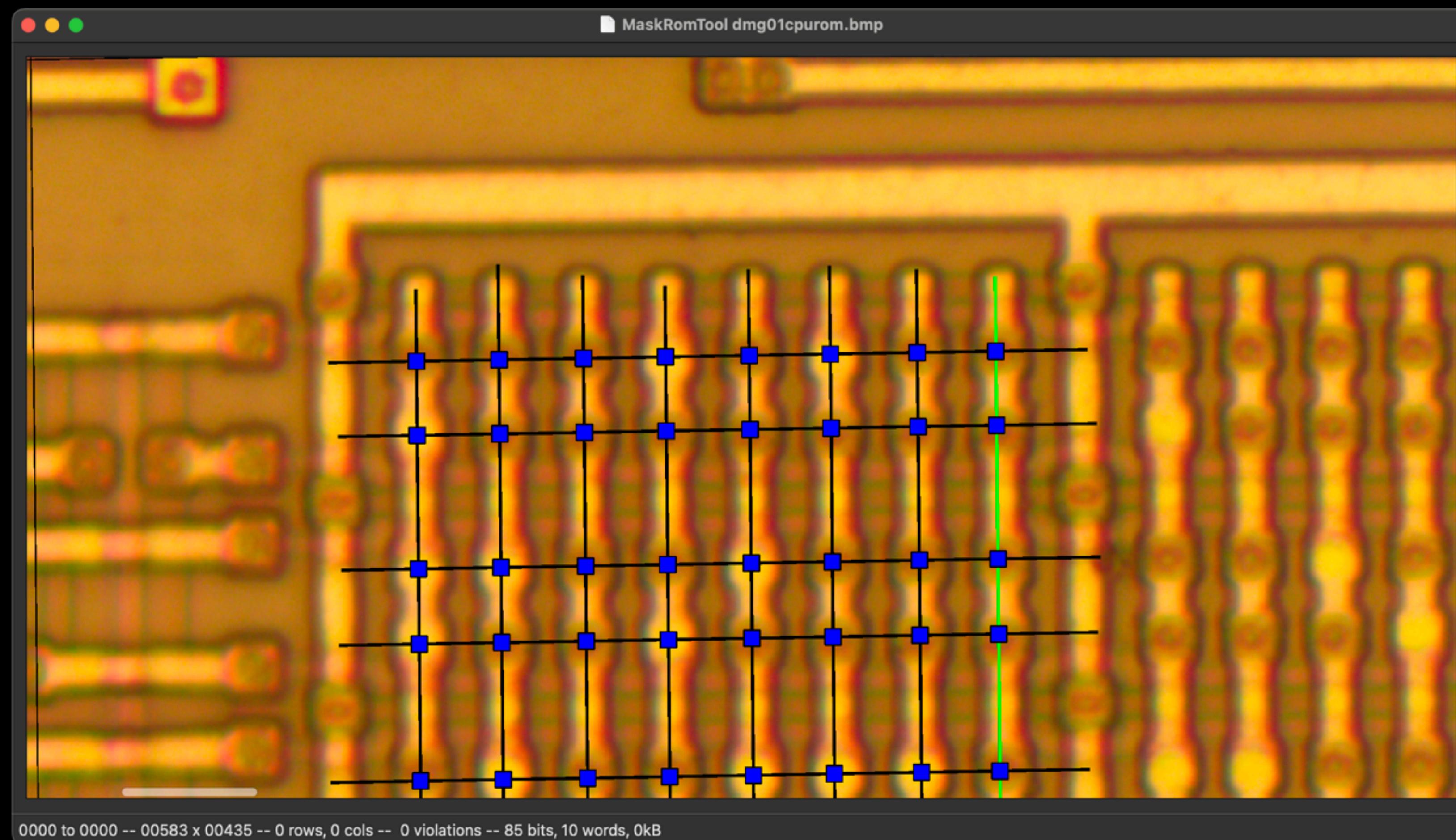


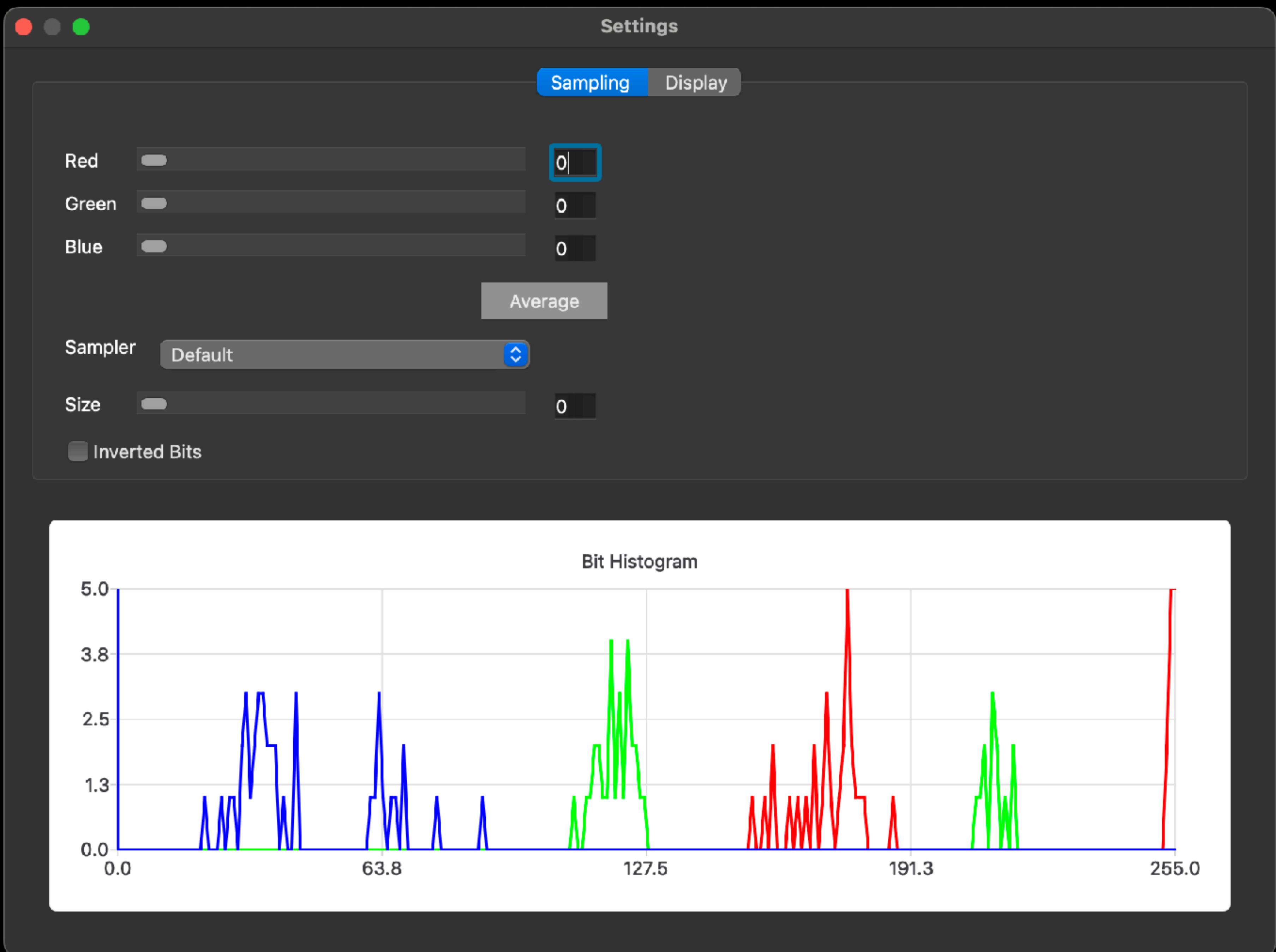


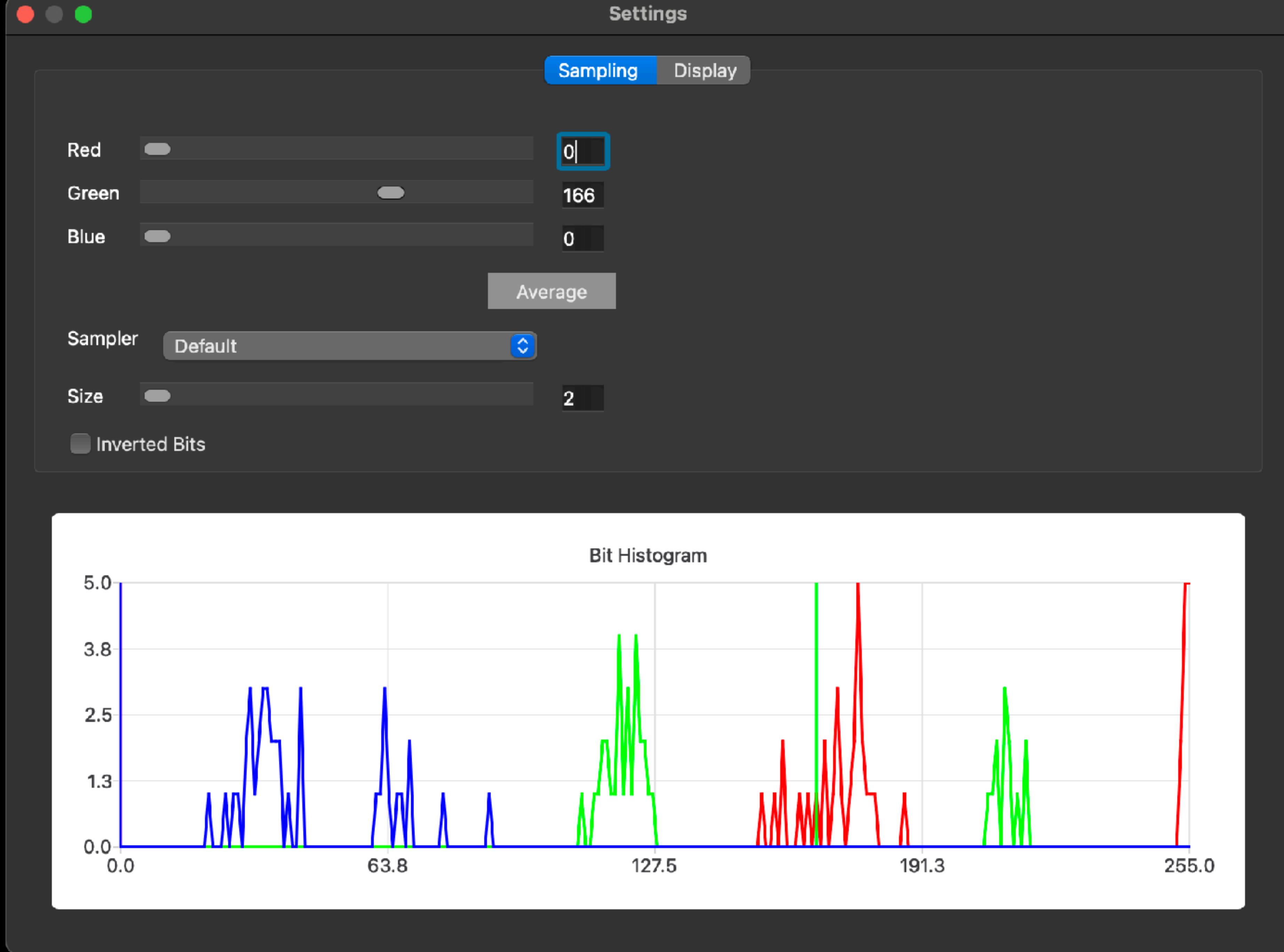


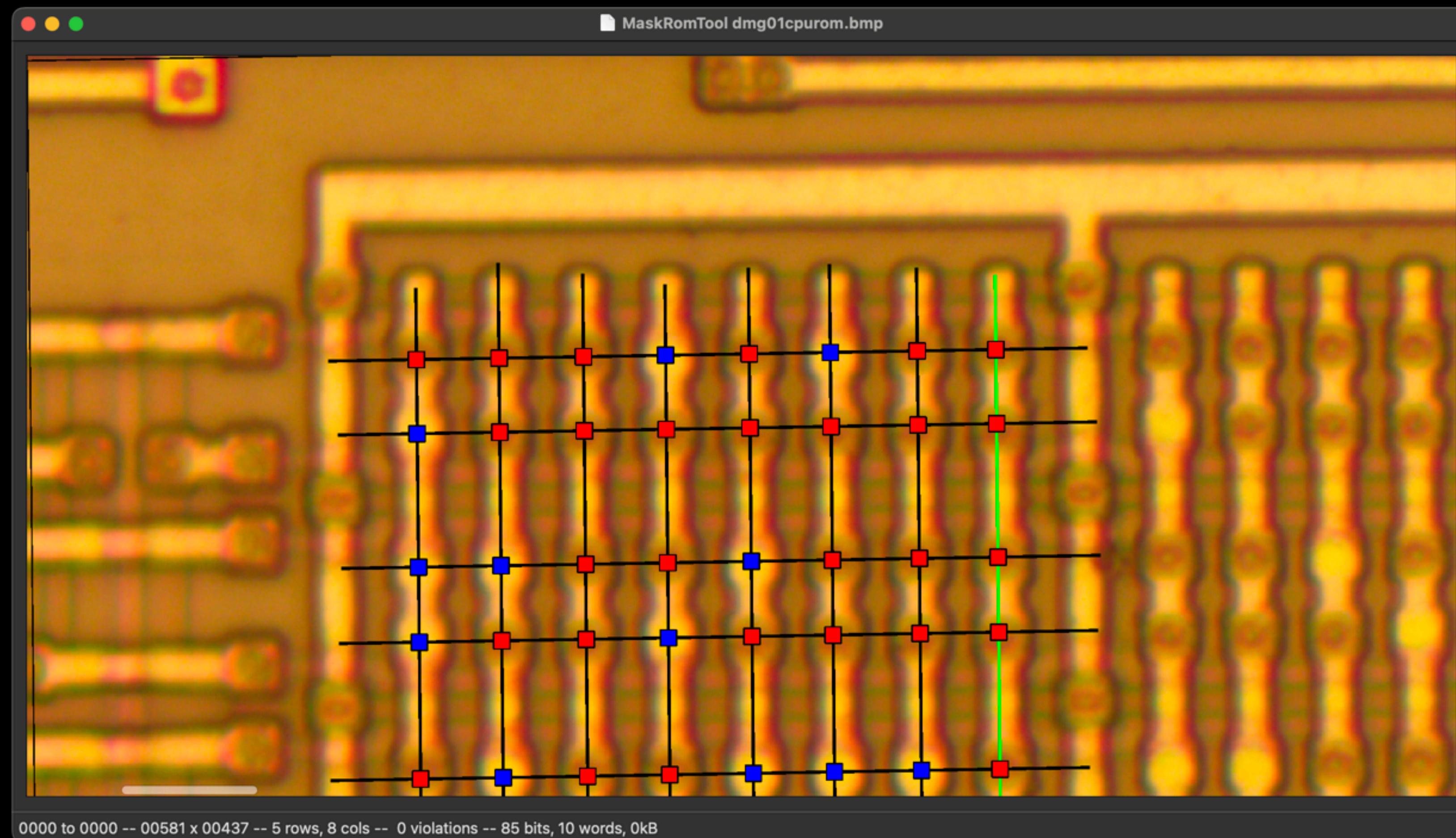








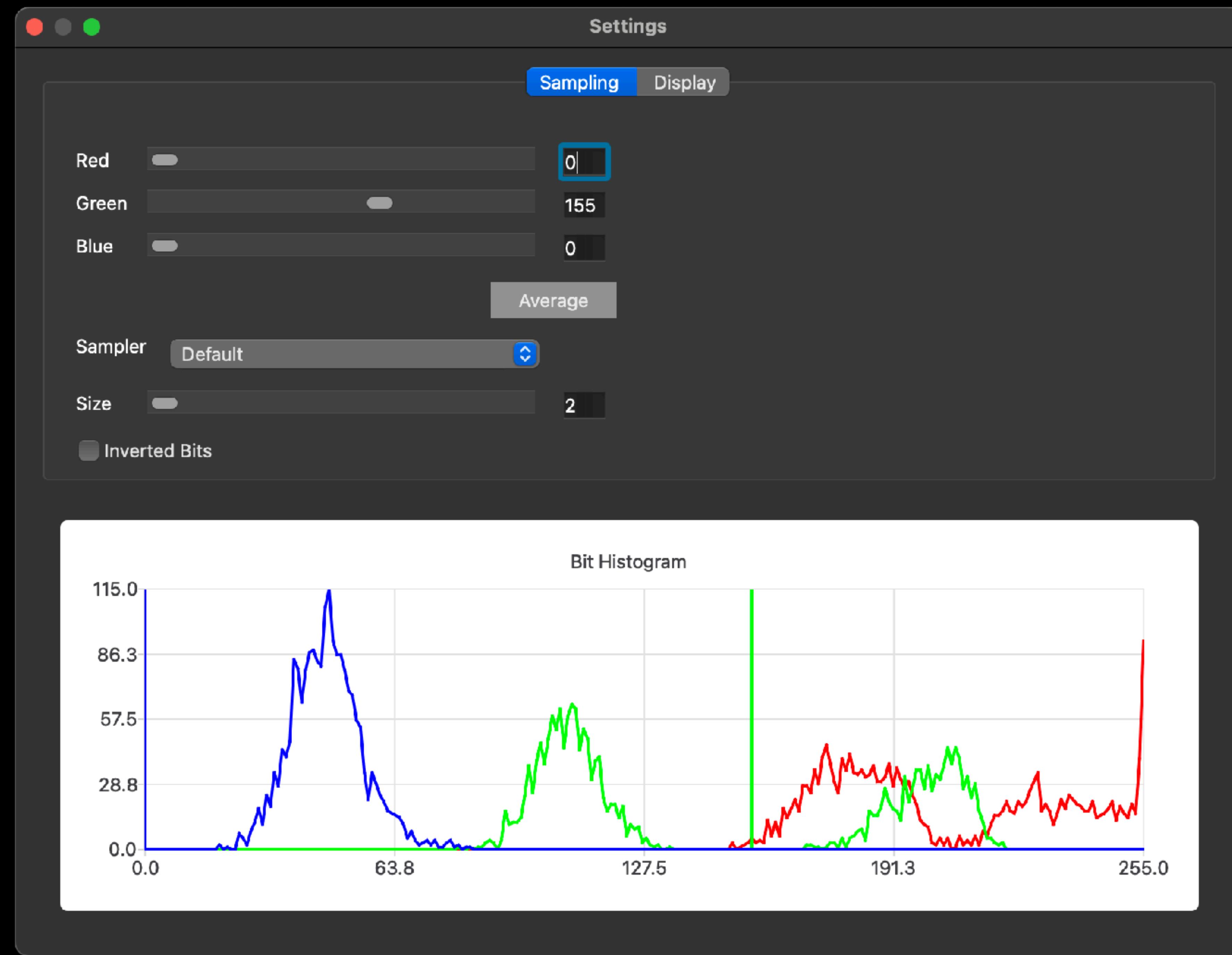






ASCII...

11101011  
01111111  
00110111  
01101111  
10110001



## ASCII Preview

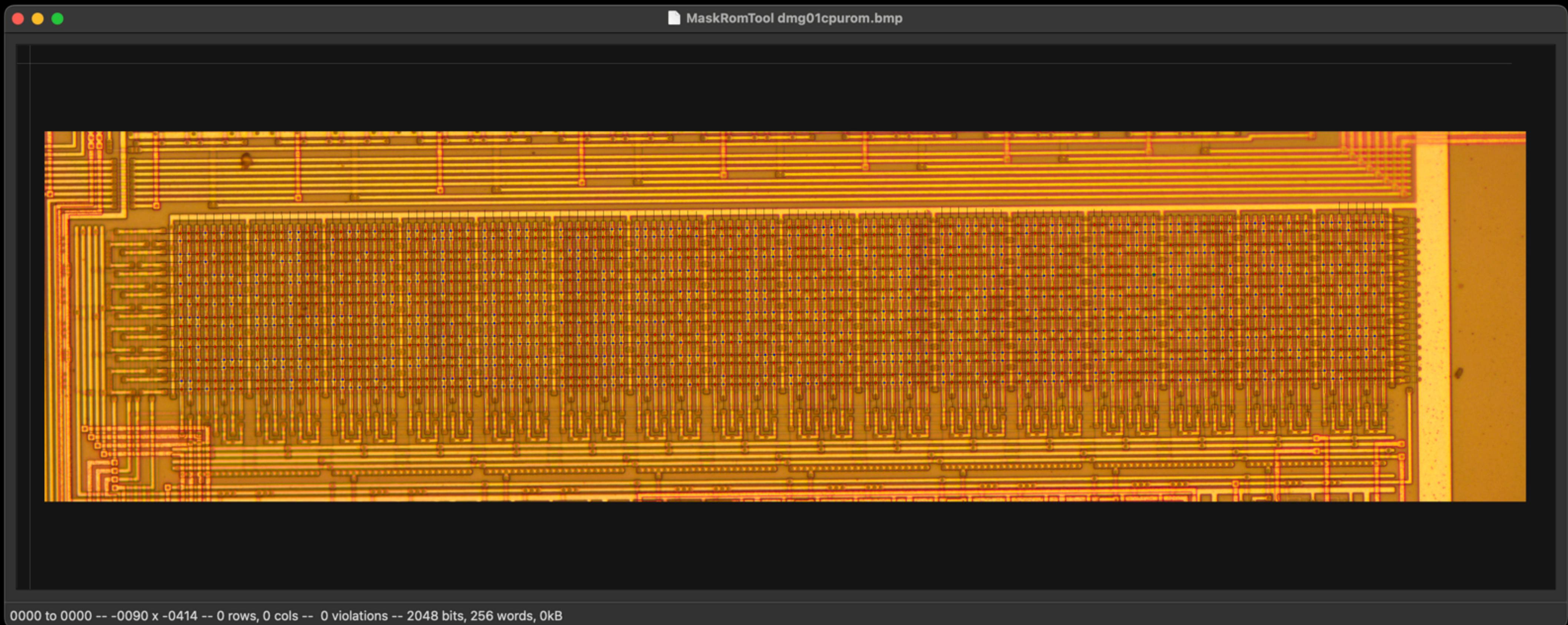
```
1110101111100101100101100100110010001000010110010110000001100111000100100100101100010000101110001101100001000  
0111111011100110111111011000110010110100111000011010101100111110011001011011000001110000010111011011011101111  
001101111101101101111101011101001011011111110110011100010111001001011000010101110001111101110111101111011000  
011011111100011111011110110001011100101101000100111000011101001111100111001011110100011010011111101001  
1011000100110000101110011011011001100111100001111001111101101100011111110011101011101000011010010001100010011  
011011100111001101001101101100110100111111100110010111110100110011100110011110100001111001110010110010  
0101110001110111111100011101110110000101101100011100010011000011110101001100001011110001100001001100101110110010  
101101011101011110100101111001110101111010000101011110001110101111000011111010111010101110001110011100110110010  
00011111000101000011011100111101011110001111101101110000100011011010000110100111001001100111101000101101110110110  
11011001000101100111100101010100011011110111011000011101011000011101011011110101111010110010110111110010111  
11111111111001111110101001111101000010011010100011011110111011000011111011011001101101111010110010110111110010111  
0101111011111010001111011011000100001110100000111011001011100100111110110110000101000101110000100000111100010001  
110110010001011101001010100111000010011000111110001111011000001001111001101100100111101100010100111100101001111001  
11111011001001111101001111100110101111101101111101100001010010110011001101110011011110101111011011110110001010  
00111111110110011101111100100010110011110100001111101111010011011111011100110111011101010111111101101011100111111  
10101101111100011011110110100001010111100110111101001001111100001101111010100110011110011010111111101101011100111111
```

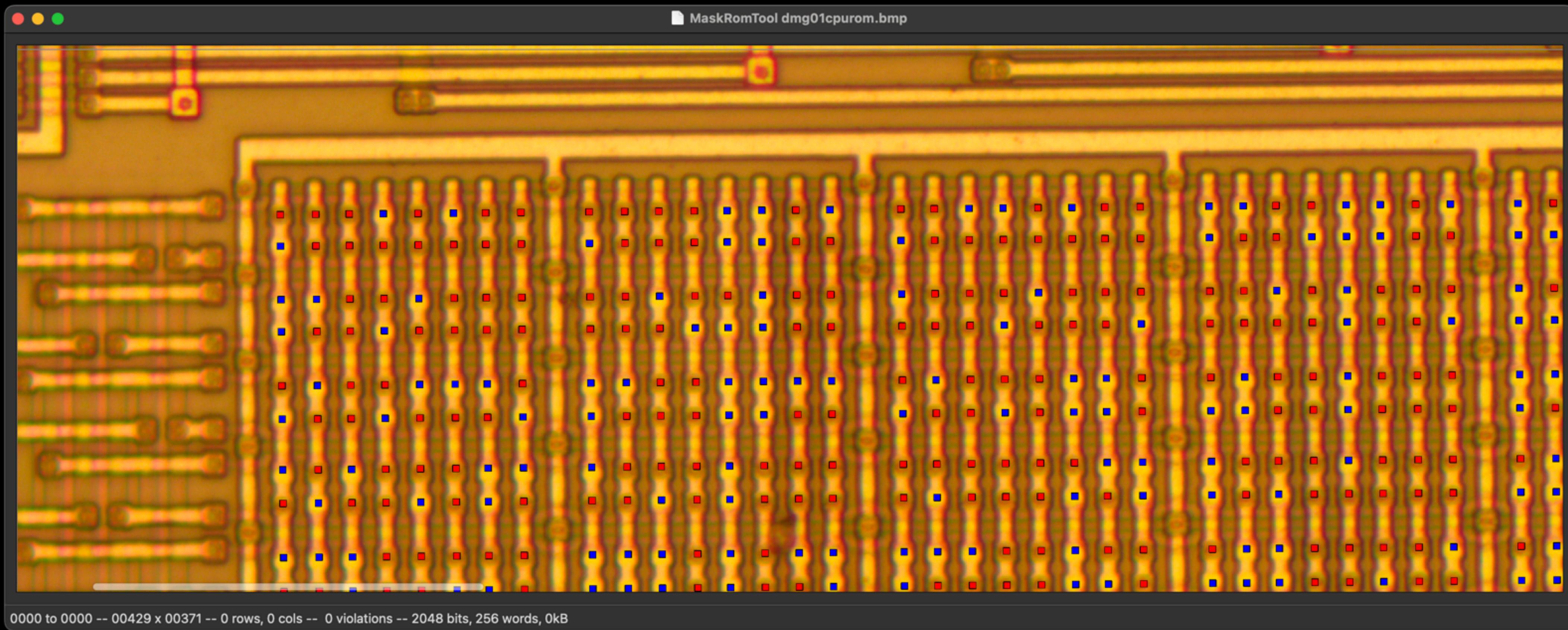
# After Bit Extraction

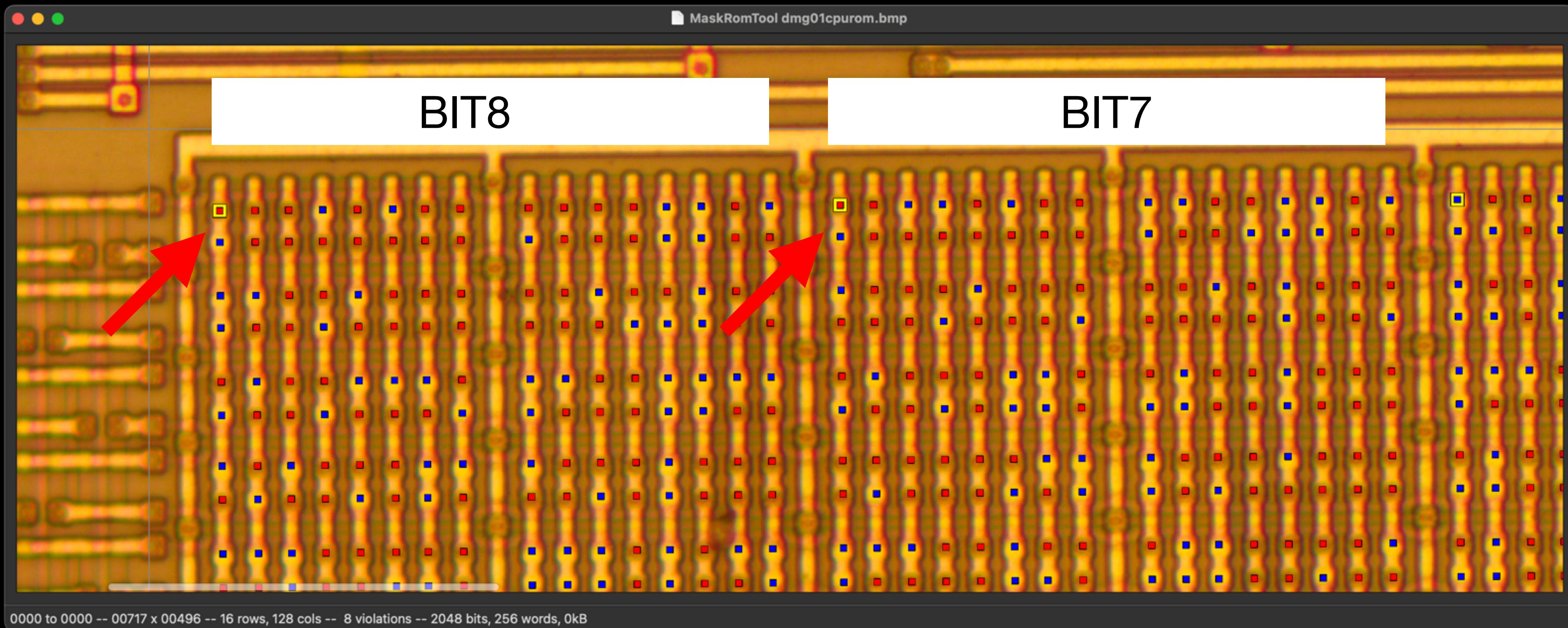
- Bits must be ordered into bytes.
- Errors must be corrected.
- Disassembly, reverse engineering!

# Bits into Bytes

- Each "major column" holds one bit of significance.
  - 8-bit ROM, eight major columns.
  - 32-bit ROM, thirty-two major columns.
- Take one bit from the same position in each major column to form a word.
- Word ordering is less consistent:
  - Left to right? Top to bottom?
  - Rotations, flips.







**Solver**

**Solutions**

**Decoder**

Bytes    Byte String    ASCII    Yara

--decode-cols-downr -i -r 0 --flipx

This solves for optionally masked bytes at absolute addresses. Comma or space delimited, the records are colon separated with an address, a byte, and an optional mask. Use the Byte String solver if you know the bytes, but not their address.

Examples:  
GameBoy: 0:31,1:fe,2:ff  
MYK82 Clipper Chip: 0:9b,7:ea

Query:  
0:31,1:fe,2:ff

**Solve**

**Hex View**

Address	Byte Value
0000	31 fe ff af 21 ff 9f 32 cb 7c 20 fb 21 26 ff 0e
0010	11 3e 80 32 e2 0c 3e f3 e2 32 3e 77 77 3e fc e0
0020	47 11 04 01 21 10 80 1a cd 95 00 cd 96 00 13 7b
0030	fe 34 20 f3 11 d8 00 06 08 1a 13 22 23 05 20 f9
0040	3e 19 ea 10 99 21 2f 99 0e 0c 3d 28 08 32 0d 20
0050	f9 2e 0f 18 f3 67 3e 64 57 e0 42 3e 91 e0 40 04
0060	1e 02 0e 0c f0 44 fe 90 20 fa 0d 20 f7 1d 20 f2
0070	0e 13 24 7c 1e 83 fe 62 28 06 1e c1 fe 64 20 06
0080	7b e2 0c 3e 87 e2 f0 42 90 e0 42 15 20 d2 05 20
0090	4f 16 20 18 cb 4f 06 04 c5 cb 11 17 c1 cb 11 17
00a0	05 20 f5 22 23 22 23 c9 ce ed 66 66 cc 0d 00 0b
00b0	03 73 00 83 00 0c 00 0d 00 08 11 1f 88 89 00 0e
00c0	dc cc 6e e6 dd dd d9 99 bb bb 67 63 6e 0e ec cc
00d0	dd dc 99 9f bb b9 33 3e 3c 42 b9 a5 b9 a5 42 3c
00e0	21 04 01 11 a8 00 1a 13 be 20 fe 23 7d fe 34 20
00f0	f5 06 19 78 86 23 05 20 fb 86 20 fe 3e 01 e0 50

Show Selected Bytes

--decode-cols-downr -i -r 0 --flipx

Flip X     Flip Y     Zorrom Mode

Invert Bits

Rotation: **p**    Wordsize: 8

Decoder: cols-left, cols-right, cols-downl, cols-downl-swap, cols-downr, squeeze-lr, tlcs47font, z86x1

Bank: left, right

Flags:

--decode-cols-downr -i -r 0 --flipx

## Decoder

## r2/gb Disassembly

Bits Disassembly

Architecture:

r2/bf  
 r2/bpf  
 r2/bpf.mr  
 r2/chip8  
 r2/cr16  
 r2/cris  
 r2/dalvik  
 r2/dis  
 r2/ebc  
 r2/evm  
 r2/fslsp  
**r2/gb**  
 r2/h8300  
 r2/hppa  
 r2/i4004  
 r2/i8080  
 r2/java  
 r2/jdh8  
 r2/kvx  
 r2/lanai  
 r2/lh5801  
 r2/lm32  
 r2/loongarch  
 r2/lua  
 r2/m680x  
 r2/m68k

0x00000000	3	31feff	ld sp, 0xffff
0x00000003	1	af	xor a
0x00000004	3	21ff9f	ld hl, 0x9fff
0x00000007	1	32	ldd [hl], a
0x00000008	2	cb7c	bit 7, h
0x0000000a	2	20fb	jr nZ, 0xfb
0x0000000c	3	2126ff	ld hl, 0xff26
0x0000000f	2	0e11	ld c, 0x11
0x00000011	2	3e80	ld a, 0x80
0x00000013	1	32	ldd [hl], a
0x00000014	1	e2	ld [0xff00 + c], a
0x00000015	1	0c	inc c
0x00000016	2	3ef3	ld a, 0xf3
0x00000018	1	e2	ld [0xff00 + c], a
0x00000019	1	32	ldd [hl], a
0x0000001a	2	3e77	ld a, 0x77
0x0000001c	1	77	ld [hl], a
0x0000001d	2	3efc	ld a, 0xfc
0x0000001f	2	e047	ld [rBGP], a
0x00000021	3	110401	ld de, 0x0104
0x00000024	3	211080	ld hl, 0x8010
0x00000027	1	1a	ld a, [de]
0x00000028	3	cd9500	call 0x0095
0x0000002b	3	cd9600	call 0x0096
0x0000002e	1	13	inc de

# Software and target!

- <https://github.com/travisgoodspeed/maskromtool/>
  - Latest release for Windows or macOS.
  - Build from source with Qt6 for Linux.
- <https://github.com/travisgoodspeed/gbrom-tutorial>
  - Clone this locally, follow instructions in README.

# Cheat Sheet

- Left-click sets source position.
  - Left-drag selects lines.
- Middle-drag pans the view.
- Right-drag moves the selected lines.
- R places a Row, C places a Column, SPACE duplicates last placement.
- D deletes a selection, S sets a new end position.
- Shift+D duplicates a selected group, then right-drag to move the copy.
- V to check for errors, E for next error.