

# Chameleon

## Polymorphic Engine

r2con2025 | Bernat – gum3t



# Whoami

- Security researcher.
- Exploiting & low level stuff.



Bernat – gum3t



# Chameleon | Introduction



- Chameleon is a polymorphic engine for x86\_64 position independent shellcode.

Position Independent Shellcode: Shellcode that executes correctly regardless of its absolute load address.



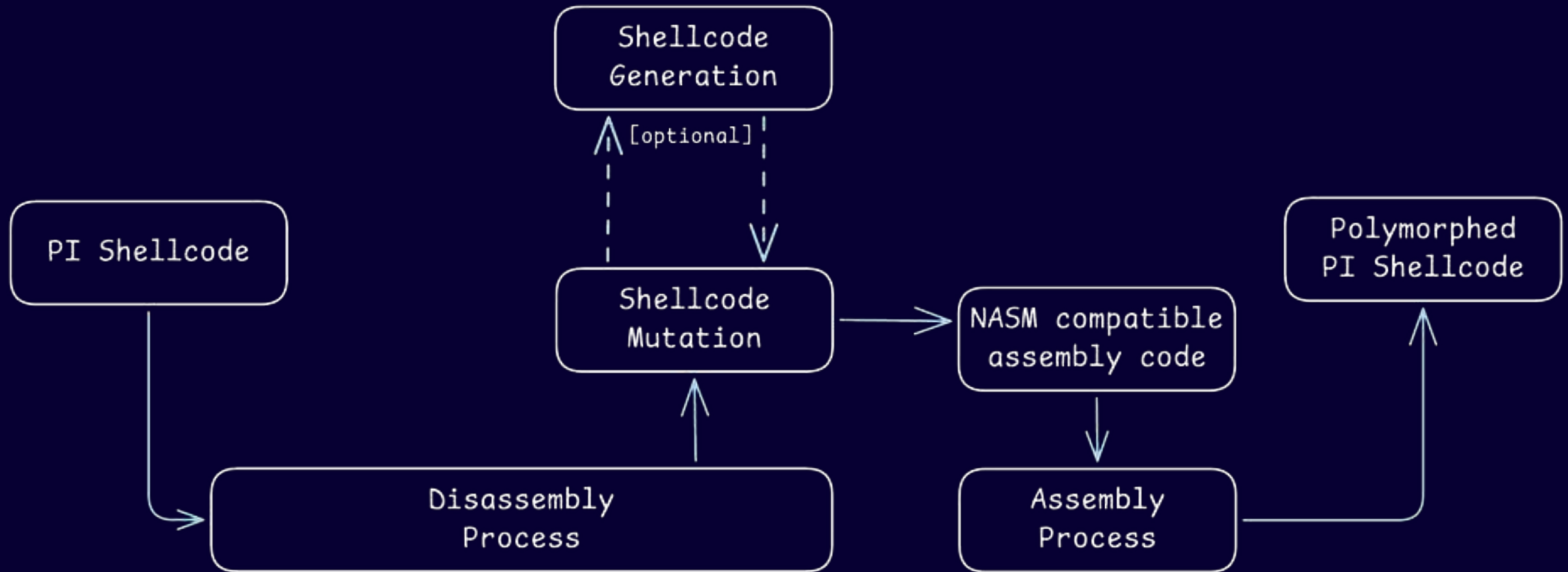
# Chameleon | Requirements



- Evade/avoid signature-based detections in red team engagements.
- Don't break the given shellcode.



# Chameleon | Architecture Overview



# Chameleon | Disassembly Process



## Problems:

- x86\_64 instructions have different sizes.
- Linear parsing for disassembly does not work.



# Chameleon | Disassembly Process



Example:

- Given the 81 c0 01 00 ff c0 eb fc sequence of bytes, we can see the following result:

```
[gun3t@ubuntu] - [~/chameleon-polymorphicengine]
$ ndisasm -b 64 tmp/code.bin
00000000  81C00100FFC0      add  eax,0xc0ff0001
00000006  EBFC             jmp  short 0x4
```



# Chameleon | Disassembly Process



81 c0 01 00 ff c0 eb fc

81 c0 01 00 ff c0	->	add eax, 0xc0ff0001
ff c0	->	inc eax
eb fc	->	jmp 0x4





# Chameleon | Disassembly Process



Solution:

- Get the disassembly data from a basic block graph (control flow graph).

Basic Block: A straight-line sequence of instructions with no branches in except to the entry and no branches out except at the exit.



# Chameleon | Disassembly Process



[0x00000000]> agf @ 0x0

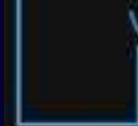
```
0x0  
10: fcn.00000000 ();  
add eax, 0xc0ff0001
```



```
0x6  
jmp 4
```



```
0x4  
:[x] CODE XREF from fcn.00000000 @ 0x6(x)  
inc eax
```



# Chameleon | Disassembly Process



Actual implementation:

- r2pipe – radare2 scripting API
  - aaa to analyze the shellcode.
  - aflq to get all function offsets.
  - agfj @ offset to get all basic blocks graph data in json format.



# Chameleon | Disassembly Process



```
[0x00000000]> aflq  
0x00000000  
0x00000208  
0x00000310  
0x000003ec  
0x000007c  
0x000004e0  
[0x00000000]>
```

```
[0x00000000]> agfj @ 0x000004e0  
[{"name":"fcn.000004e0","addr":1248,"ninstr":13,  
ptr":0,"fcn_addr":1248,"fcn_last":1290,"size":3,  
["fcn.000004e0"],"xrefs":[{"addr":405,"type":"CA  
isasm":"mov rcx, qword [rbx]","bytes":"488b0b","  
":"0,rcx,rcx,&,<=>,$z,zf,<=>,$p,pf,<=>,63,$s,sf,<=>,  
y":"cpu","type":"acmp","reloc":false,"type_num":  
last":1291,"size":2,"opcode":"je 0x4fa","disasm"  
de":"1274","type":"CODE","norm":"<=>11111 f"addr"
```





How do we turn the radare2 parsed data into valid assembly code?



# Chameleon | Disassembly Process



- Each basic block gets assigned a custom asm label:

block\_0x1000:

...

- Control flow instructions immediates are replaced by respective labels:

jne 0x103f -> jne block\_0x103f



# Chameleon | Disassembly Process



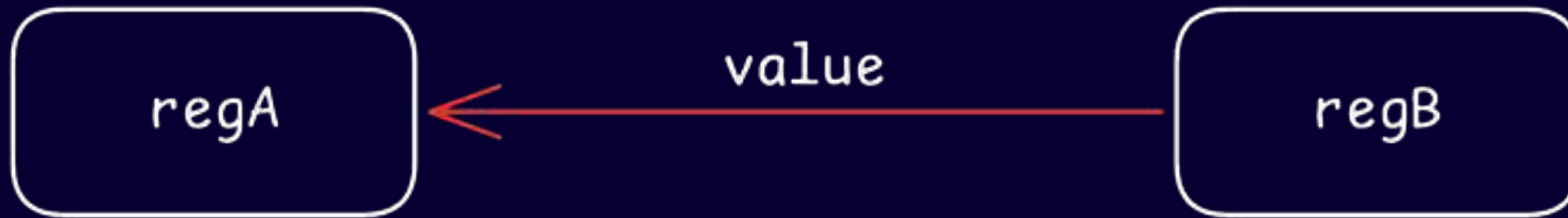
- Instructions may be mutated and later hardcoded via db or represented by their mnemonics and operands.
- A final enforced jmp to the fall-through block is placed at the end of the generated asm code of each basic block.



# Chameleon | Shellcode Mutation



- MutationPatternPool: Focuses on the mutation of an action.





# Chameleon | Shellcode Mutation



- MutationPatternPool -> match\_rules:
  - Focus on the detection of the many patterns an action can have.

instances of an  
action pattern

```
block_0x103f:  
ins_0;  
ins_1;  
ins_2;  
ins_3;  
ins_4;  
ins_5;  
ins_6;  
ins_7;  
...  
ins_n-2;  
ins_n-1;  
ins_n;  
jmp block_0x1180
```



# Chameleon | Shellcode Mutation



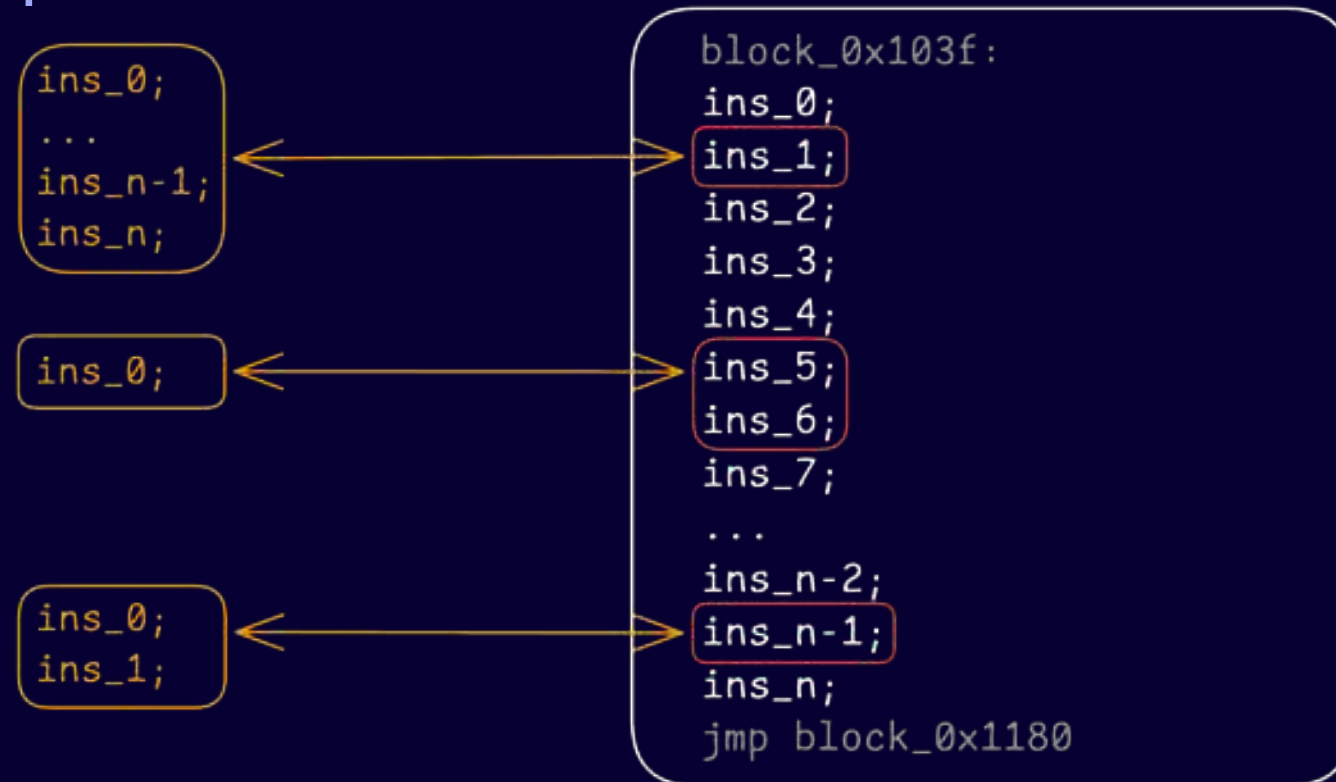
- MutationPatternPool -> mutation\_probability:
  - Probability to apply a mutation once a pattern is found. It is a percentage.



# Chameleon | Shellcode Mutation



- MutationPatternPool -> generators:
  - Focus on the generation of code equivalent to the found patterns.



# Chameleon | Shellcode Mutation



- Cook your own mutations!
  - Detect patterns by opcodes, bytes, ESIL... with your own match rules.
  - Create equivalent code with your own generators.



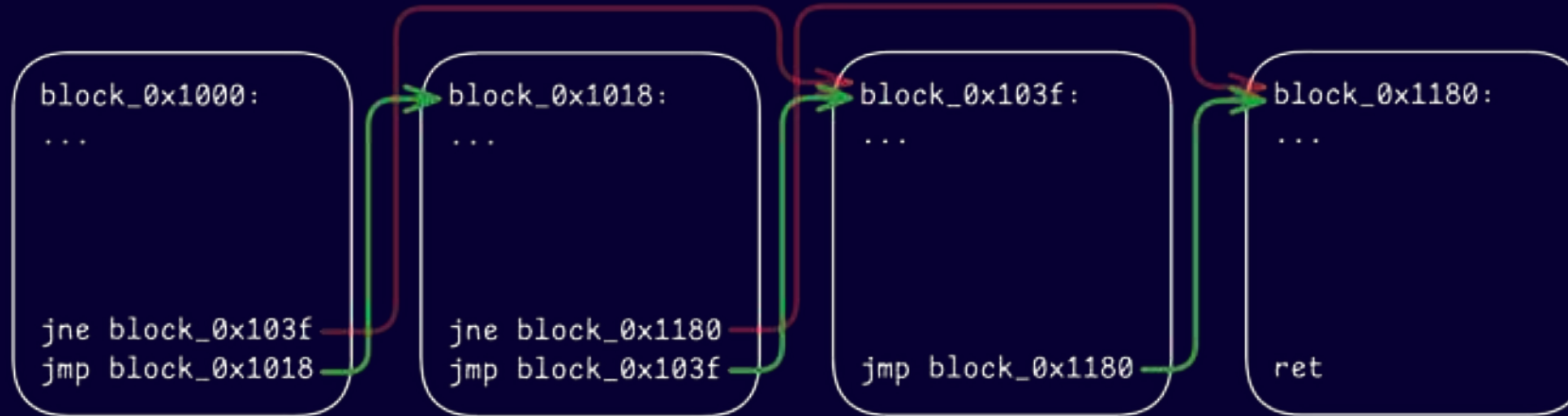
# Chameleon | Shellcode Mutation



- Remember the enforced jmp we add at the end of each basic block?
- Now this allows us to shuffle basic blocks without breaking the control flow!



# Chameleon | Shellcode Mutation



Before  
shuffling  
blocks



After  
shuffling  
blocks



# Chameleon | Shellcode Mutation



- After this process, enforced jmp instructions that are redundant can be removed.



# Chameleon | Shellcode Generation



- Mutation generators can make use of a code generation feature.
- So, what is it, and how does it work?





# Chameleon | Shellcode Generation

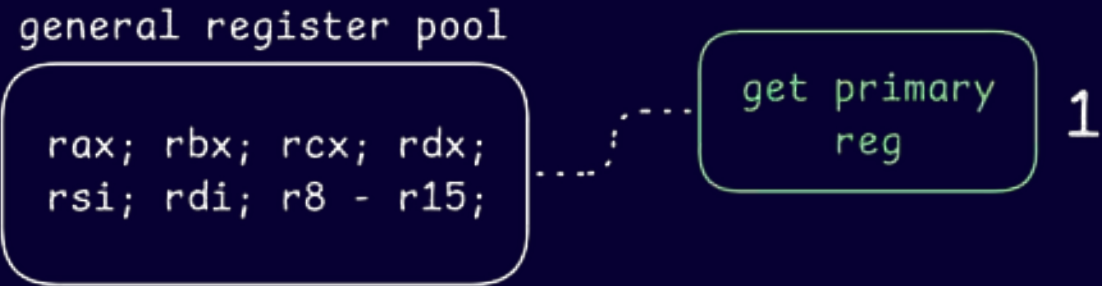


- Chameleon's code generator is a feature that is capable of creating semantically neutral shellcode blobs that can include nested branches and loops.

Semantically neutral code: Code that changes the program appearance but not its behaviour.



# Chameleon | Shellcode Generation

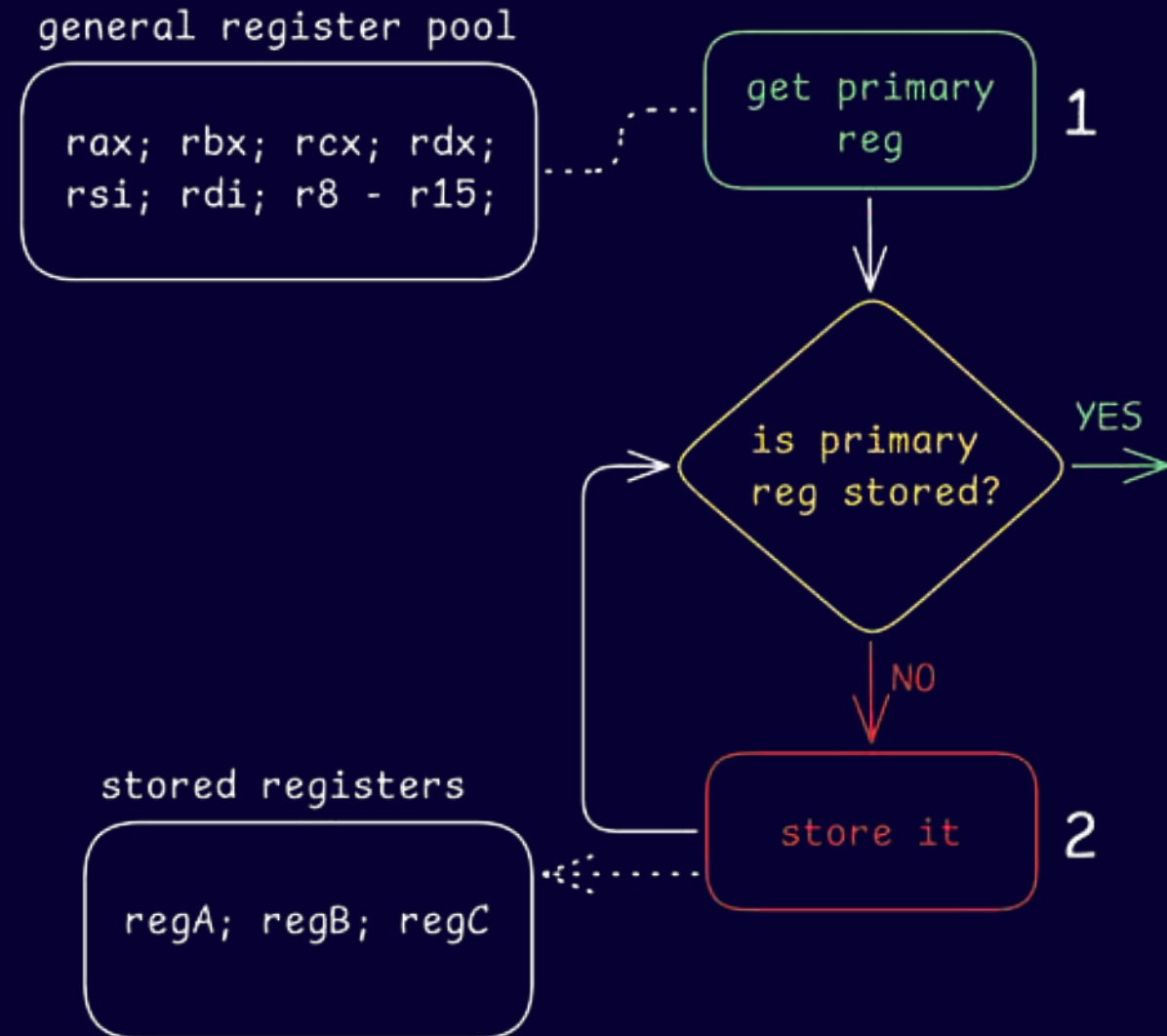


stored registers

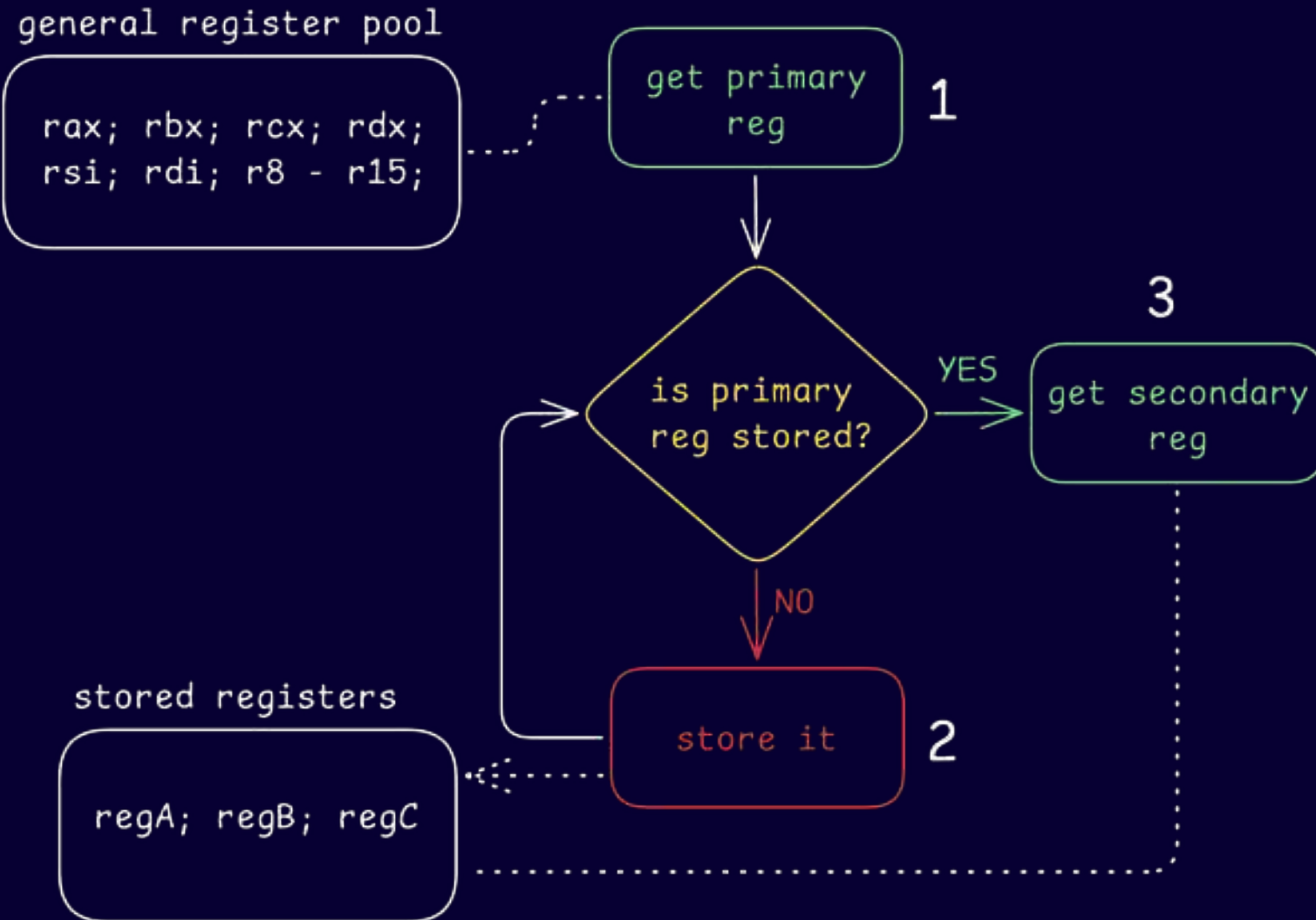
```
regA; regB; regC
```



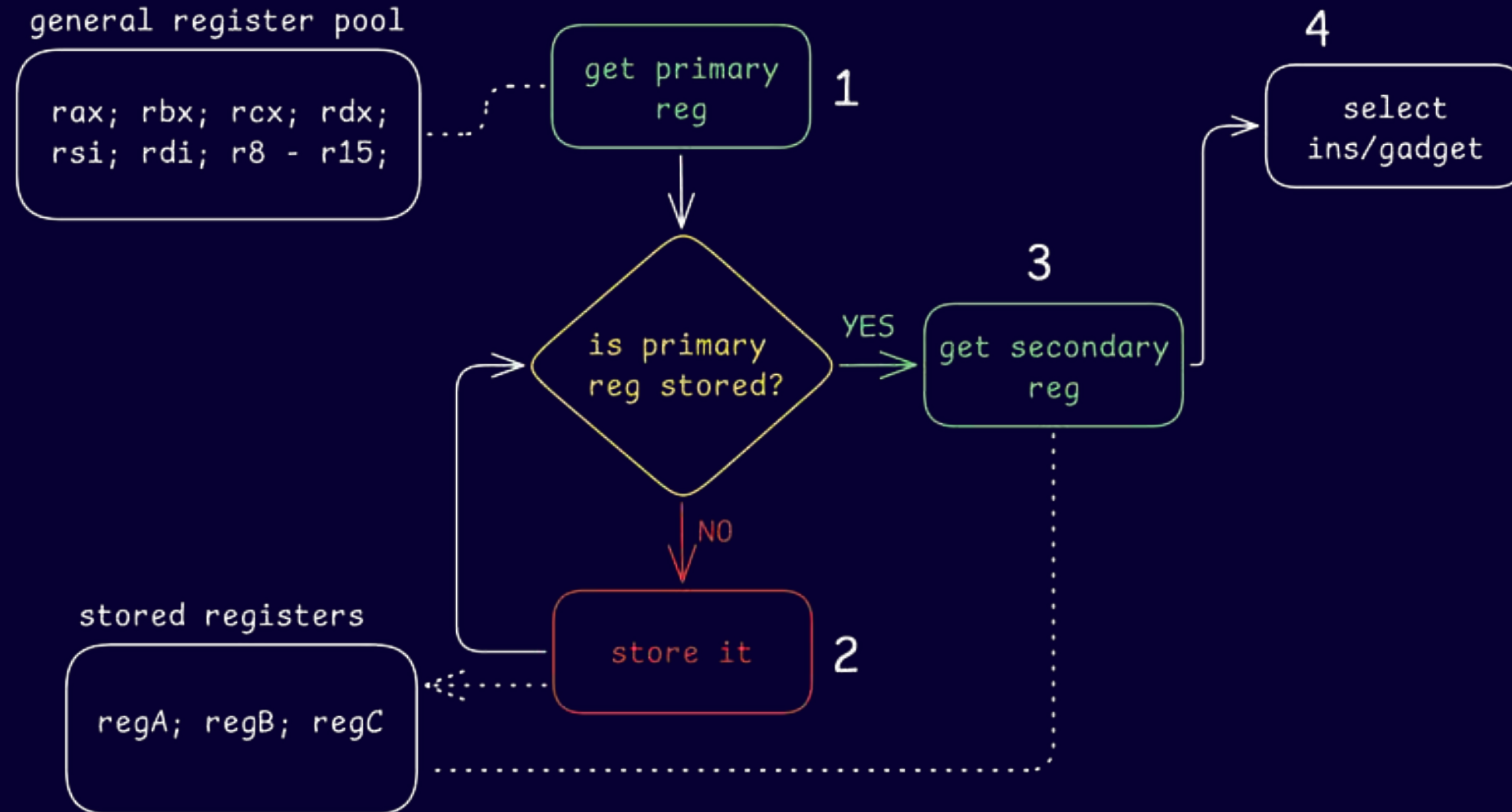
# Chameleon | Shellcode Generation



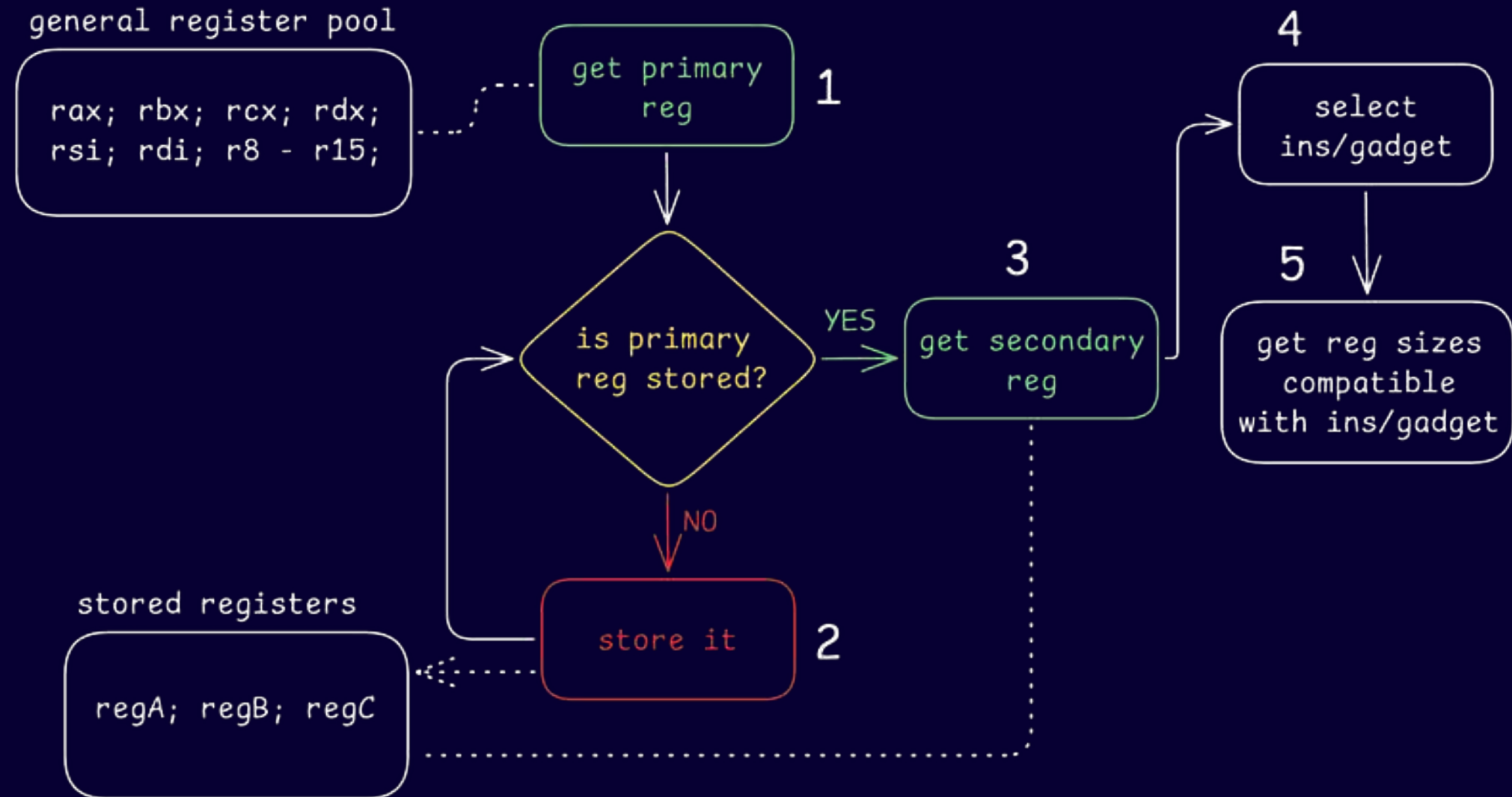
# Chameleon | Shellcode Generation



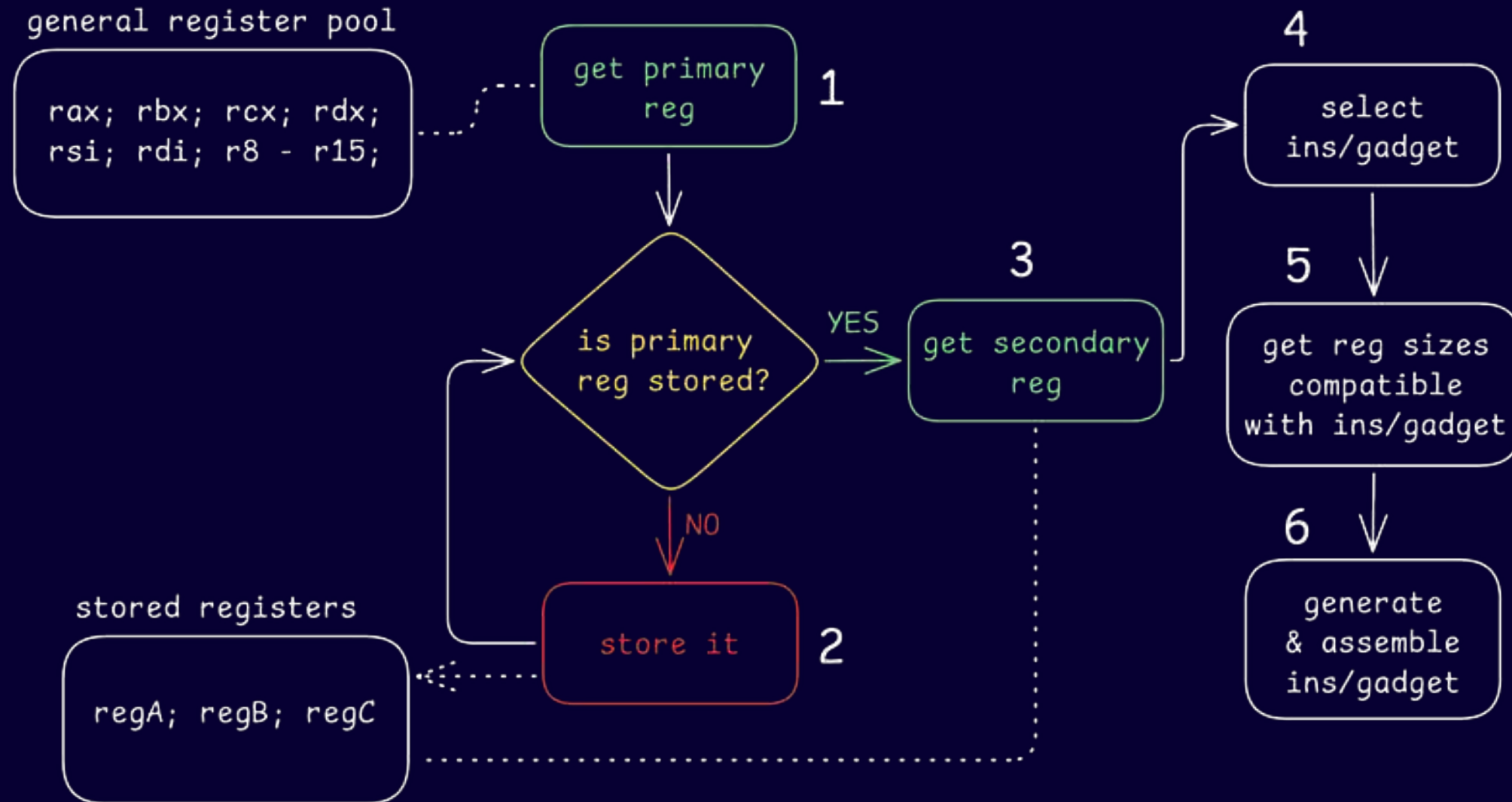
# Chameleon | Shellcode Generation



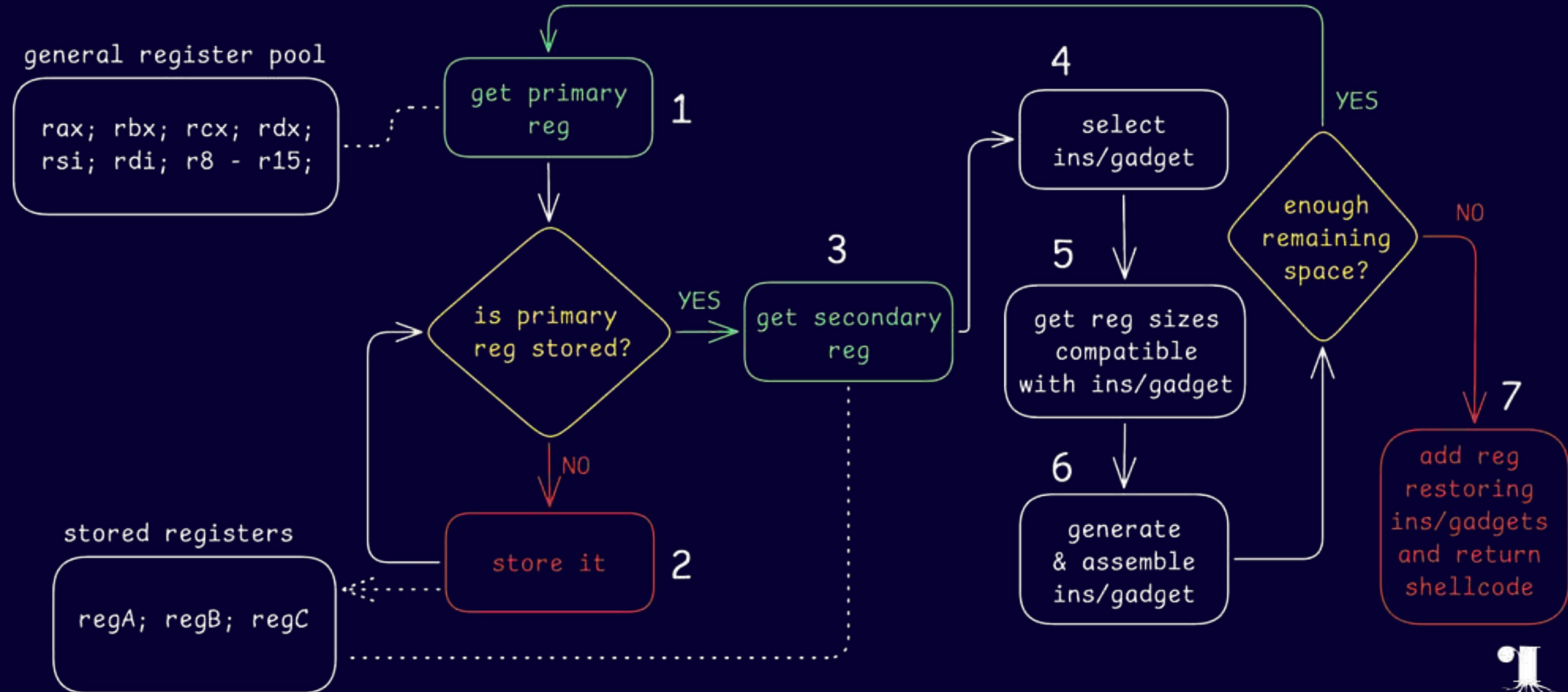
# Chameleon | Shellcode Generation



# Chameleon | Shellcode Generation



# Chameleon | Shellcode Generation





# Chameleon | Shellcode Generation



- Instruction implementation example:

```
( lambda reg, sec_reg: f"mov {reg}, rsp;", REG_64, 1 )
```



# Chameleon | Shellcode Generation



- Gadget implementation example:

```
( self.br_check_regs, REG_ALL, 1 )
def br_check_regs(self, reg: str, sec_reg: str) ->
str:
    ...
    gadget += f"cmp {reg}, {sec_reg};"
    gadget += pseudo random cjmp gadget to label
    gadget += get_n_junk_ins(reg, sec_reg, n)
    gadget += label
    ...
```



# Chameleon | Shellcode Generation



- The generated shellcode is later hardcoded into the block asm using db.



# Chameleon | Assembly Process

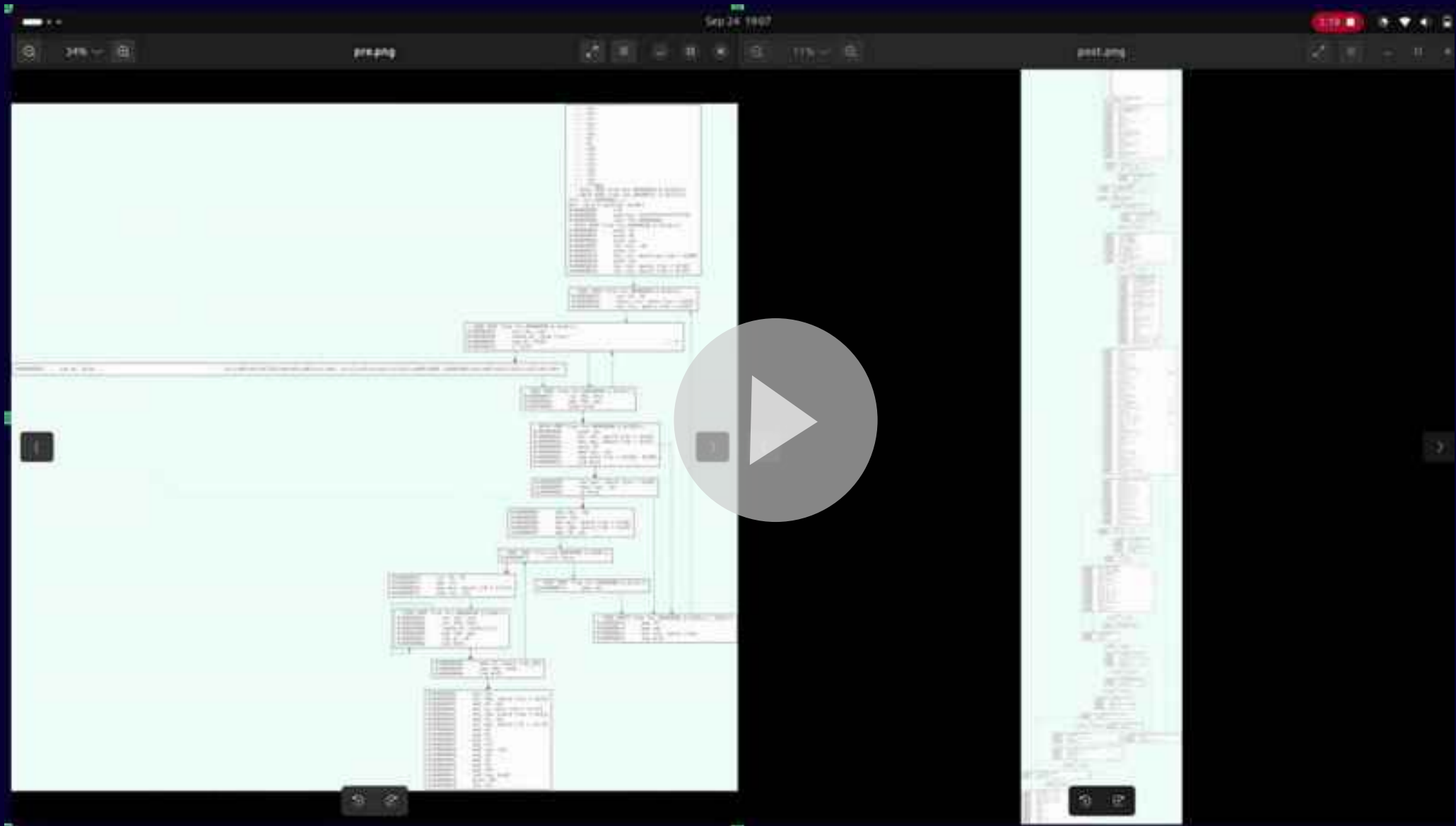


- Nasm compatible asm header:

```
BITS 64
default rel
global _start
section .text
_start:
mutated_sc
```

```
nasm -f bin file.asm -o output.bin
```







```
Oct 1 2004
gum31@ubuntu: ~/chameleon
gum31@ubuntu: ~/chameleon
gum31@ubuntu: ~/chameleon-tests/ufn_poc

[+] labels generated for each basic block
[v] mutations for block_0x1000:
[v] mutation at 0x1014:
old      | new
-----|-----
mov rdi, rax | push rax
            | 4883ec0848890424488d04044883ec084c8
            | 93c244181e7000080004883ec0848093424
            | 48c7c6ba00000004883ec084c892c244c8d2
            | c764989c54883ec084889142401caedf0fa
            | e389c6f7c607000000740383e6f80883ec0
            | 848891c24488b5c2408488d55e048c7c6ae
            | 000000678d1c5b49c7c71800000081e6fdc
            | 809eabef0000000678d3470d1e6ffc683ce
            | 0249ffcfc4983ff0075e14883ec0848893c2
            | 466f7c78000750466bfe60066c1ef024428
            | fe21d84889df48f7c7ff0f000074074881e
            | 700f0fffff83ceff4889d88b442410d1e848
            | c7c2e500000048c7c09e0b000090488b3c2
            | 44883c408488b1c244883c408488b142448
            | 83c4884c8b2c244883c408488b34244883c
            | 4084c8b3c244883c408488b04244883c408
            | pop rdi

[v] mutations for block_0x101c:
```



Esitorio

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Nuevo - Ordenar - Ver - Detalles

Nombre	Fecha de modificación	Tipo	Tamaño
Desktop	25/09/2025 11:16	Carpeta de archivos	
ufo_repo_distributable_optimized	22/09/2025 17:40	Carpeta de archivos	
p_UFO_x64_unprivileged.bin	01/10/2025 20:08	Archivo BIN	7 KB

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ufo\_repo\_distrib  
Este equipo  
Unidad de DVD  
Red  
Inicio  
3 elementos

Activar Windows  
Ve a Configuración para activar Windows.

23°C  
Mayorm. soleado  
Buscar  
20:11  
01/10/2025





Any questions?



<https://github.com/gum3t/chameleon>

