VALIDATION ENGINEER - DHRUV KUMAR PANDEY

CAREER OBJECTIVE

• To be involved in work where I can utilize skill and creatively involved with the system that effectively contributes to growth of organization.

WORK EXPERIENCE

- Currently Working as a "Software Engineer" in "Eximius Design Pvt Ltd" since 26'August 2019 to till date.
- Worked as a "Software engineer" in "Binsoft Techno Solutions" from 1
 August 2017 to 8 August 2019.

SKILL SUMMARY

- Having 2+ years of experience in embedded software Independent Verification and Validation.
- Strong programming skills in C
- Good Knowledge on software life cycle (SDLC, STLC and Bug Life Cycle).
- Involved in Functional testing, Structural testing, and Regression Testing
- Good Experience in structural coverage analysis (Statement, Branch and MC/DC)
- Involved in **Testing and Analysis**.
- Skilled in Performing White Box Testing.
- Experience in Unit Testing and Integration Testing.
- Involved in Code Based Testing.
- Involved in Rational Test Real Time (RTRT) , VectorCast.
- Ample knowledge of SDLC (Software Development Life Cycle).
- Involved in raising Technical Issue Report (TIR), Problem Report(PR)
- Reporting and Documentation

Programming Language	С
Tools	VectorCast, Visual Studio, Gerrit, Jenkins, Jira
Compiler/Editors	GNU C Compiler, gdb Debugger
Operating System	Windows, Linux
Microcontroller	8051
Communication Protocols	SPI, I2C
IDE	Keil-u-Vision

PROJECTS

1. Boot loader for multi SoC Drive OS system

Tested Scenarios: Module Testing / Integration Testing **Tools used:** Vector Cast, JIRA, Gerrit & Jenkins, Jama

Description: Bootloader Quick Boot is used to call hypervisor and is called by MicroBoot.It is divided into three phase: Quickboot, Partition Loader and Operating system loader.

Responsibilities:

- Analyzing and creating plan to achieve the software requirements.
- Developing Test cases and Test procedures as per Software unit design document by following ISO26262 Standards.
- Achieve 100% code coverage, Branch coverage, MCDC & functional tests.
- Executing Test cases in VectorCast for both Host and Target environment.
- Involved in Regression and Retesting.
- Generate Test results and Test Summary reports.
- Develop Integration test as per requirements.
- Achieve functionality coverage as per the requirements.

2. ADAPTIVE CRUISE CONTROL

Tested Scenarios: Unit Testing

Tools used: IBM Rational Test Real Time(RTRT), ATGS,Code Wright

Description: The objective of the project is to carry out Independent Software Verification and Validation (IV&V) activities for ADAPTIVE CRUISE CONTROL SYSTEM. ACC is controlled by the Engine Control Unit (ECU) which receives information from the ACC module and Instrument Cluster and control the vehicle's speed based on this information.

Responsibilities:

- Creating Software Unit Test Plan(SUP).
- Creating requirement based test cases as per the test plan for each unit.
- Execution of Test Cases using RTRT.
- Requirement Coverage analysis.
- Structural Coverage analysis.
- Identify defects and raise the issue.
- Technical Issue Report.
- Preparing Test results and Test Summary Report

3. AIRCRAFT CONTROL SYSTEM

Tested Scenarios: Unit Testing

Tools used: IBM Rational Test Real Time(RTRT), ATGS,Code Wright

Description: The objective of project is to carry out unit and Integration testing activities for AIRCRAFT CONTROL SYSTEM. The task involved in this project is unit test as per customer provided standards. The Unit Test Activity performed was done according to the **DO-178B** Guidelines and involved in all coverage compliant to the **LEVEL A** software

Responsibilities:

- Creating Software Unit Test Plan(SUP).
- Creating requirement based test cases as per the test plan for each unit.
- Execution of Test Cases using RTRT.
- Requirement Coverage analysis.
- Structural Coverage analysis.
- · Identify defects and raise the issue.
- · Technical Issue Report.
- Preparing Test results and Test Summary Report

EDUCATIONAL QUALIFICATION

- B.E in AERONAUTICAL ENGINEERING (Aeromechanical) with 61% from The Aeronautical Society of India.
- H.S.C (10+2) with aggregate 73% from State Board Uttar Pradesh.
- S.S.C(10) with aggregate 65% from State Board Uttar Pradesh.

PERSONAL DETAILS

Name Dhruv Kumar Pandey

• Father Name Dayashankar Pandey

• **Gender** Male

Marital Status Single

Nationality Indian

• Languages English, Hindi

Known

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• I hereby declare that the above statements are true and complete to the best of my knowledge and belief.

PLACE: BANGALORE

DATE: