For each member in your team, provide 1 paragraph detailing what parts of the lab that member implemented / researched. (You may skip this question if you are doing the lab by yourself).

Shweta – I have modified the file vmx.c to implement the logic to find the total number of exits, total number of cycles, total number of individual exits and total number of individual cycles as per the assignment requirement. I have helped to prepare this document.

Radhika – I have researched on the modifications to be made to the code and the logic to be used for the implementation of total number of exits, total number of cycles, total number of individual exits and total number of individual cycles as per the assignment requirements. I have written also this document.

Describe in detail the steps you used to complete the assignment. Consider your reader to be someone skilled in software development but otherwise unfamiliar with the assignment. Good answers to this question will be recipes that someone can follow to reproduce your development steps.

Open linux→arch→x86→kvm→vmx.c

Make changes to implement the requirement in the vmx.c to add counters into the KVM that track the following information:

Total number of exits (for each type of exit KVM enables)

Max number of CPU cycles for each exit type

Min number of CPU cycles for each exit type

Average number of CPU cycles for each exit type

Total amount of cycles spent processing all exits

Compile the kernel and its modules by checking number of processing units available

nproc [ to know the number of processing units, in our case it was 4]

sudo make -j 4 && sudo make modules\_install -j 4 && sudo make install -j 4

Next reboot the system and start a virtual machine in order to trigger the exits.

Now, execute dmesg to see the output.

Note whether or not you used a larger count of exits between outputs (1000 or 2000 exits vs the suggested 500).

No, we used only 500 for the count of exits.

Include a sample of your print output from dmesg (take just one set of outputs).

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| Tile |
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What did you learn from the count of exits? Was the count what you expected? If not, why not?

The total count was higher than what we expected, and specifically MSR\_WRITE, IO\_INSTRUCTION, CPUID had a high no of exits count.

The reason for high count is that, the virtual machine is trying to execute these operations a lot of times, and each time it is exiting to the KVM.