RAJITH RADHAKRISHNAN

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OBJECTIVE

I am seeking an internship opportunity to broaden my knowledge of any technologies as well as apply my skills towards practical applications.

INTERESTS

Hardware Engineering, VLSI design, IoT, Embedded Hardware and Software design, Computer Vision

EDUCATION

Stony Brook University , College of Engineering and Applied Sciences — **Stony Brook, NY, USA**

Master of Engineering in Electrical Engineering		May 2018
Bachelor of Engineering in Electrical Engineering (Honors Program)	GPA: 3.85/4.00	May 2017
Minor: Physics	Dean's List:	Fall 2013 – Present

RELEVANT COURSEWORK

Digital design using VHDL to program PLDs and FPGAs, Electronics, Advanced VLSI Testing, Random signals and system, Semiconductor device physics, Integrated Electronic devices and circuits (VLSI), Embedded micro-processor systems design, DSP, , Computer Vision, Modern Circuit Board Design Data structures and algorithms using Java

SOFTWARE & ENGINEERING PROFICIENCES

Languages: VHDL, Java, C/C++, Python, MATLAB, Verilog, OpenCV, Assembly, HTML, Java Script, Bash Script
Familiar with: Virtuoso, Linux, Eagle, AutoCAD, LabVIEW, PSpice, Hspice/Spectre, Aldec, MS office, Visio, Atom

EXPERIENCE

Brookhaven National Laboratory — **Brookhaven, NY**
Engineering Intern — Summer 2016

- Wrote program to control level probes and flow meters to maintain liquid helium levels in superconducting magnets.
- Collected quantitative data using Programmable Logic Controllers (PLCs) and Field Programmable Gate Arrays (FPGA)
- Developed System to obtain and process data from Strain gauge sensors.
- Wrote software to test IRCMS cancer research magnet for next generation oncology treatment.

Brookhaven National Laboratory — **Brookhaven, NY**
Engineering Intern — Summer 2015

- Wrote a custom LabVIEW program to analyze data from superconducting magnets for the CERN particle accelerator.
- Wrote DAQ program for NI PXIe 1075 to collect voltage and current measurements from Magnets.
- Optimized the user interface to handle millions of data points gathered every instant.
- Devised a means of assessing magnet quenching and system failure.

Department of Electrical Engineering, Stony Brook University — **Stony Brook, NY**
Teaching Assistant, Digital System Design — Fall 2014

- Instructed a classroom of 20 students on designing applications using logic gates.
- Assisted individual design groups in hardware testing, which included optimization of the hardware-software interface.

SELECT PROJECTS

- Designed a talking voltmeter intended to test battery cell charge retention in electric vehicles using microprocessors.
- Created a display system to monitor the New York City Metropolitan Transportation Authority (MTA) bus schedules by programming an Intel Edison board using Python script.
- Power Harvesting and Telemetry in CMOS for Implanted Devices
- Fiber optic laser delivery system with smartphone based temperature monitoring and pulse control for treating skin lesions and other skin disorders.

EXTRACURRICULAR ACTIVITIES

Eta Kappa Nu (HKN) Honor Society for Engineers, Stony Brook Chapter, Member — 2015 – Present
Institute of Electrical and Electronics Engineers (IEEE), Stony Brook Chapter, Member — 2013 – Present