



Radxa CM3 IO Board

schematic

V1.33

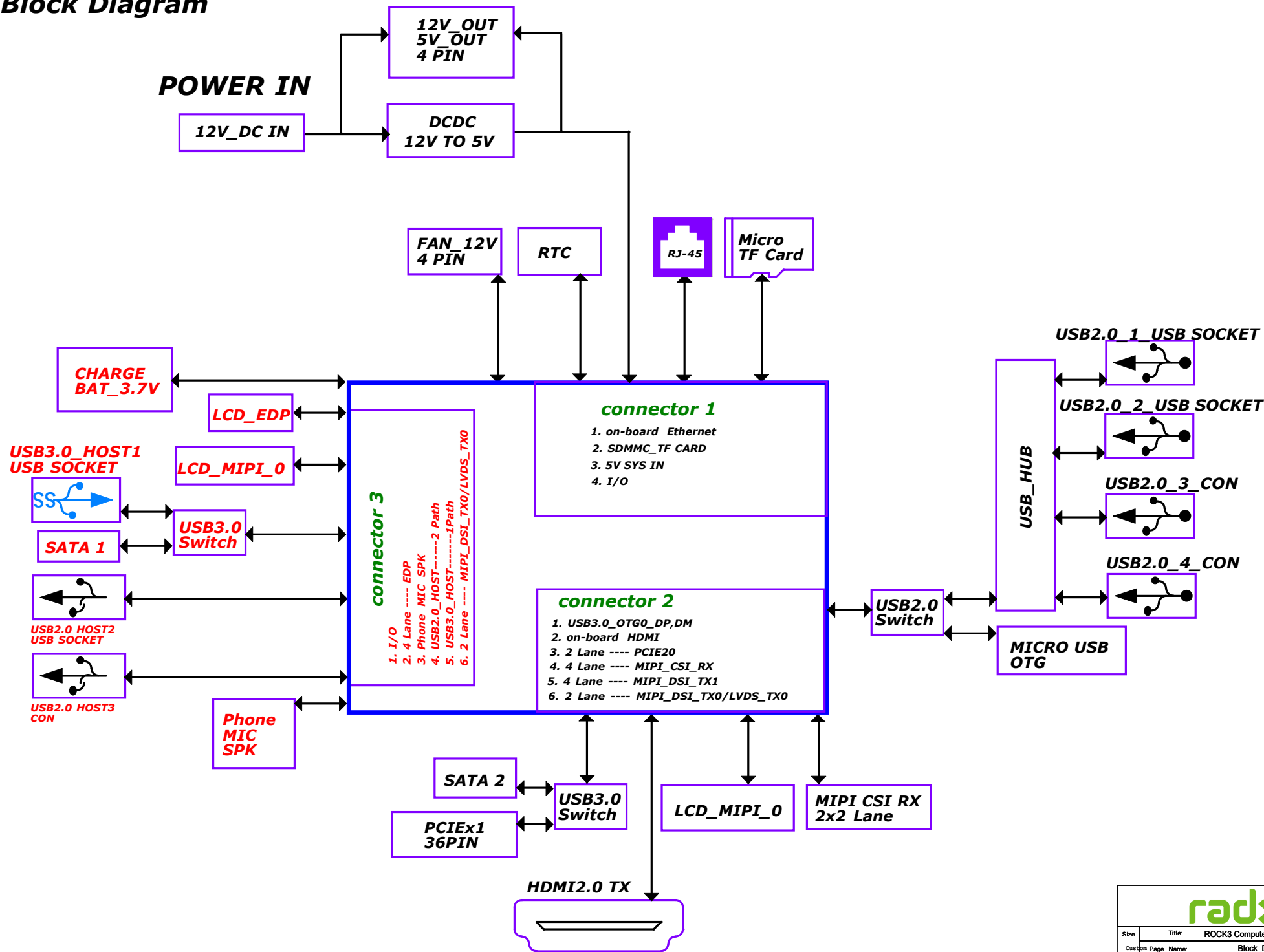
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17.GPIO+VCC_Connector
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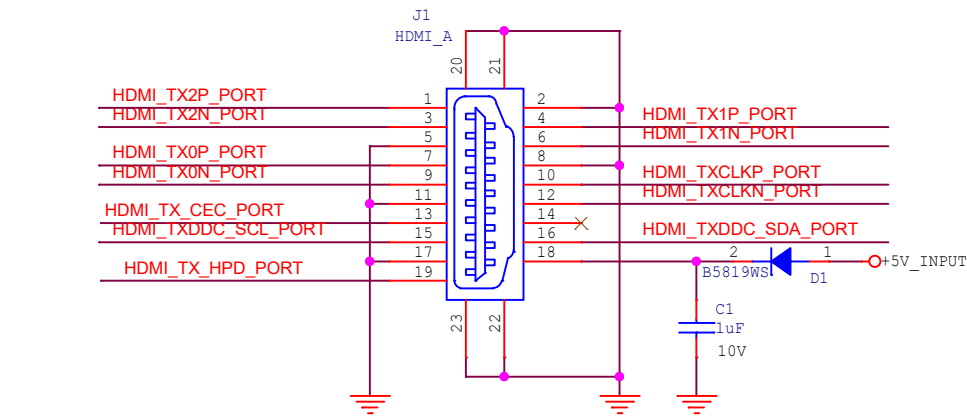
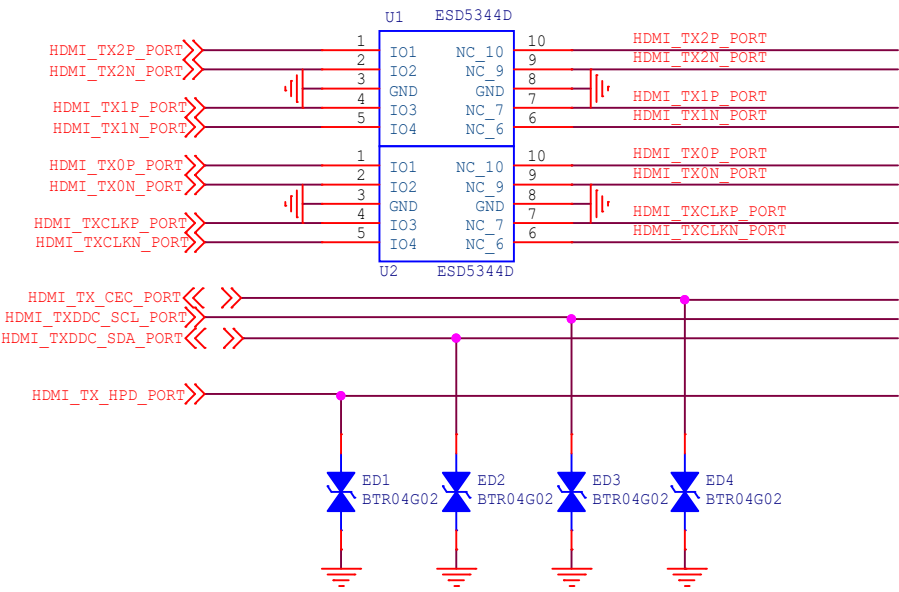


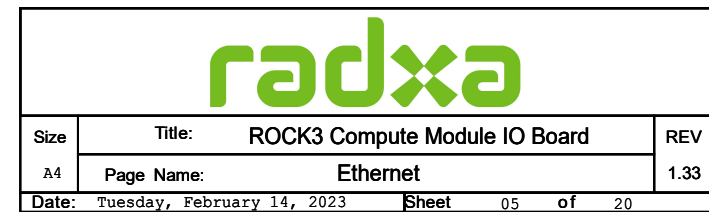
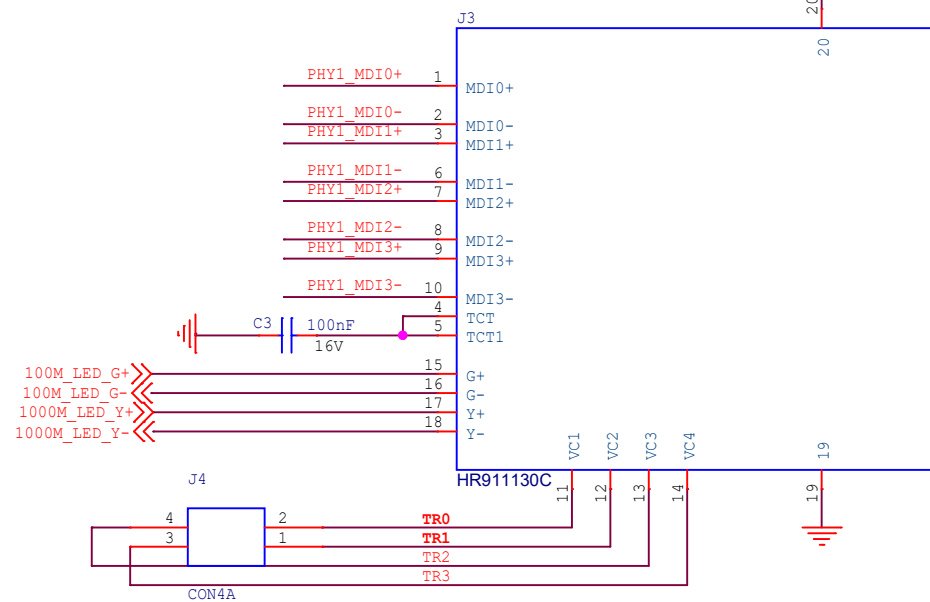
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Block Diagram

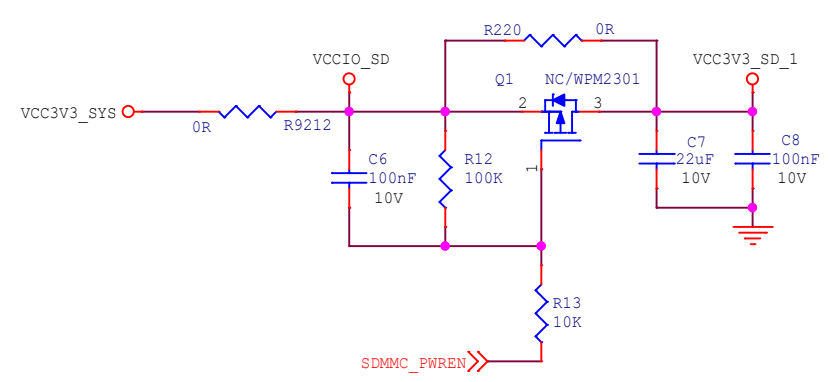
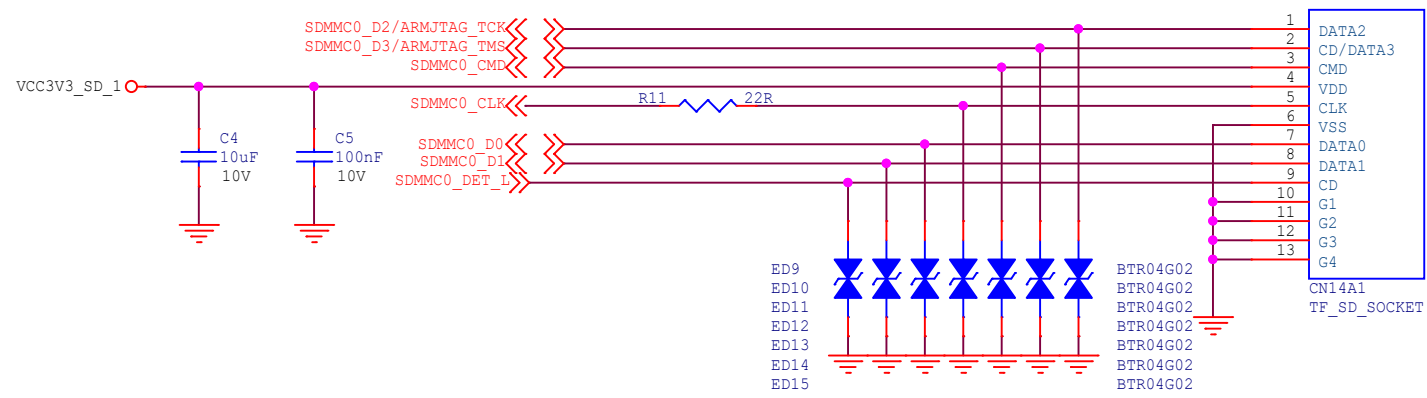


HDMI2.0 TX

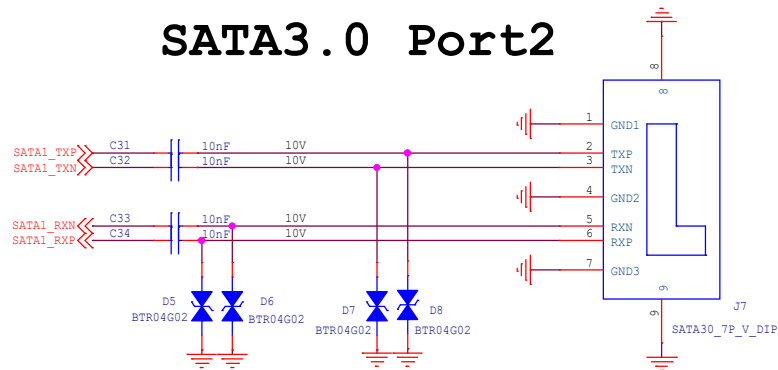




MicroSD Card

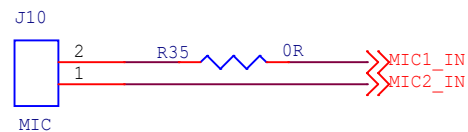
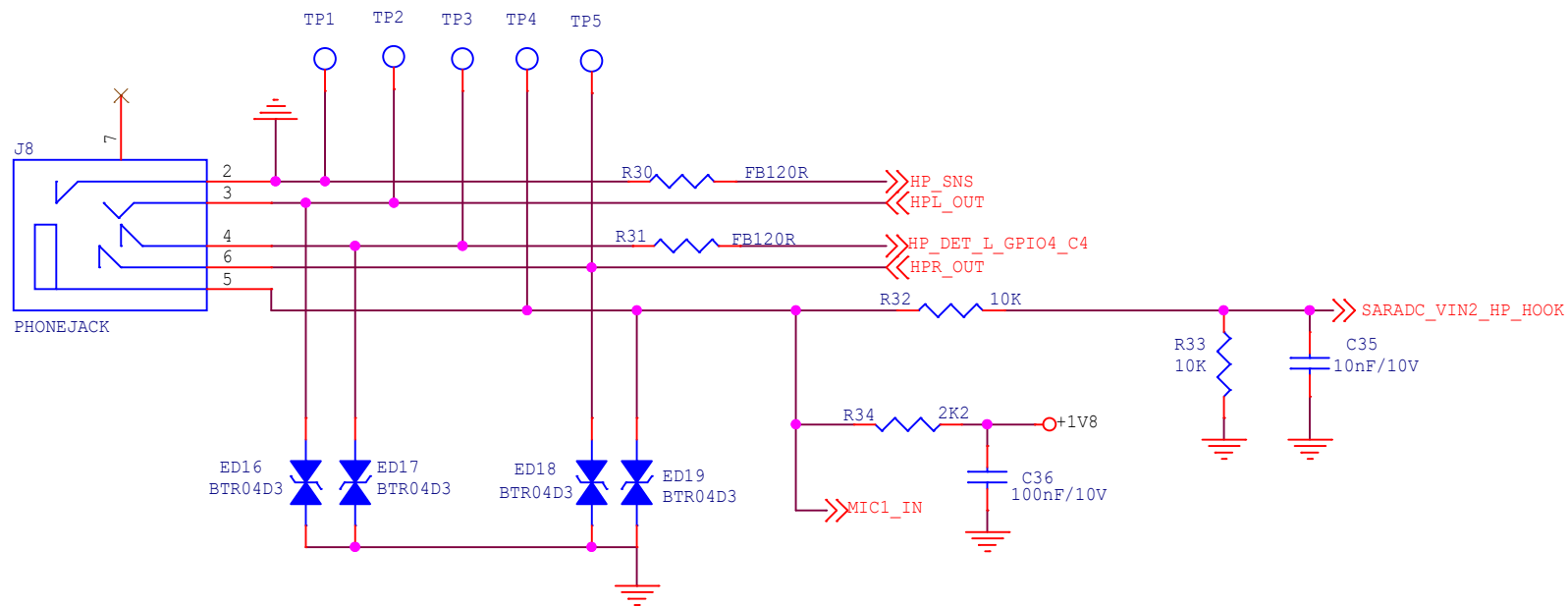


SEL (0=SW0, 1=SW1)



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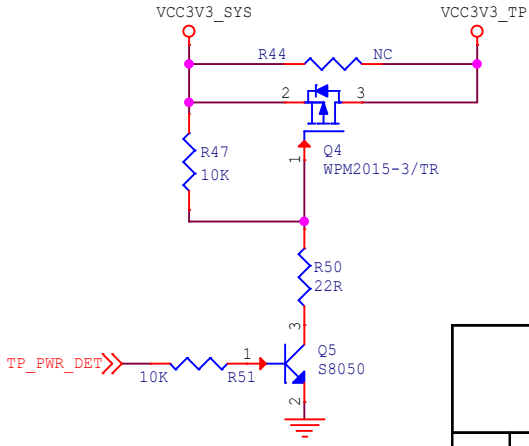
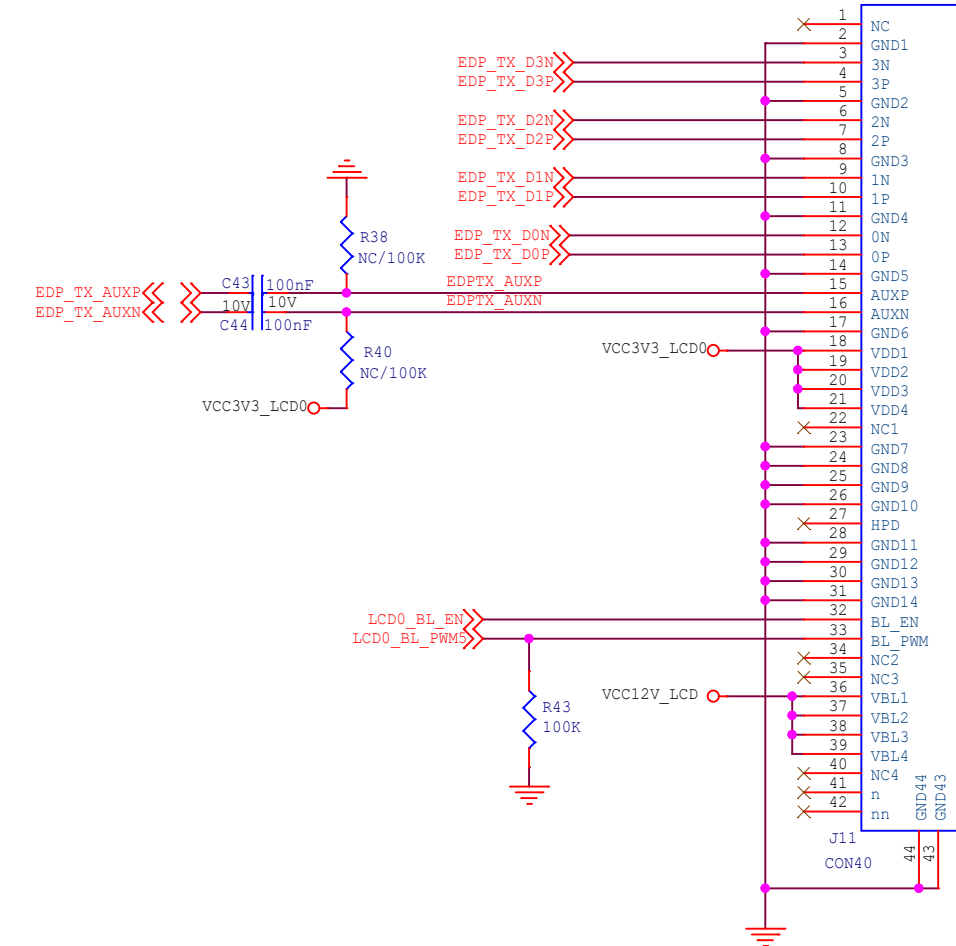
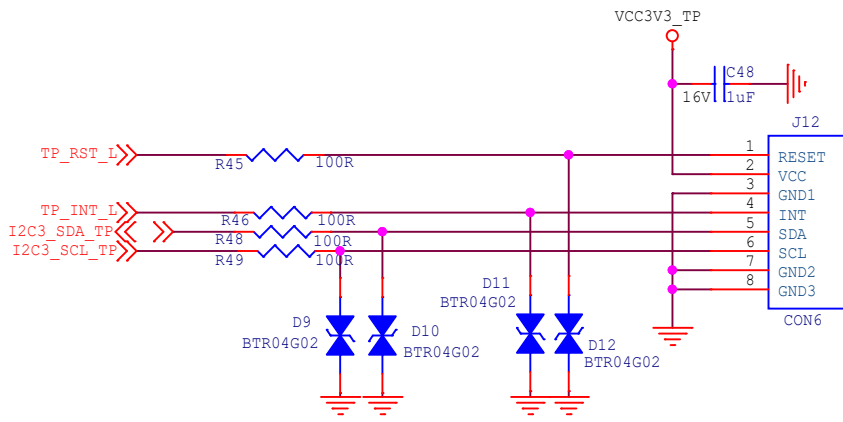
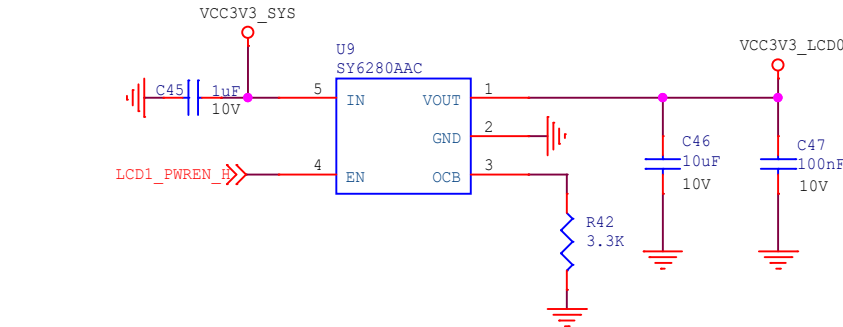
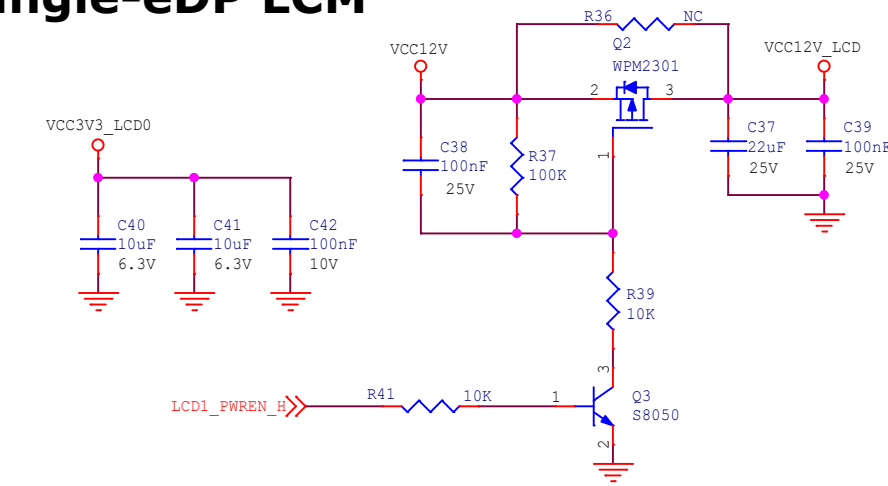
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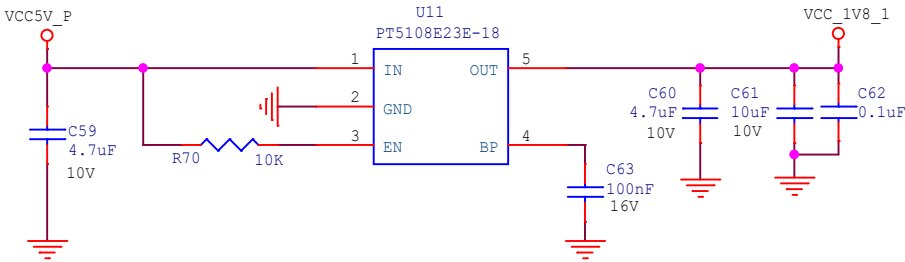
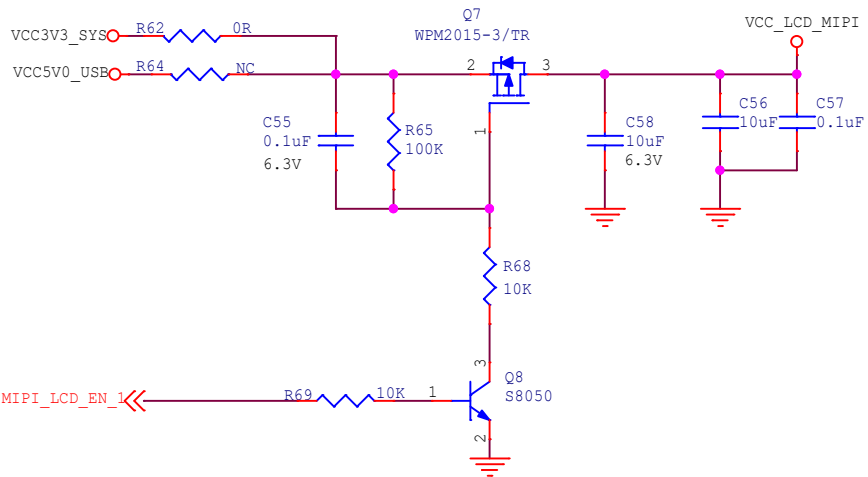
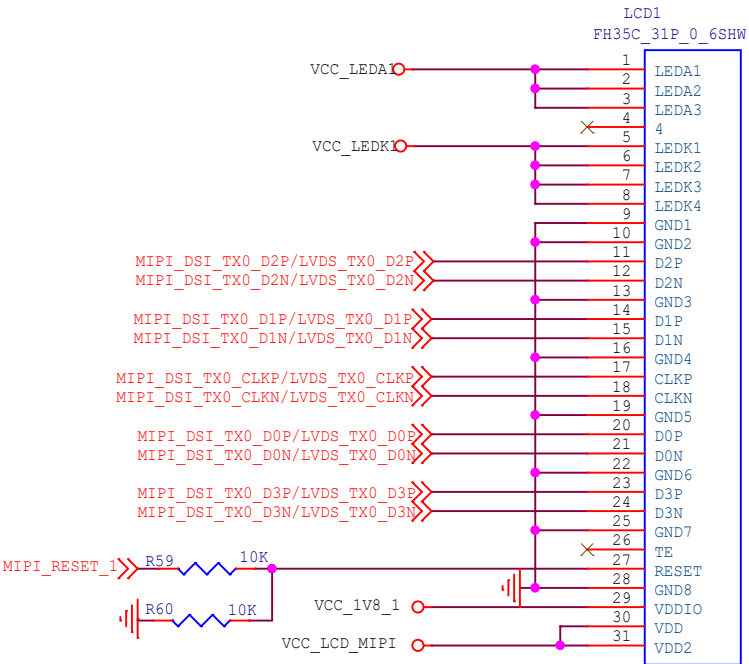
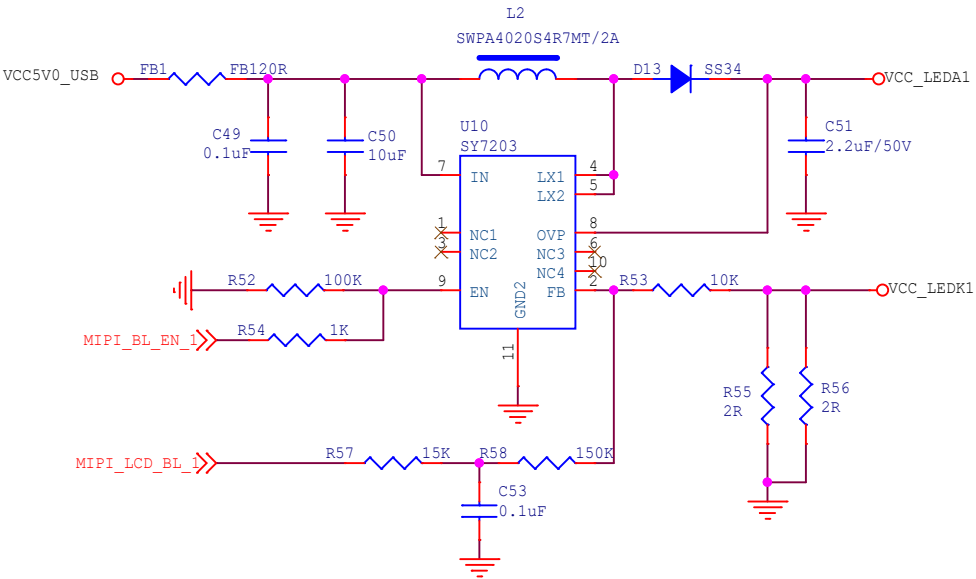
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Single-eDP LCM

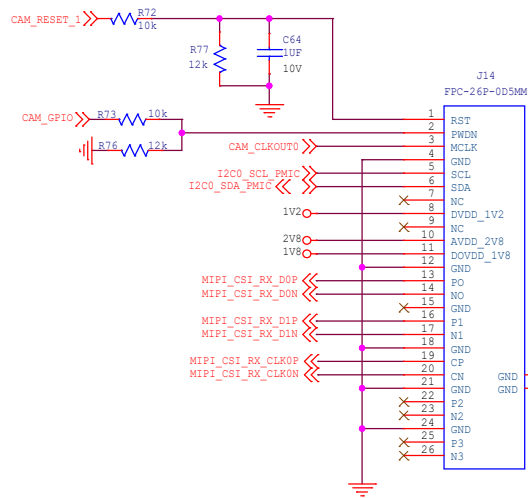


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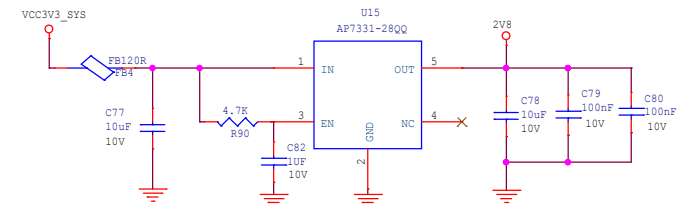
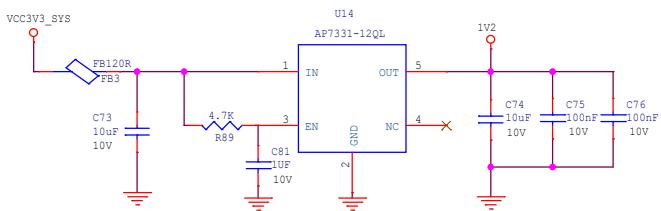
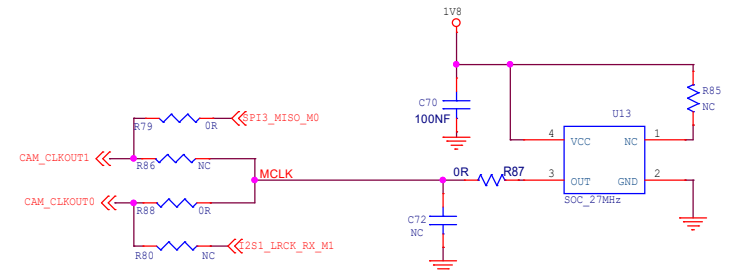
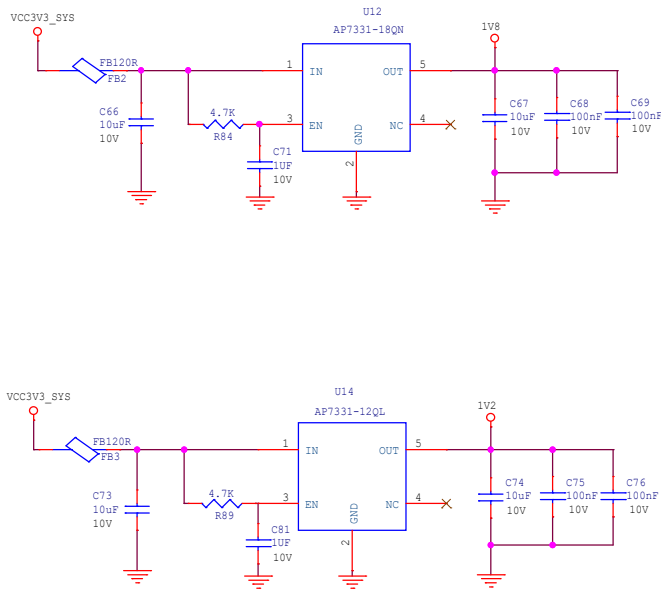
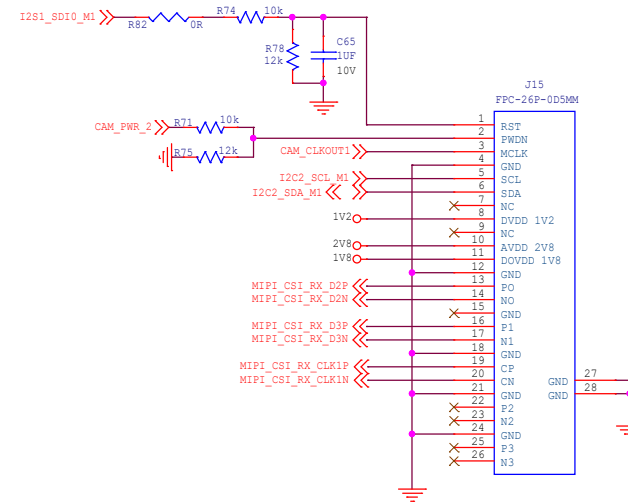
MIPI_LCD_TX0



MIPI_CSI_RX 2Lanes

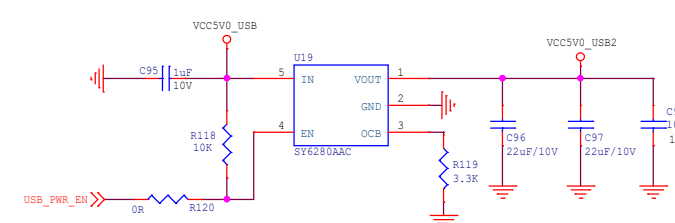
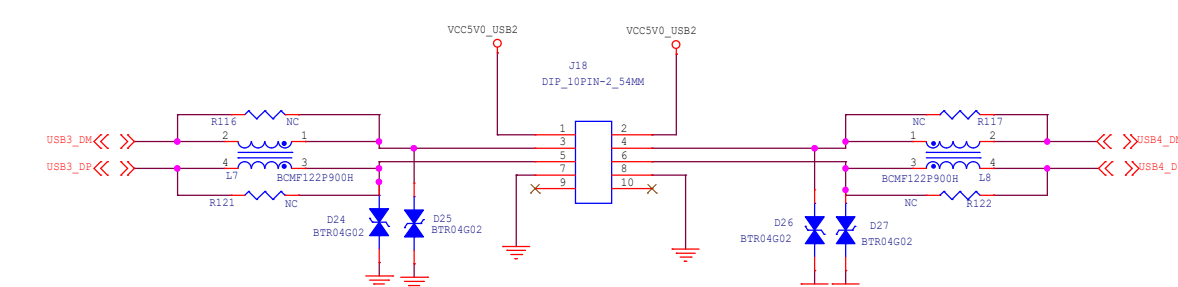
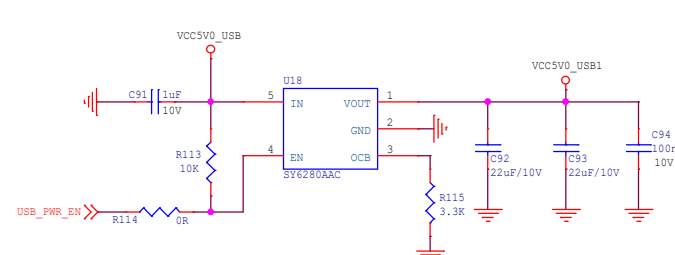
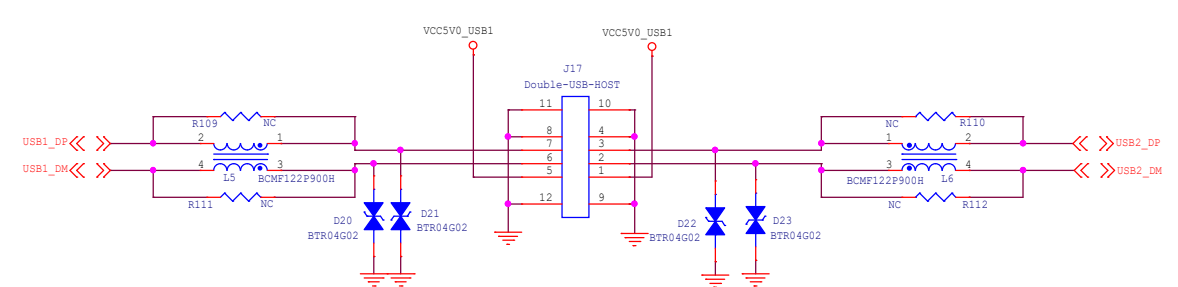
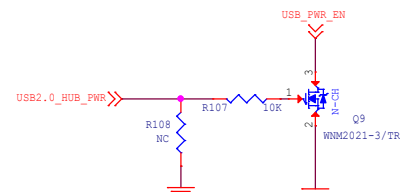
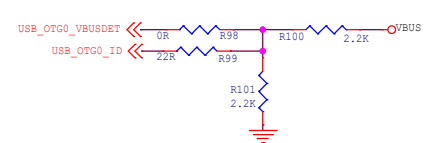
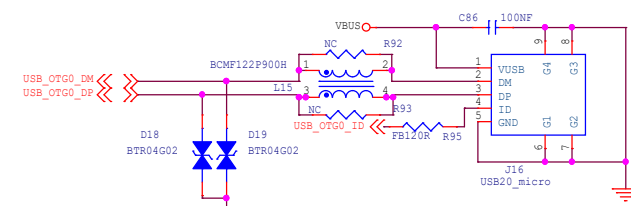
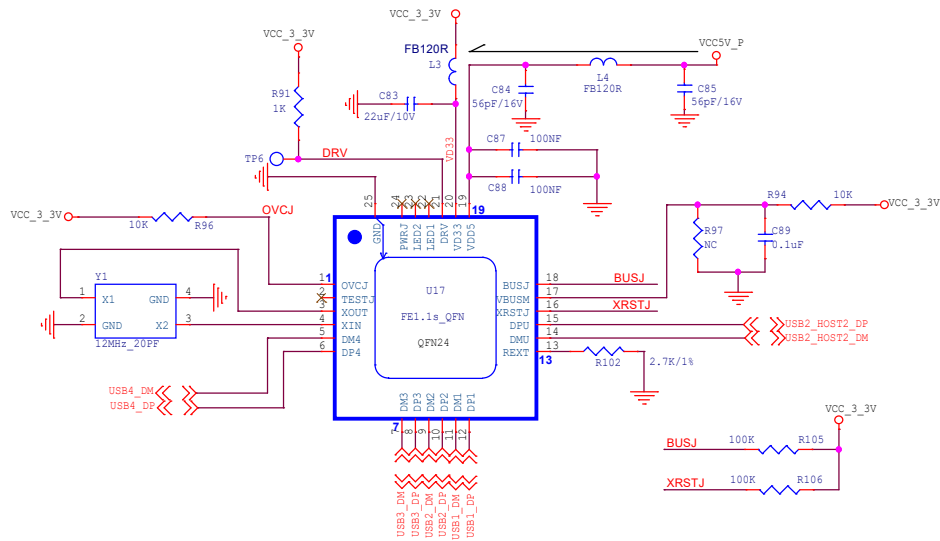


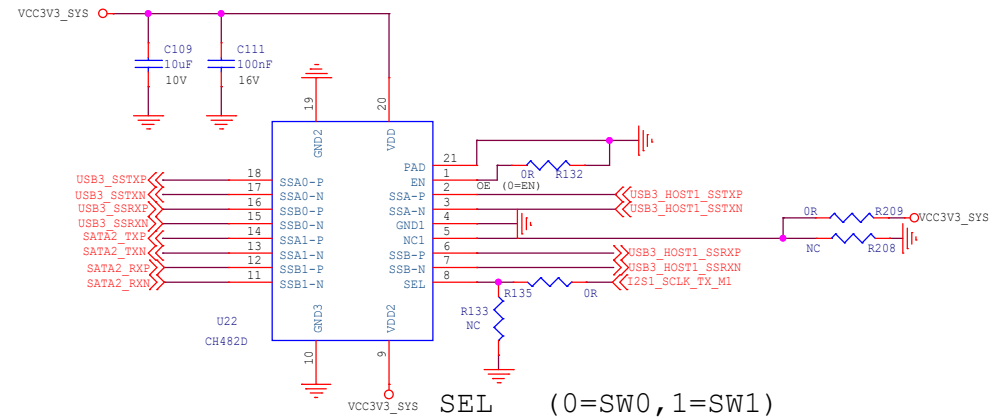
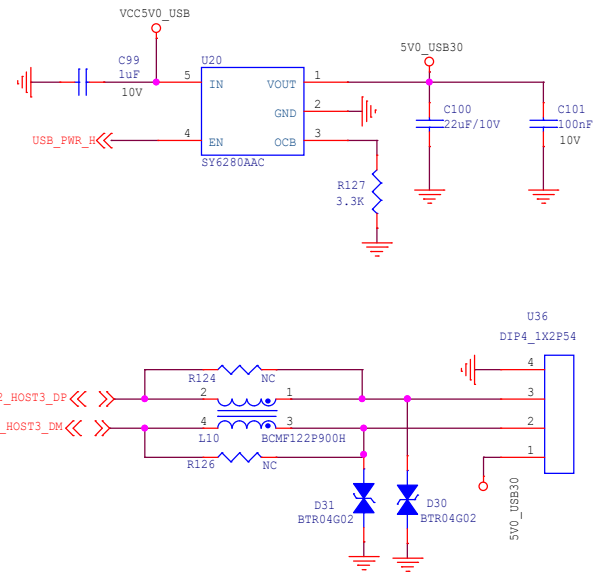
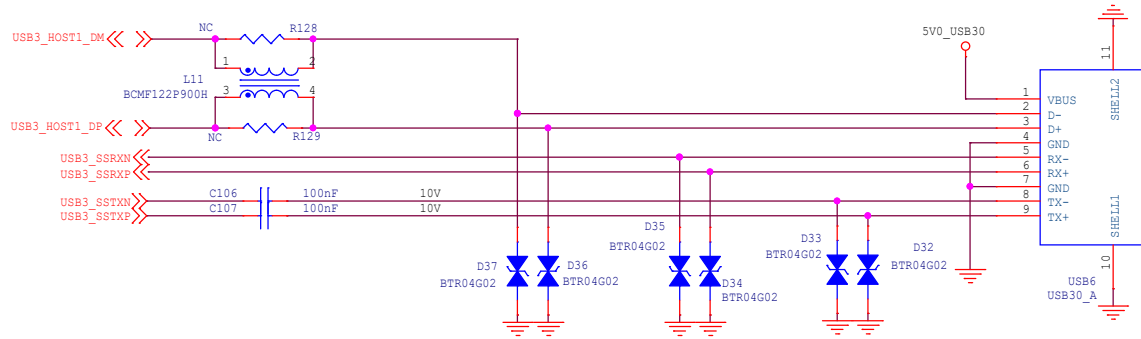
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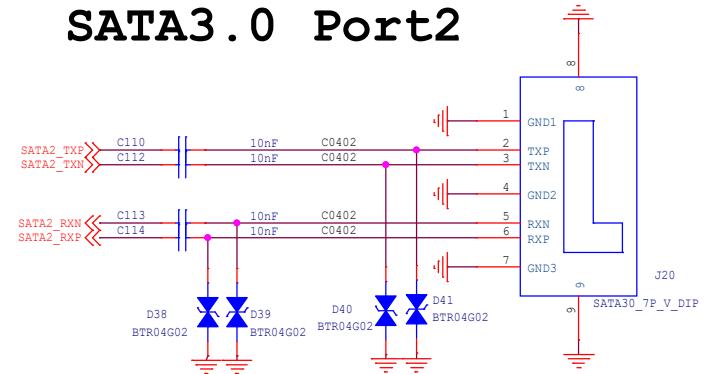
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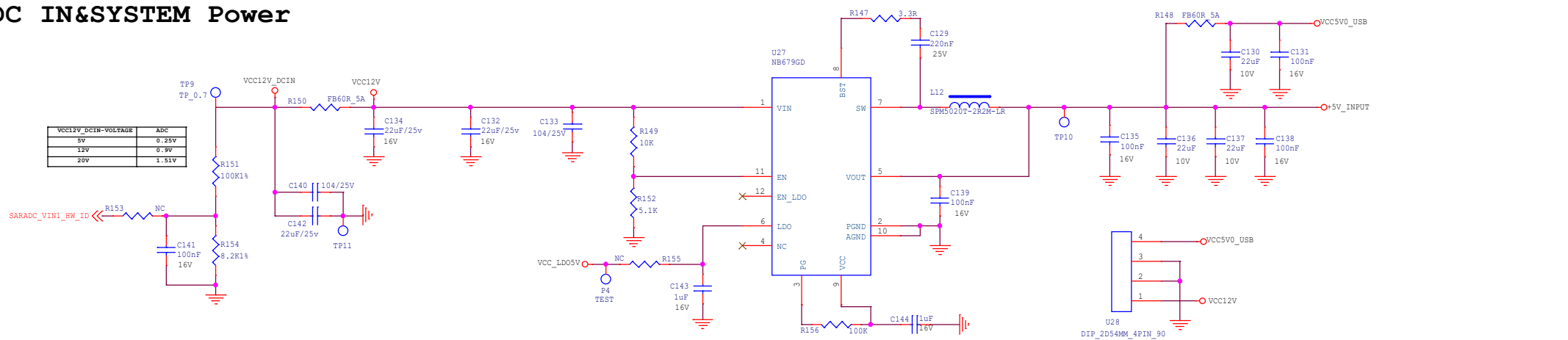
SATA3.0 Port2



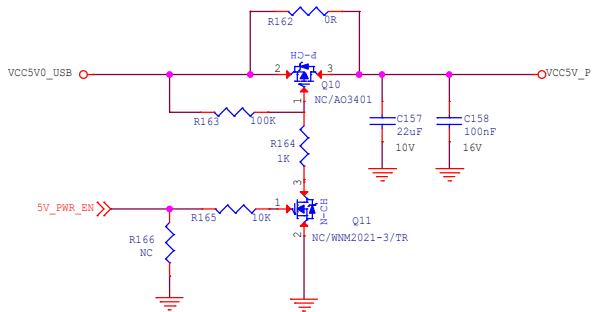
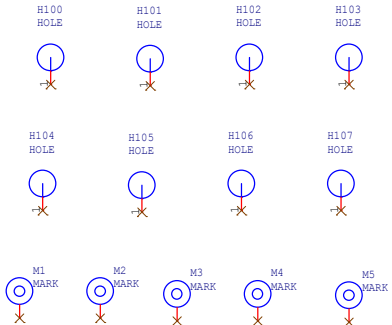
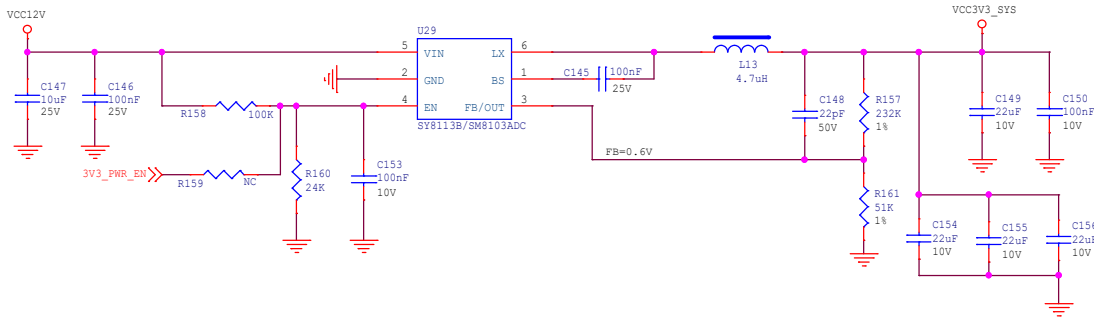
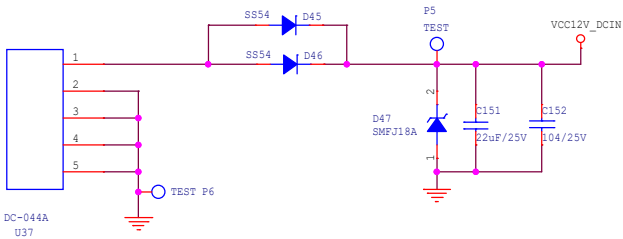
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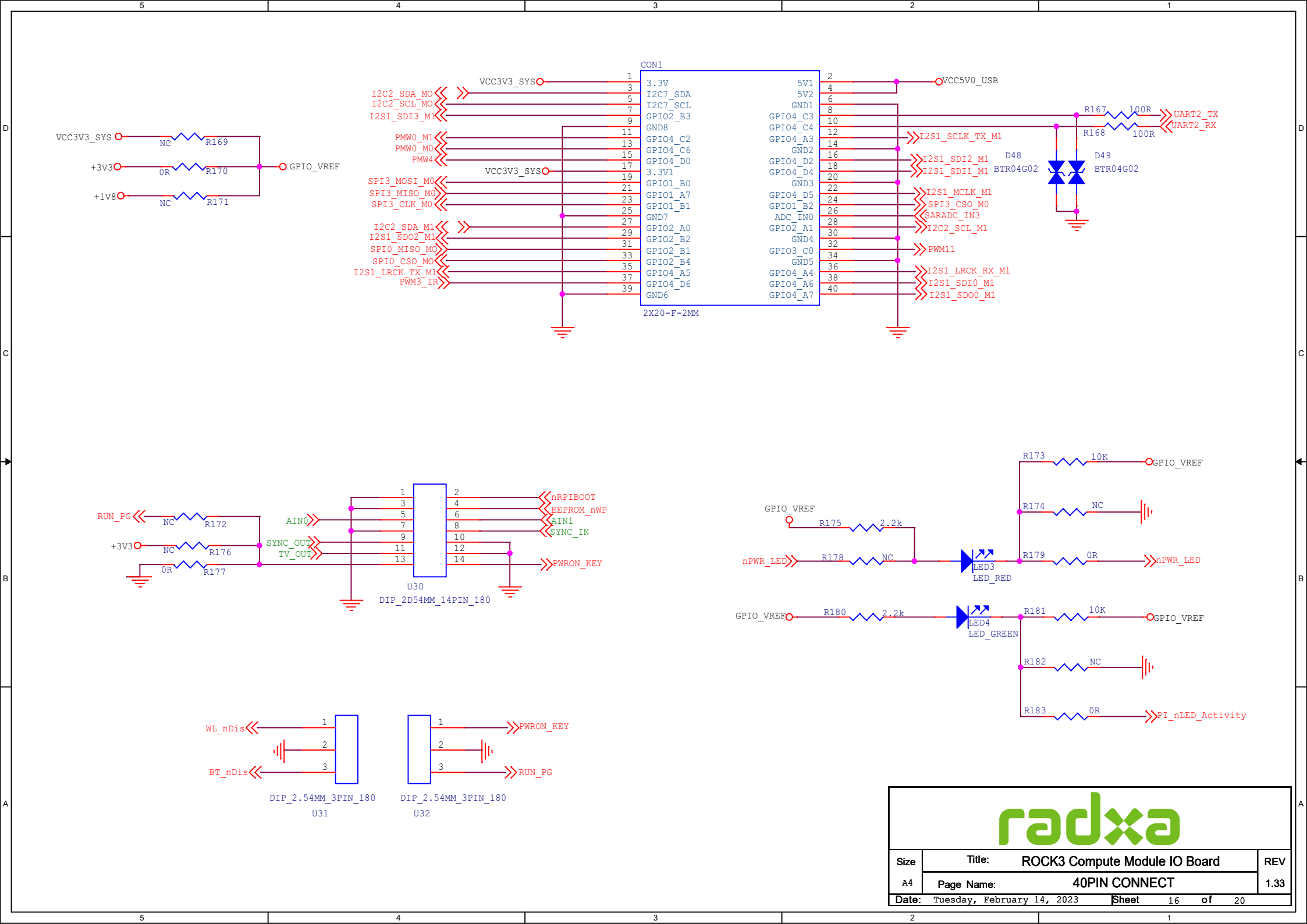
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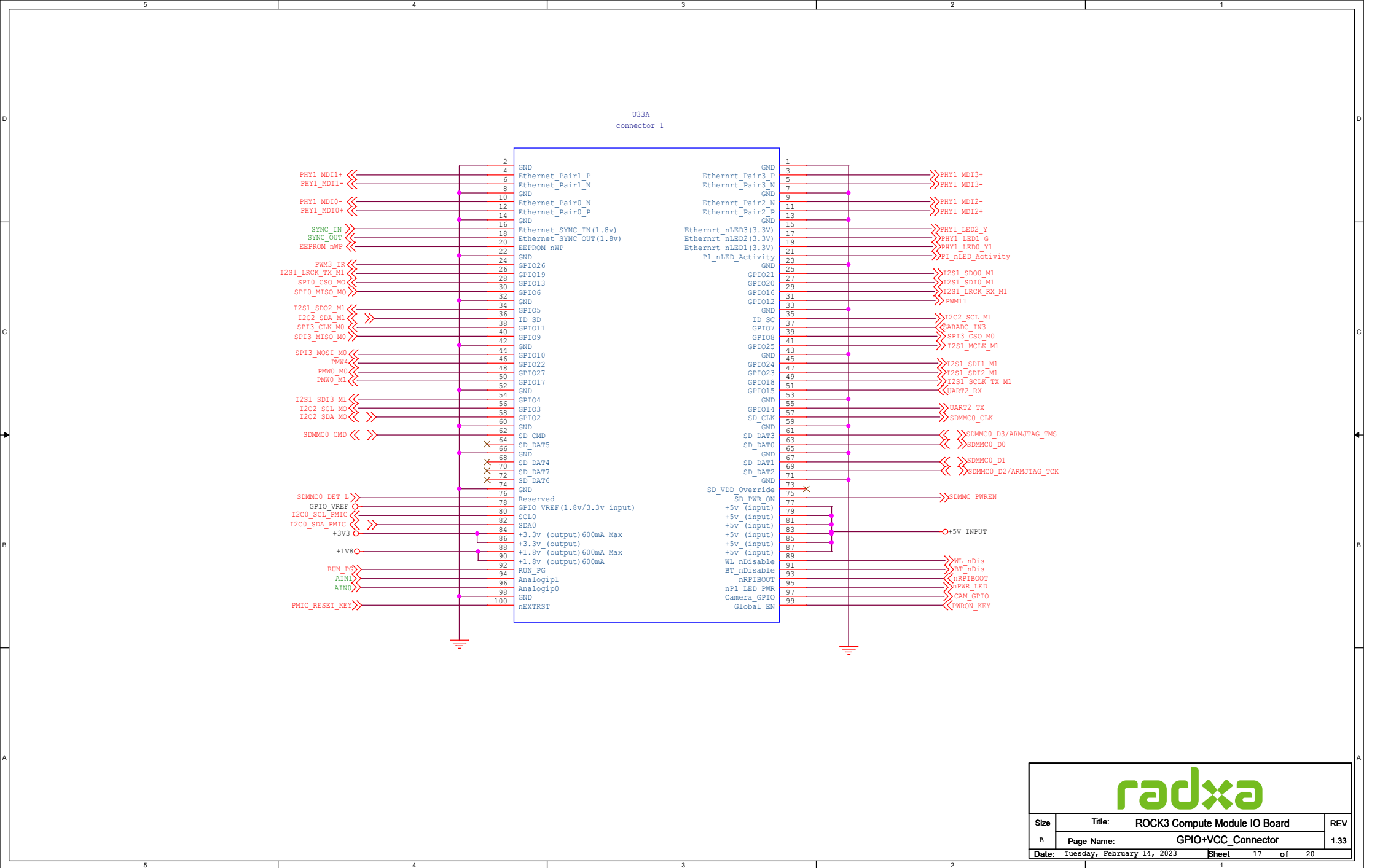
DC IN&SYSTEM Power



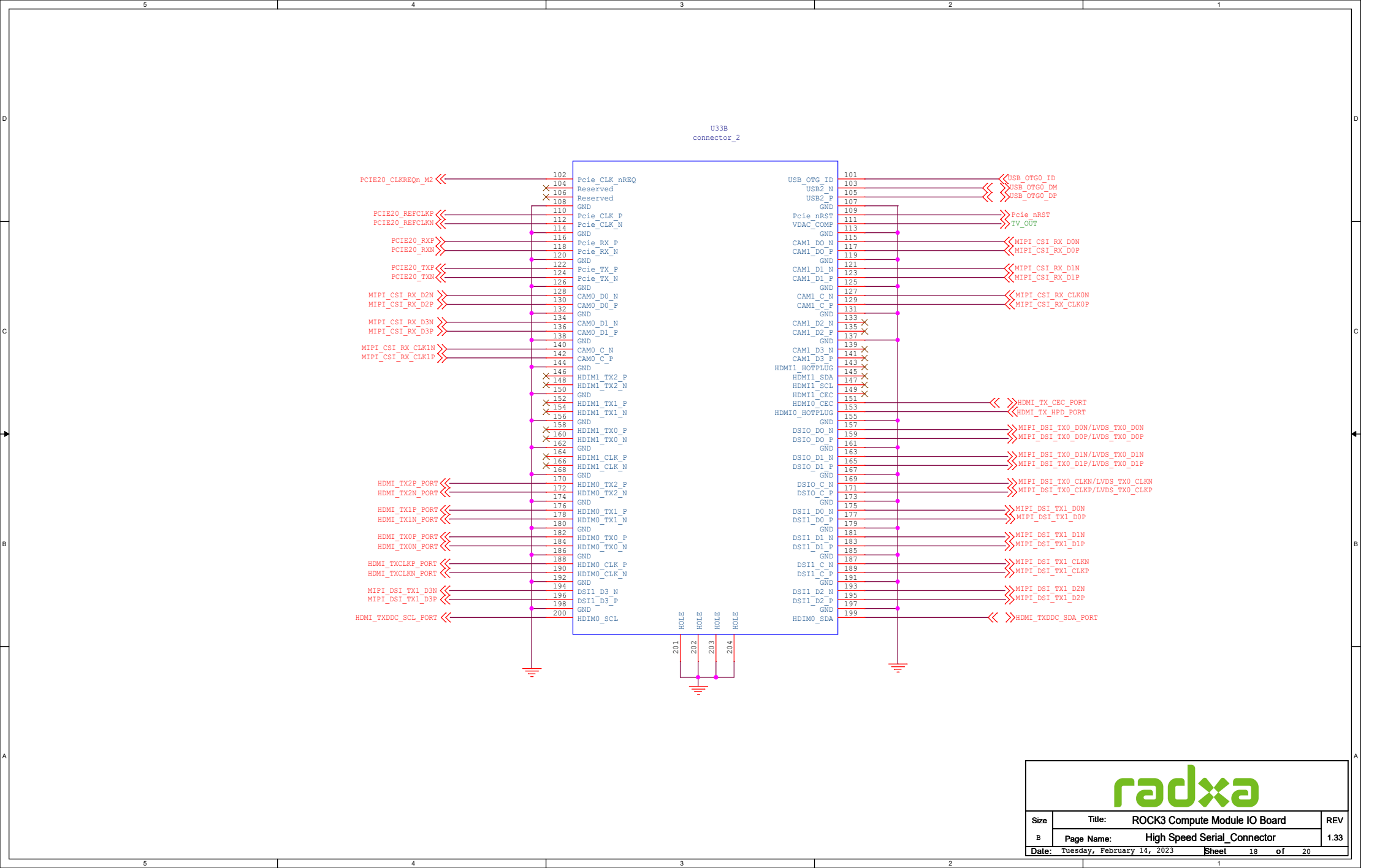
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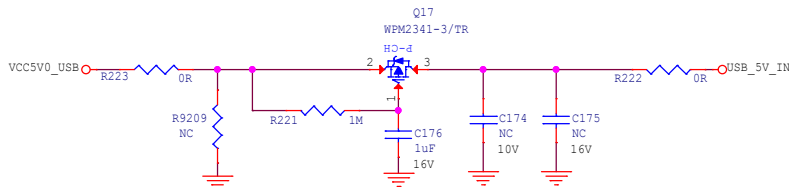
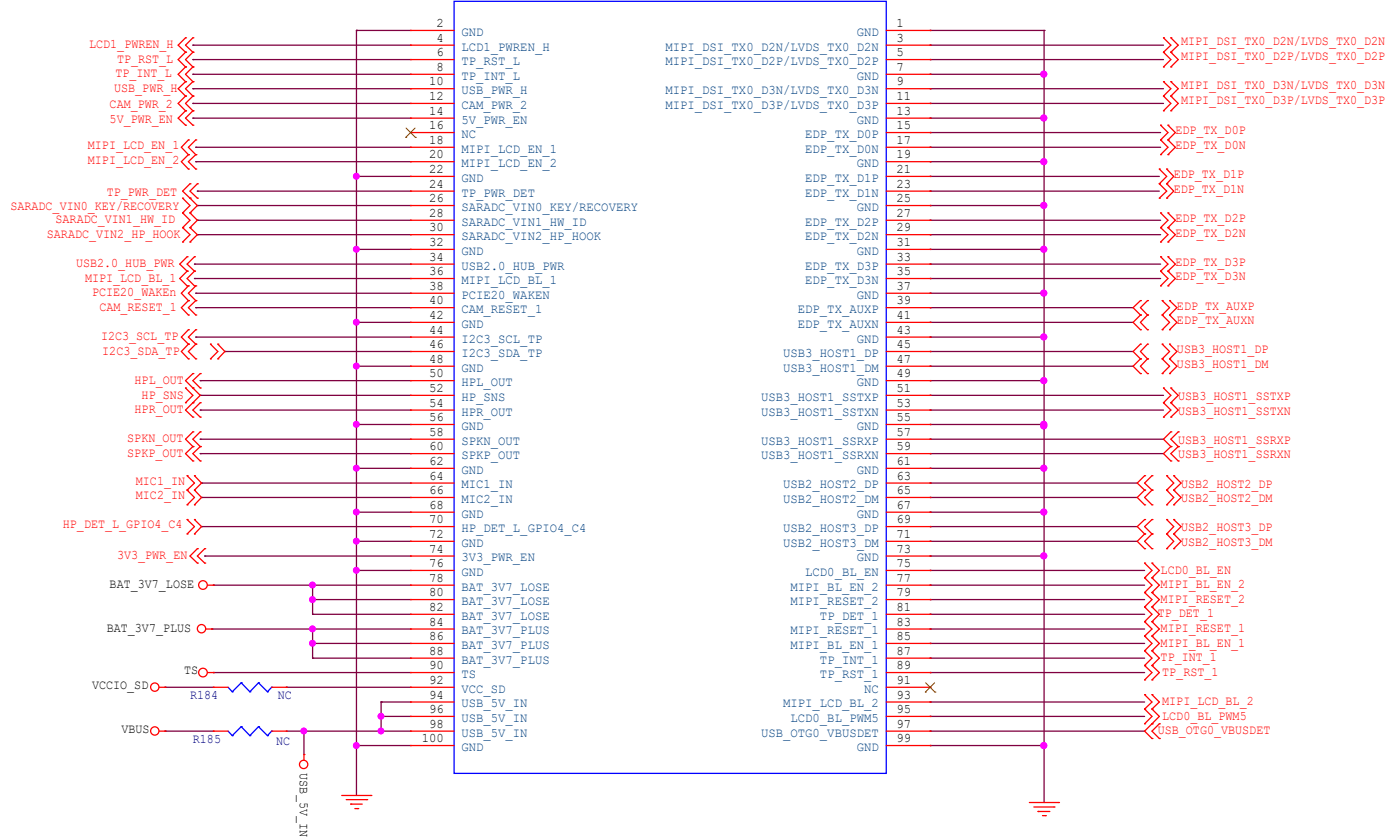
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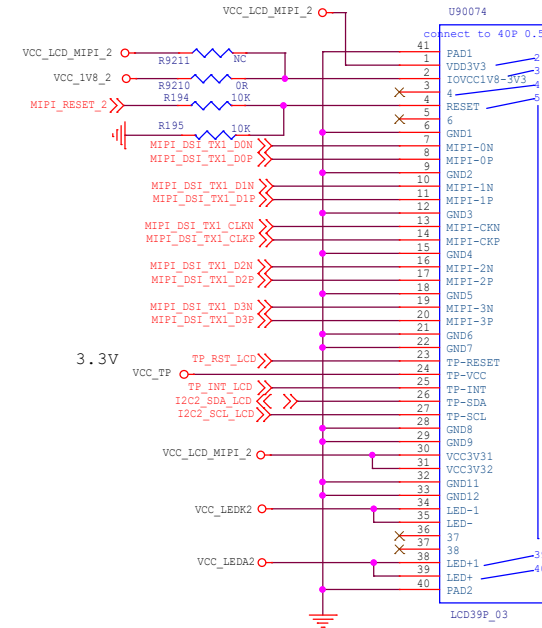
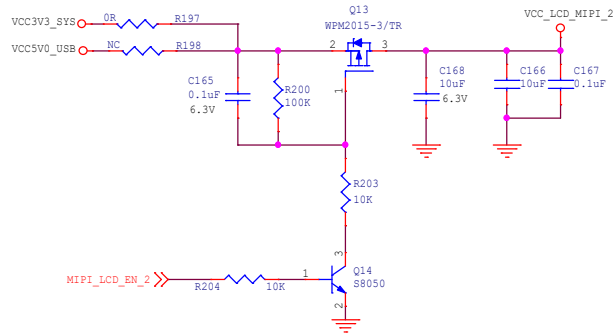
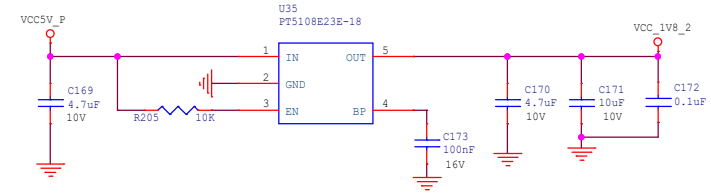
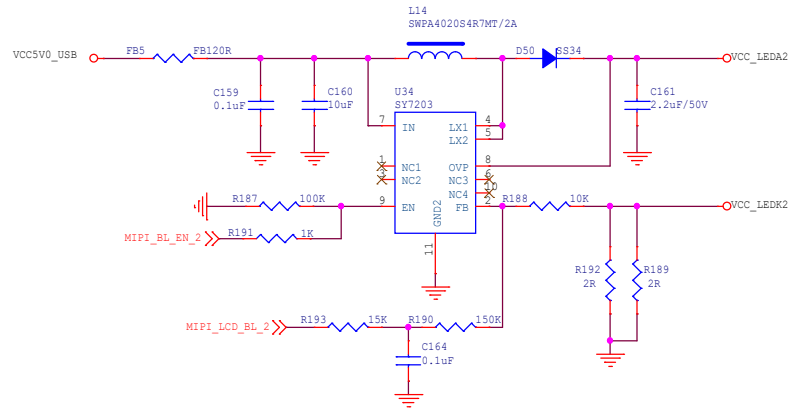
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connector3



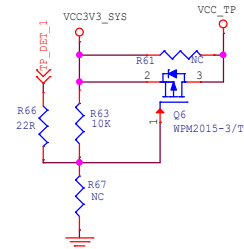
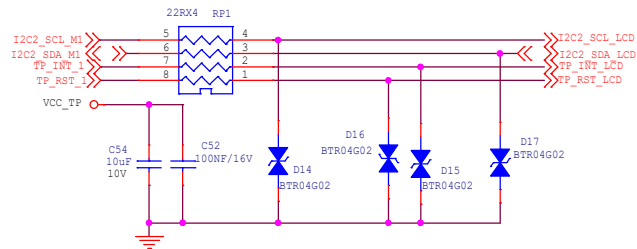
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MIPI_LCD_TX1



LCD_TP



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