

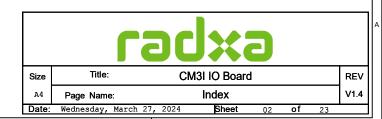
Radxa CM3I IO Board

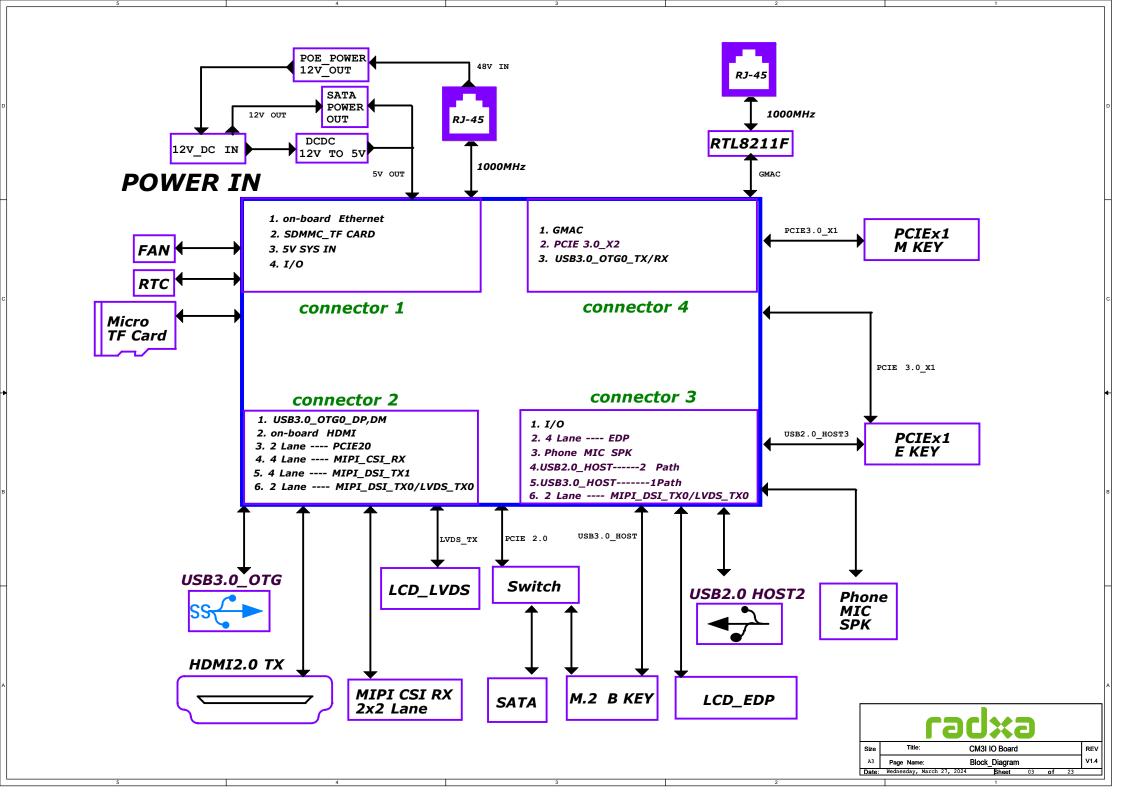
schematic

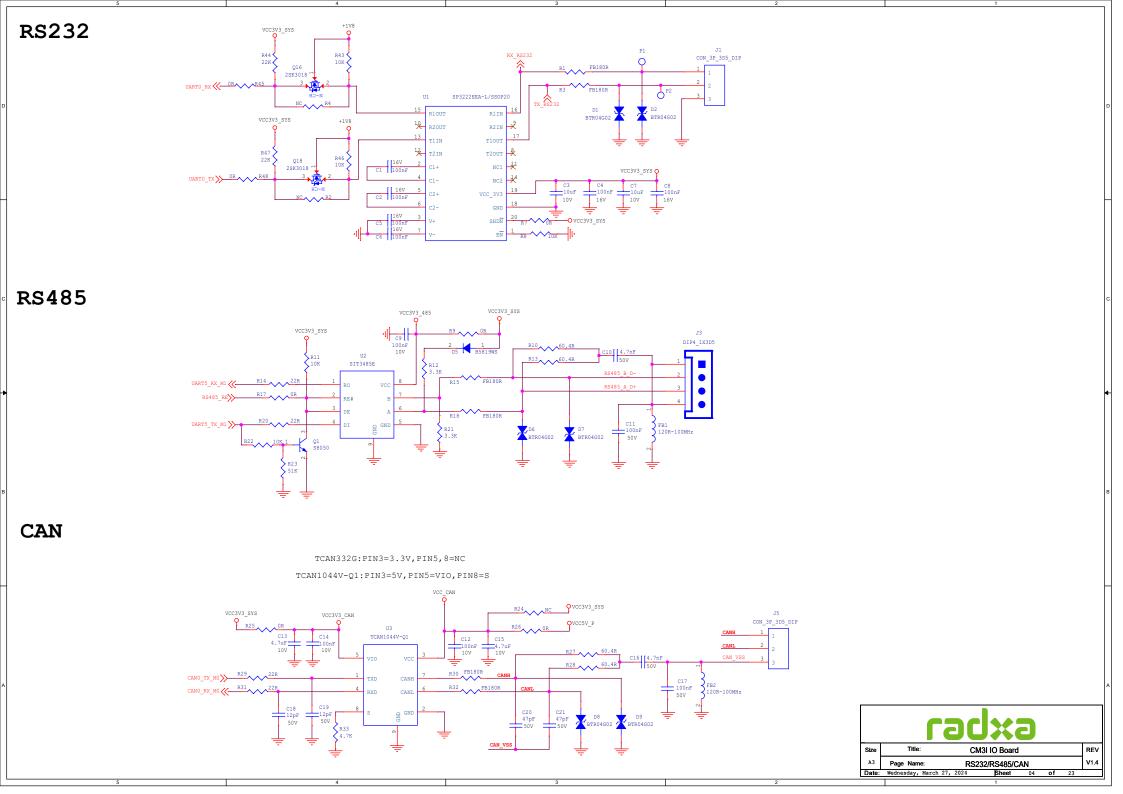


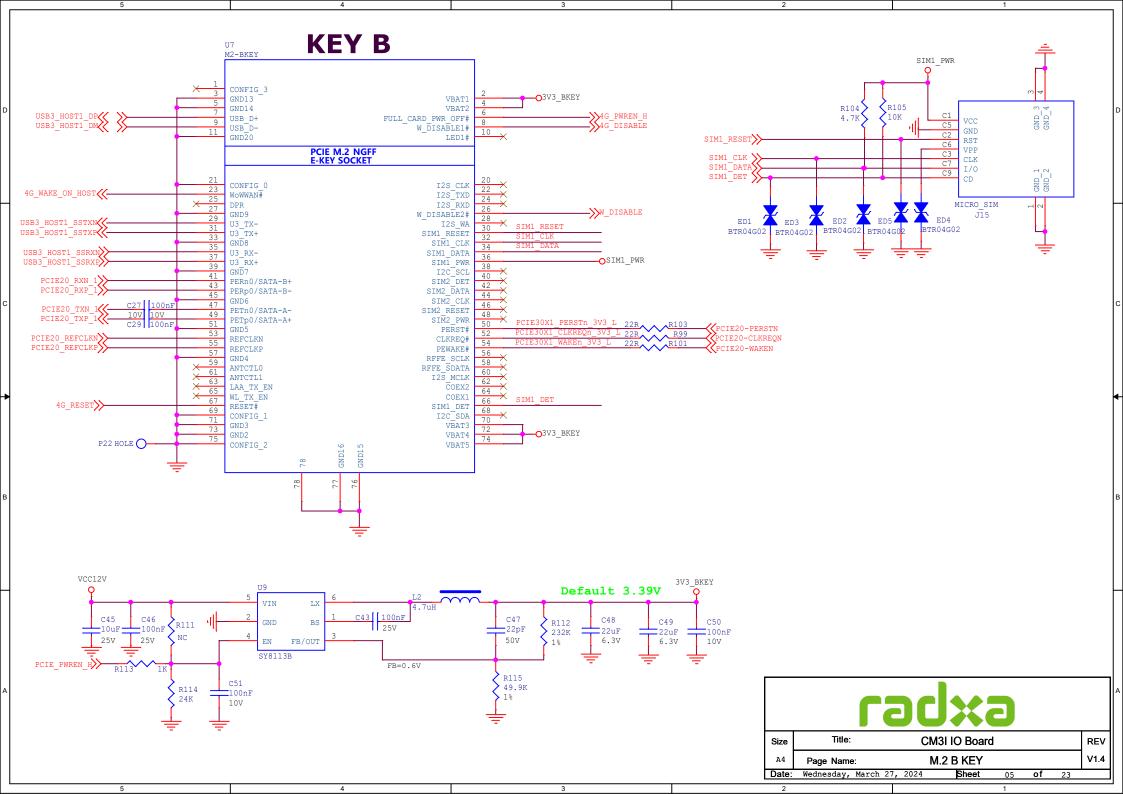
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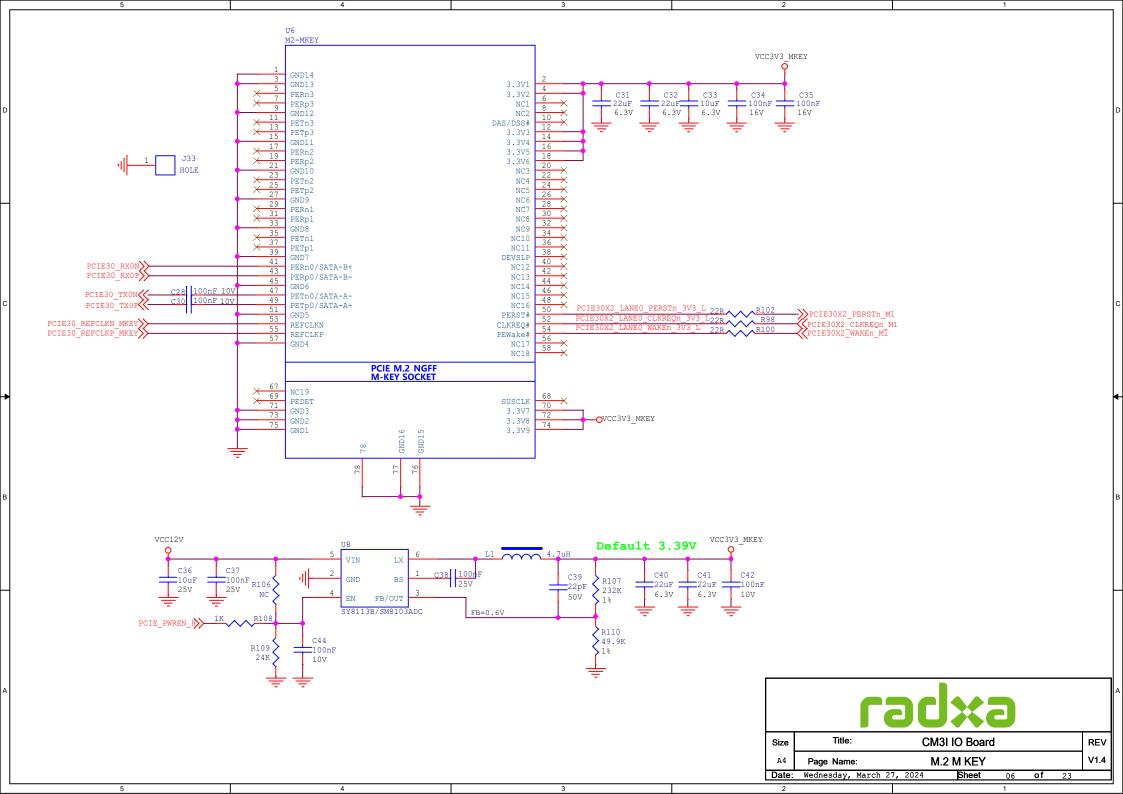
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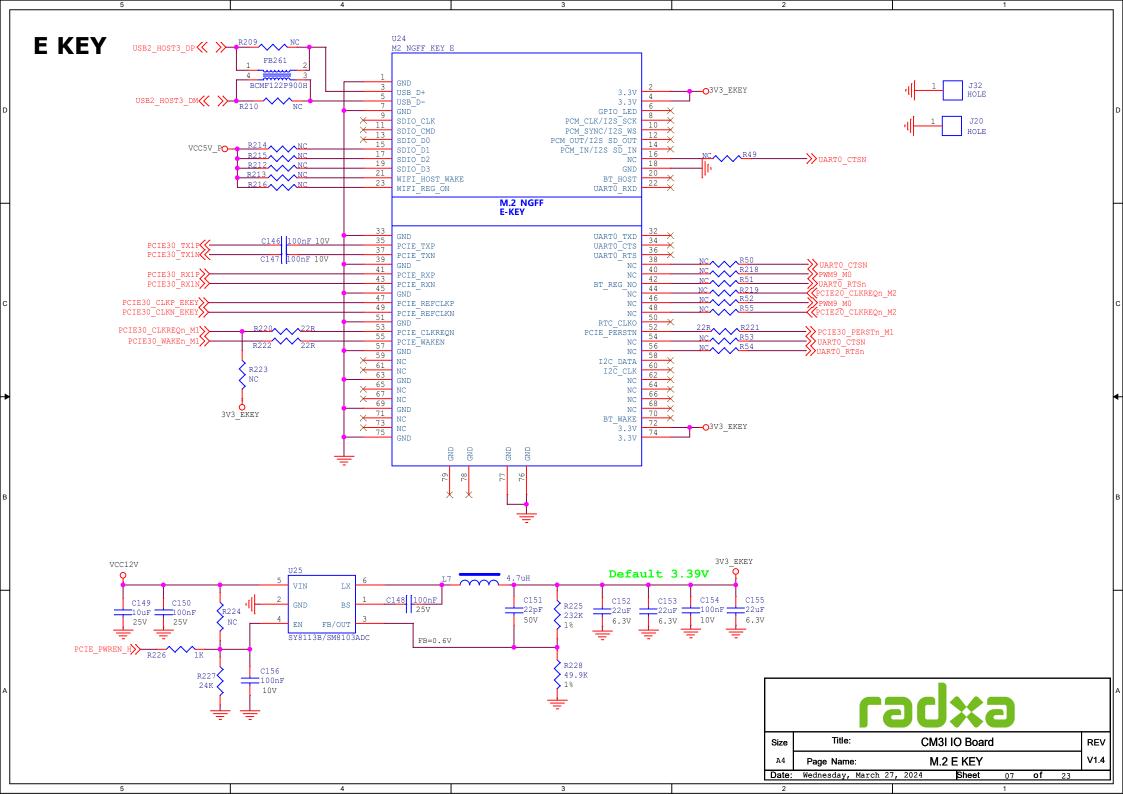


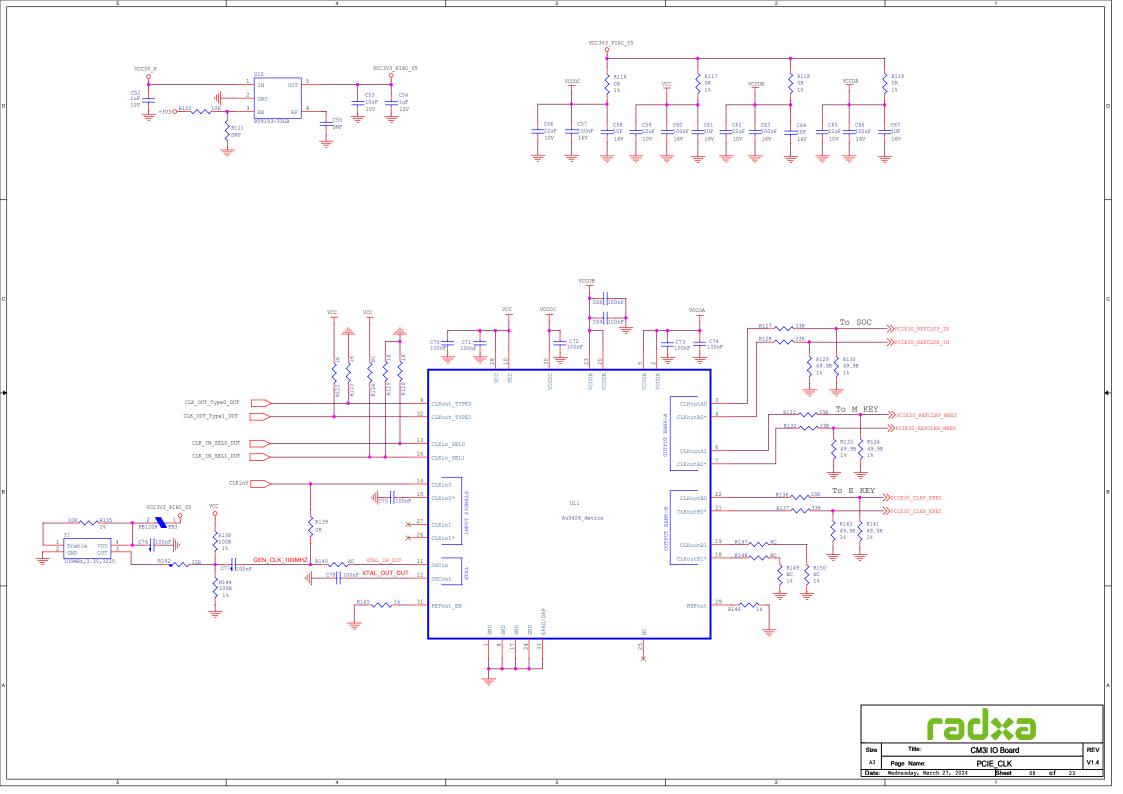




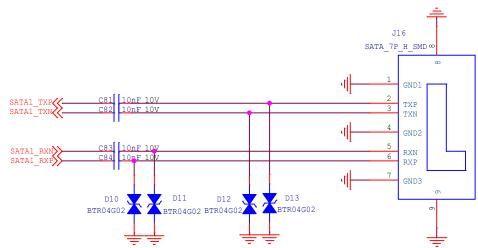


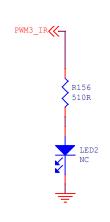


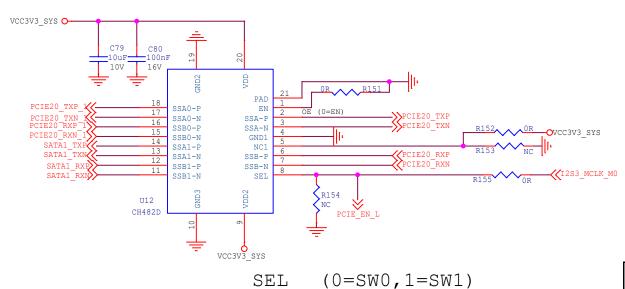


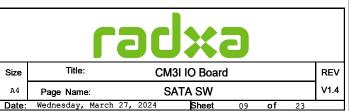






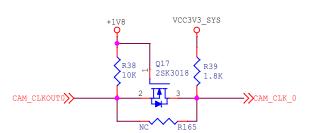


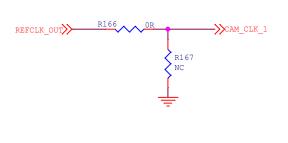




MIPI_CSI_RX 2Lanes

CON15 MIPI_CSI_RX_DON MIPI_CSI_RX_DOP 13 12 11 MIPI_CSI_RX_D1N MIPI_CSI_RX_D1P 10 MIPI_CSI_RX_CLK0N MIPI_CSI_RX_CLK0P 24 × 25 × 26 × 27 × 26 27 28 29 30 CAM_GPIO CAM CLK 28 I2C0_SCL_PMI I2C0_SDA_PMIC 29 30 C144 J8 10uF 100nF 10V





MIPI_CSI_RX 2Lanes

