EXECUTION-TABLE	. O1A						RS OCCUPANCY			
					TAG	TAGs		ADDD,		
Thread #1	Issue	EX	MEM	WB	wrtten	read	MEM	MULD	DIVD	
LD F0, 0(R1)										
MULD F0,F0,F31										
LD F1,0(R2)										
ADDD F0,F0,F1										
SD F0,0(R1)										
SUBI R1,#8										
SUBI R2,#8										
BGEZ R1, loop										
						RS OCCUPANCY (Thread 1) =>				
LD F0, 0(R1)										
MULD F0,F0,F31										
LD F1,0(R2)										
ADDD F0,F0,F1										
SD F0,0(R1)										
SUBI R1,#8										
SUBI R2,#8										
BGEZ R1, loop										
						RS OCCUPANCY (Thread 1) =>				
LD F0, 0(R1)										
MULD F0,F0,F31										
LD F1,0(R2)										
ADDD F0,F0,F1										
SD F0,0(R1)										
SUBI R1,#8										
SUBI R2,#8										
BGEZ R1, loop										
						RS OCCUPANCY (Thread 1) =>				
LD F0, 0(R1)										
MULD F0,F0,F31										
LD F1,0(R2)										
ADDD F0,F0,F1										
SD F0,0(R1)										
SUBI R1,#8										
SUBI R2,#8										
BGEZ R1, loop										
						RS OCCUPANCY (Thread 1) =>				
LD F0, 0(R1)						(				
MULD F0,F0,F31										
LD F1,0(R2)				1						
ADDD F0,F0,F1										
SD F0,0(R1)										
SUBI R1,#8										
SUBI R2,#8										
BGEZ R1, loop										
						RS OCCUPANCY (Thread 1) =>				

EXECUTION-TABLE	. O1A						RS OCCUPANCY			
					TAG	TAGs		ADDD,		
Thread #1	Issue	EX	MEM	WB	wrtten	read	MEM	MULD	DIVD	
LD F0, 0(R1)										
MULD F0,F0,F31										
LD F1,0(R2)										
ADDD F0,F0,F1										
SD F0,0(R1)										
SUBI R1,#8										
SUBI R2,#8										
BGEZ R1, loop										
						RS OCCUPANCY (Thread 1) =>				
LD F0, 0(R1)										
MULD F0,F0,F31										
LD F1,0(R2)										
ADDD F0,F0,F1										
SD F0,0(R1)										
SUBI R1,#8										
SUBI R2,#8										
BGEZ R1, loop										
						RS OCCUPANCY (Thread 1) =>				
LD F0, 0(R1)										
MULD F0,F0,F31										
LD F1,0(R2)										
ADDD F0,F0,F1										
SD F0,0(R1)										
SUBI R1,#8										
SUBI R2,#8										
BGEZ R1, loop										
						RS OCCUPANCY (Thread 1) =>				
LD F0, 0(R1)										
MULD F0,F0,F31										
LD F1,0(R2)										
ADDD F0,F0,F1										
SD F0,0(R1)										
SUBI R1,#8										
SUBI R2,#8										
BGEZ R1, loop										
						RS OCCUPANCY (Thread 1) =>				
LD F0, 0(R1)						(				
MULD F0,F0,F31										
LD F1,0(R2)				1						
ADDD F0,F0,F1										
SD F0,0(R1)										
SUBI R1,#8										
SUBI R2,#8										
BGEZ R1, loop										
						RS OCCUPANCY (Thread 1) =>				

EXECUTION-TABLE,	Q2A				1	1	RS OCCUPANCY		
Thread #2	Issue	EX	MEM	WB	TAG wrtten	TAGs read	MEM	ADDD, MULD	DIVD
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
2022 KI, 100p						DC OCCUPANCY (Thread	2) ->		
						RS OCCUPANCY (Thread	2) =>		
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread	2) =>		
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread	2) =>		
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread	2) =>		
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)				1					
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
	J					RS OCCUPANCY (Thread	2) ->		1

EXECUTION-TABLE,	Q2A				1	1	RS OCCUPANCY		
Thread #2	Issue	EX	MEM	WB	TAG wrtten	TAGs read	MEM	ADDD, MULD	DIVD
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
2022 KI, 100p						DC OCCUPANCY (Thread	2) ->		
						RS OCCUPANCY (Thread	2) =>		
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread	2) =>		
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread	2) =>		
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread	2) =>		
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)				1					
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
	J					RS OCCUPANCY (Thread	2) ->		1

EXECUTION-TABLE		, Q3A, Thread 1						RS occupancy		
						TAG	TAGs		ADDD,	
Thread		Issue	EX	MEM	WB	wrtten	read	MEM	MULD	DIVD
LD	F0, 0(R1)								<del>                                     </del>	
MULD	F0,F0,F31								<del> </del>	
LD	F1,0(R2)									
ADDD	F0,F0,F1									
SD	F0,0(R1)									
SUBI	R1,#8									
SUBI	R2,#8									
BGEZ	R1, loop									
1							RS OCCUPANCY (Thread 1) =>			
							RS OCCUPANCY (Thread 2) =>			
LD	F0, 0(R1)									
	F0,F0,F31									
LD	F1,0(R2)									
	F0,F0,F1									
SD	F0,0(R1)									
SUBI	R1,#8									
									+	
SUBI	R2,#8								+	
BGEZ	R1, loop								+	
							RS OCCUPANCY (Thread 1) =>		1	
							RS OCCUPANCY (Thread 2) =>		+	
LD	F0, 0(R1)									
MULD	F0,F0,F31								<u> </u>	
LD	F1,0(R2)									
ADDD	F0,F0,F1									
SD	F0,0(R1)									
SUBI	R1,#8									
SUBI	R2,#8									
BGEZ	R1, loop									
							RS OCCUPANCY (Thread 1) =>			
							RS OCCUPANCY (Thread 2) =>			
LD	F0, 0(R1)									
	F0,F0,F31									
LD	F1,0(R2)								-	
	F0,F0,F1								1	
									†	
SD	F0,0(R1)								†	
	R1,#8								+	
	R2,#8								-	
BGEZ	R1, loop								1	
							RS OCCUPANCY (Thread 1) =>		+	
							RS OCCUPANCY (Thread 2) =>		1	
LD	F0, 0(R1)					1				
MULD	F0,F0,F31								-	
LD	F1,0(R2)								-	
ADDD	F0,F0,F1									
SD	F0,0(R1)									
SUBI	R1,#8									
SUBI	R2,#8									
BGEZ	R1, loop									
							RS OCCUPANCY (Thread 1) =>			
							RS OCCUPANCY (Thread 2) =>			

EXECUTION-TABLE,	Q3A, Thread 2	!					RS OCCUPANCY		
Thread #2	Issue	EX	MEM	WB	TAG wrtten	TAGs read	MEM	ADDD, MULD	DIVD
LD F0, 0(R1)	13500	EX	III CIII	WB	Witten	read	HEH	HOLD	BIVE
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 1) =>		1	1
						RS OCCUPANCY (Thread 2) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									-
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 1) =>			
						RS OCCUPANCY (Thread 2) =>			<u> </u>
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 1) =>			
						RS OCCUPANCY (Thread 2) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
DULZ K1, 100P						DC OCCUDANCY (Thread 1)		<u> </u>	†
						RS OCCUPANCY (Thread 1) =>		+	†
						RS OCCUPANCY (Thread 2) =>			
LD F0, 0(R1)									+
MULD F0,F0,F31									+
LD F1,0(R2)								-	-
DIVD F0,F0,F1								+	+
SD F0,0(R1)									-
SUBI R1,#8								-	<del>                                     </del>
SUBI R2,#8									<del>                                     </del>
BGEZ R1, loop			1					1	<del>                                     </del>
						RS OCCUPANCY (Thread 1) =>		<u> </u>	<u> </u>
						RS OCCUPANCY (Thread 2) =>		<u> </u>	