EXECUTION-TABLE, Q1A

Thread #1	Issue	EX	MEM	WB	TAG wrtten	TAGs read	RS OCCUPANCY MEM	ADDD, MULD	DIVD
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
ADDD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 1) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
ADDD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 1) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
ADDD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 1) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
ADDD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 1) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
ADDD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 1) =>			

EXECUTION-TABLE, Q1A

							RS OCCUPANCY		
Thread #1	Issue	EX	MEM	WB	TAG wrtten	TAGs read	MEM	ADDD, MULD	DIVD
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
ADDD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 1) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
ADDD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 1) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
ADDD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 1) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
ADDD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 1) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
ADDD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 1) =>			

EXECUTION-TABLE, Q2A

							RS OCCUPANCY		
Thread #2	Issue	EX	MEM	WB	TAG wrtten	TAGs read	MEM	ADDD, MULD	DIVD
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 2) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 2) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 2) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 2) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 2) =>			

EXECUTION-TABLE, Q2A

Thread #2	Issue	EX	MEM	WB	TAG wrtten	TAGs read	RS OCCUPANCY MEM	ADDD, MULD	DIVD
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 2) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 2) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 2) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 2) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 2) =>			

EXECUTION-TABLE, Q3A, Thread 1

							RS occupancy		
Thread #1	Issue	EX	MEM	WB	TAG wrtten	TAGs read	MEM	ADDD, MULD	DIVD
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
ADDD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 1) =>			
						RS OCCUPANCY (Thread 2) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
ADDD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 1) =>			
						RS OCCUPANCY (Thread 2) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
ADDD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 1) =>			
						RS OCCUPANCY (Thread 2) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
ADDD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 1) =>			
						RS OCCUPANCY (Thread 2) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
ADDD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 1) =>			
						RS OCCUPANCY (Thread 2) =>			

EXECUTION-TABLE, Q3A, Thread 2

Thread #2	Issue	EX	MEM	WB	TAG wrtten	TAGs read	RS OCCUPANCY MEM	ADDD, MULD	DIVD
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 1) =>			
						RS OCCUPANCY (Thread 2) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 1) =>			
						RS OCCUPANCY (Thread 2) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 1) =>			
						RS OCCUPANCY (Thread 2) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 1) =>			
						RS OCCUPANCY (Thread 2) =>			
LD F0, 0(R1)									
MULD F0,F0,F31									
LD F1,0(R2)									
DIVD F0,F0,F1									
SD F0,0(R1)									
SUBI R1,#8									
SUBI R2,#8									
BGEZ R1, loop									
						RS OCCUPANCY (Thread 1) =>			
						RS OCCUPANCY (Thread 2) =>			