

# *PEW PEW PEW:*

## DESIGNING SECURE BOOT SECURELY

**riscure**

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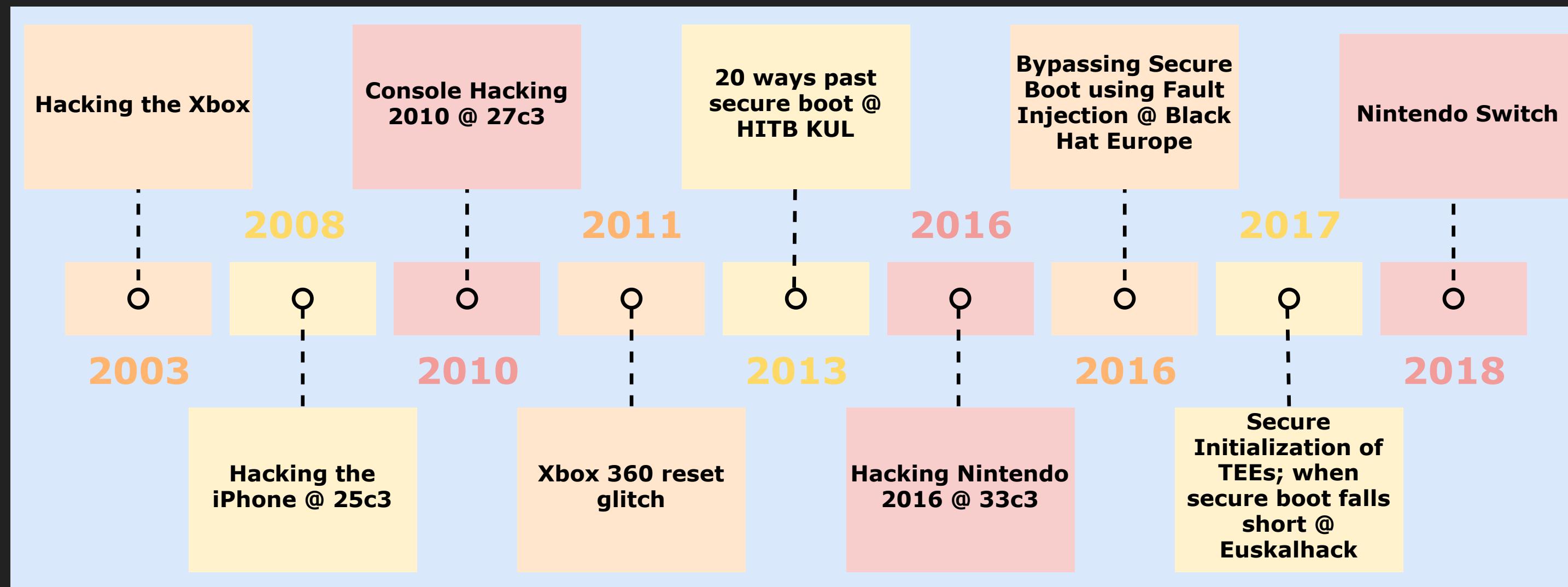
**AVAILABLE**

Albert Spruyt

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# WHY THIS TALK?

# SOME HISTORY...



SECURE BOOT IS STILL OFTEN VULNERABLE...

# OUR GOAL

*Create a Secure Boot guidance for  
designers and implementers.*

# THIS PRESENTATION

Defensive focus

Offensive for context

# AGENDA

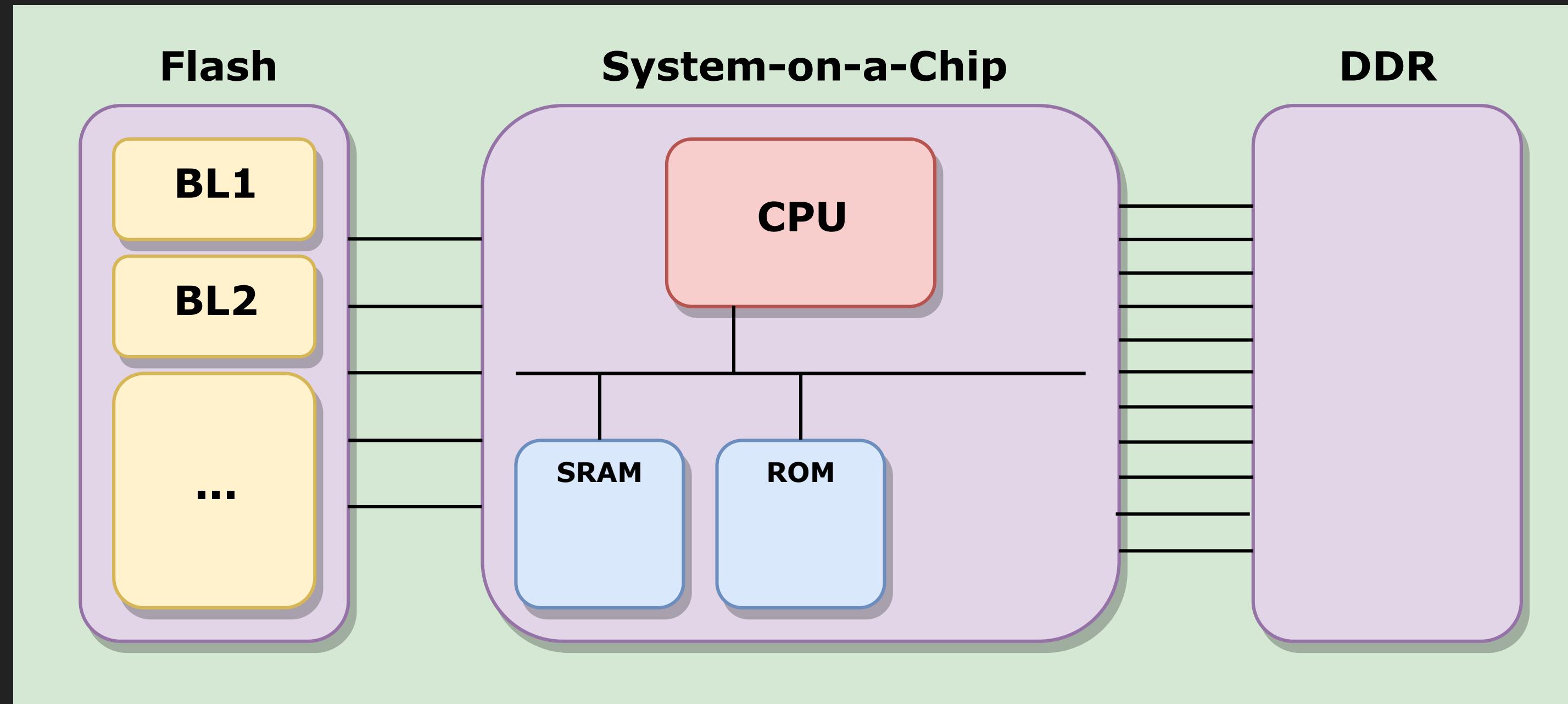
Secure Boot

Fault Injection demo

Designing Secure Boot securely

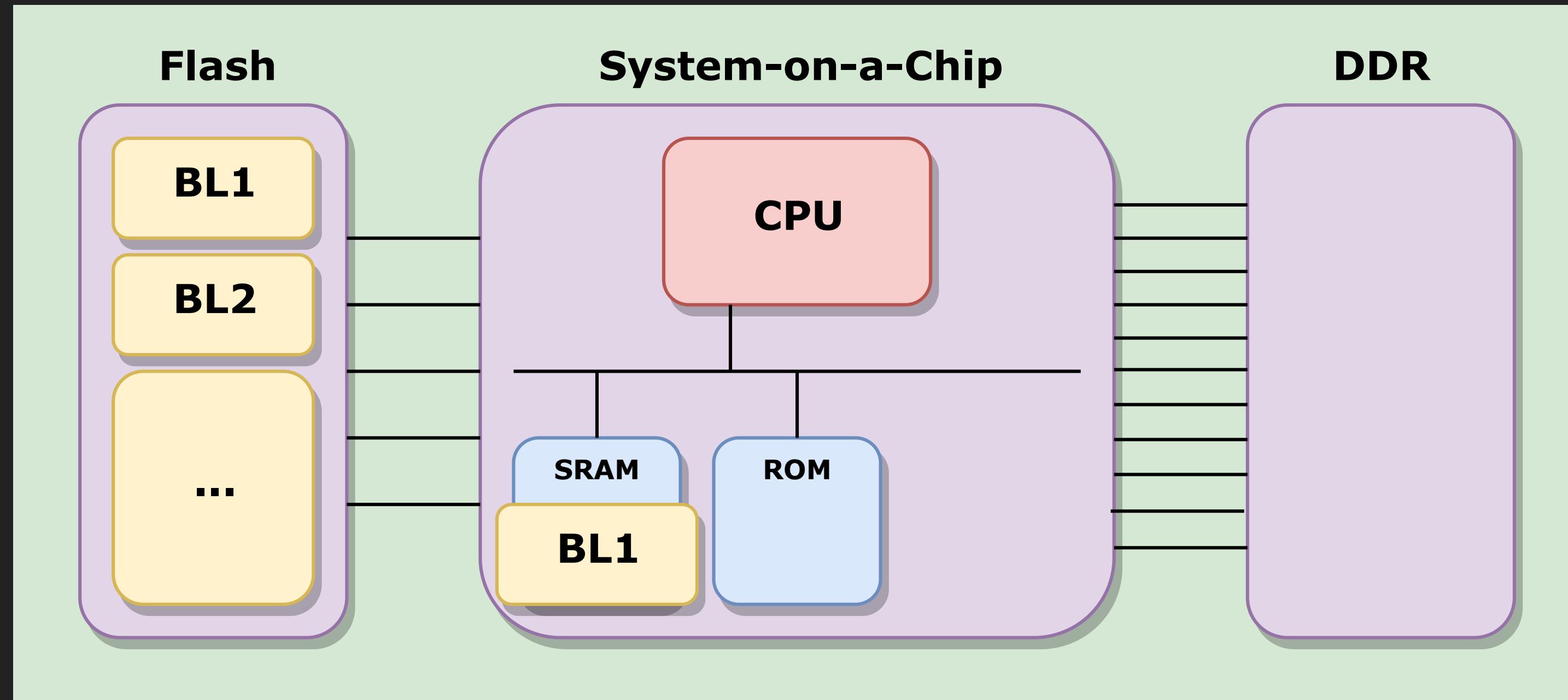
Takeaways

# GENERIC DEVICE



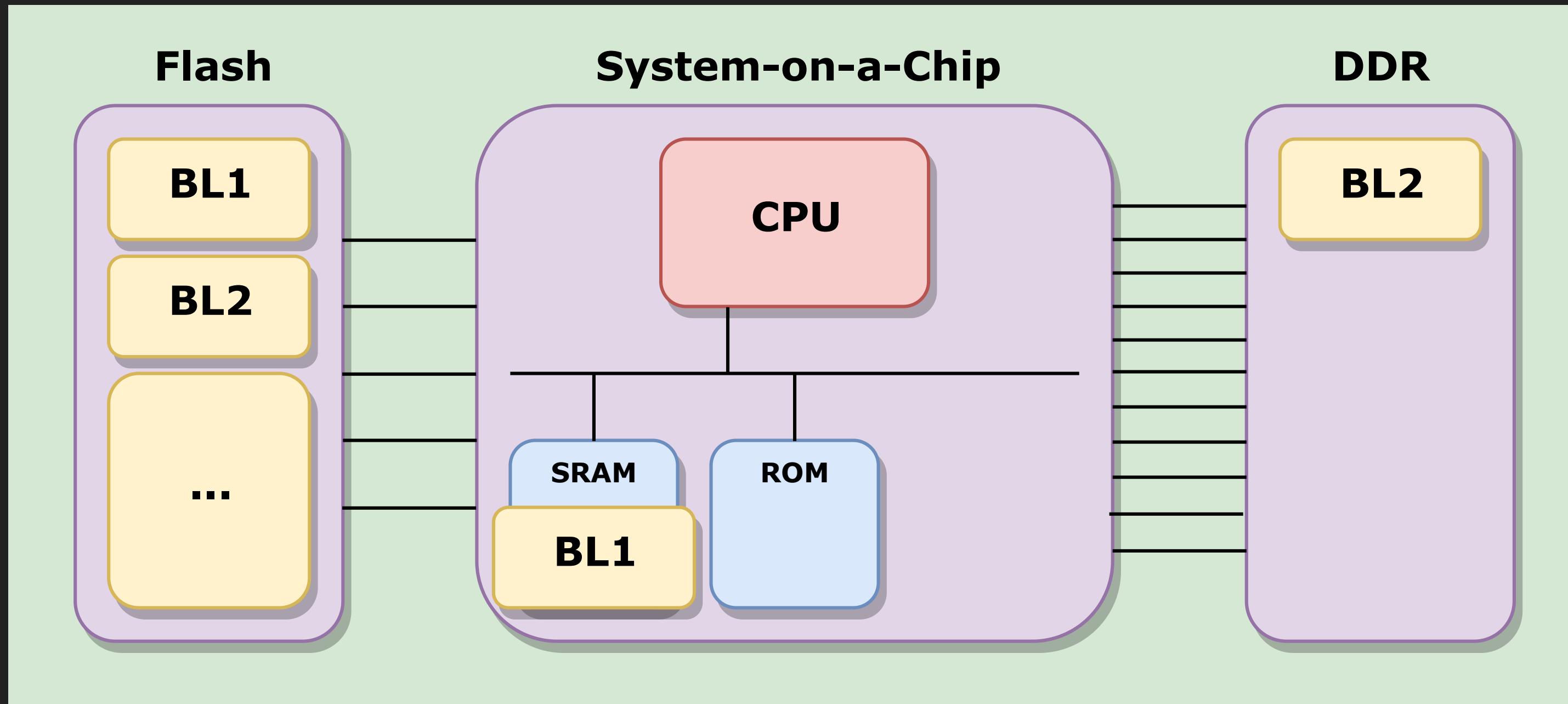
Device is turned off

# GENERIC DEVICE



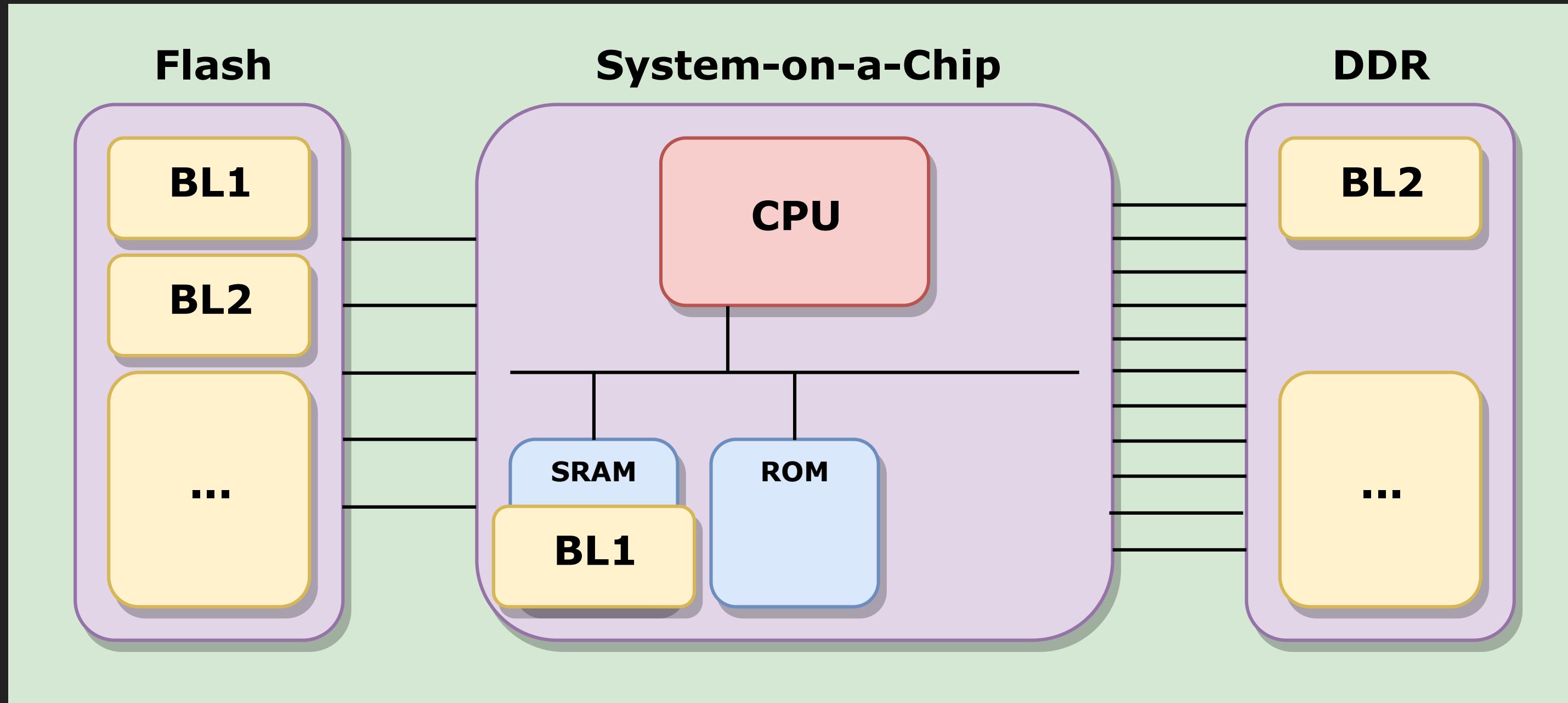
ROM code loads BL1 into internal SRAM

# GENERIC DEVICE



BL1 initializes DDR and loads BL2 into DDR

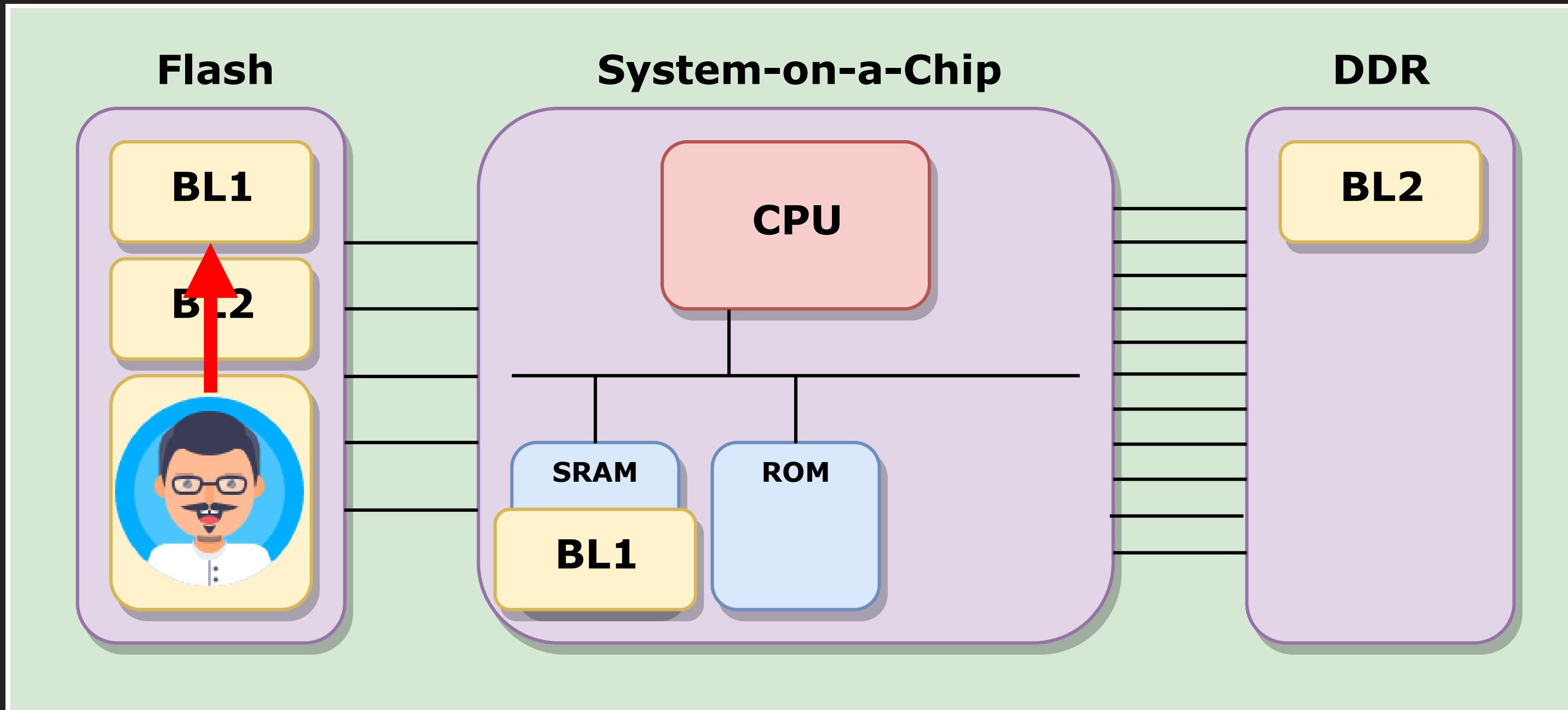
# GENERIC DEVICE



Afterwards more is loaded and executed...

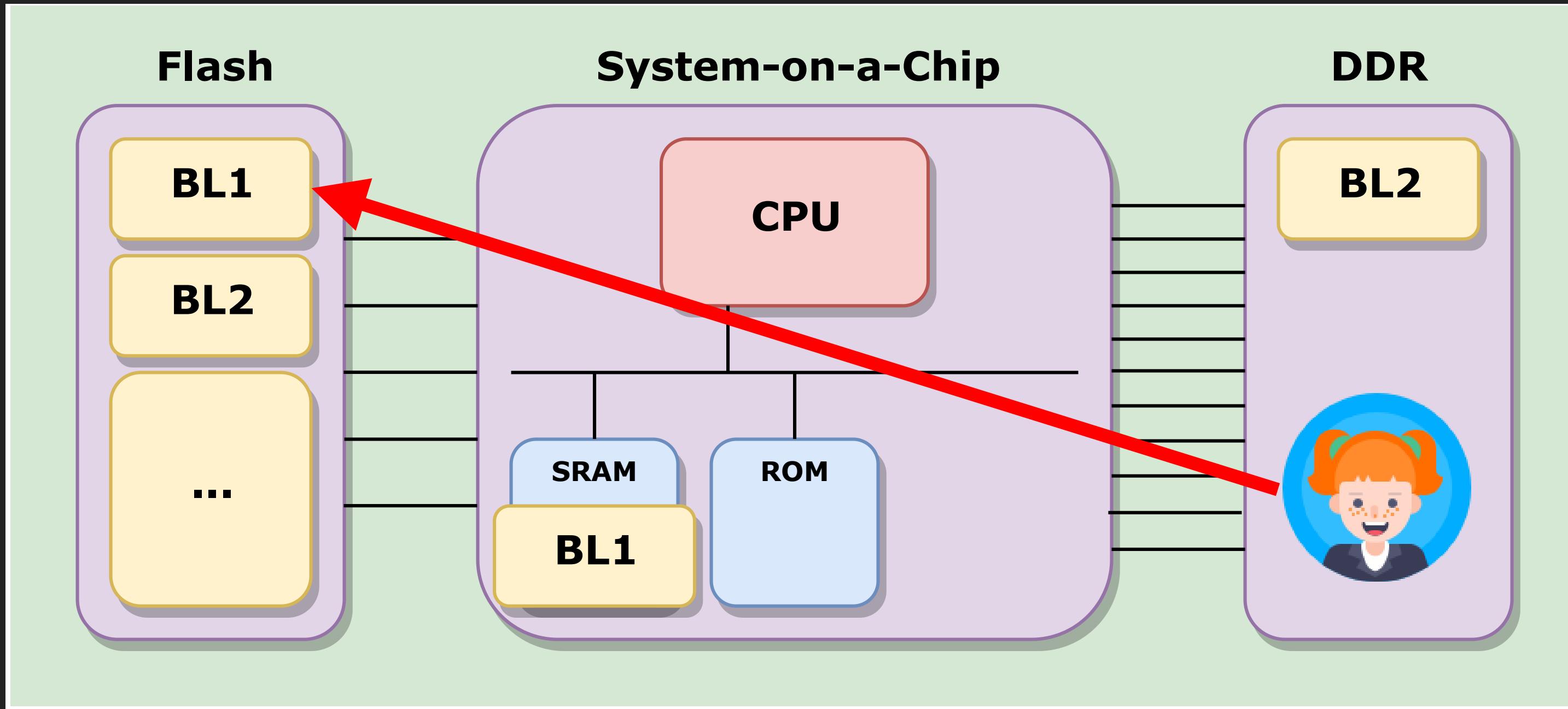
TWO MAJOR THREATS...

# ATTACKERS



Attacker 1: hardware hacker modifies flash

# ATTACKERS



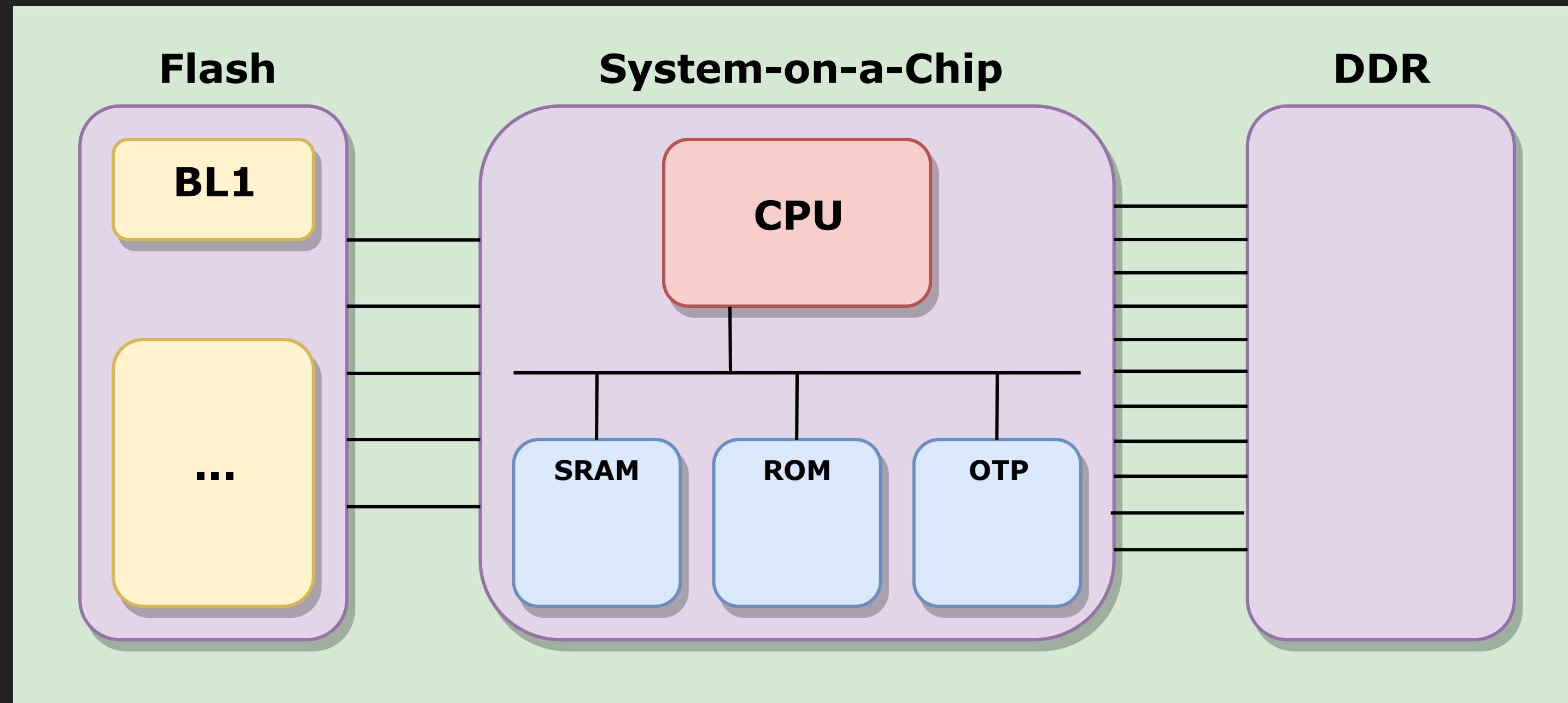
Attacker 2: (remote) software hacker modifies flash

**THEREFORE WE NEED SECURE BOOT**

# SECURE BOOT

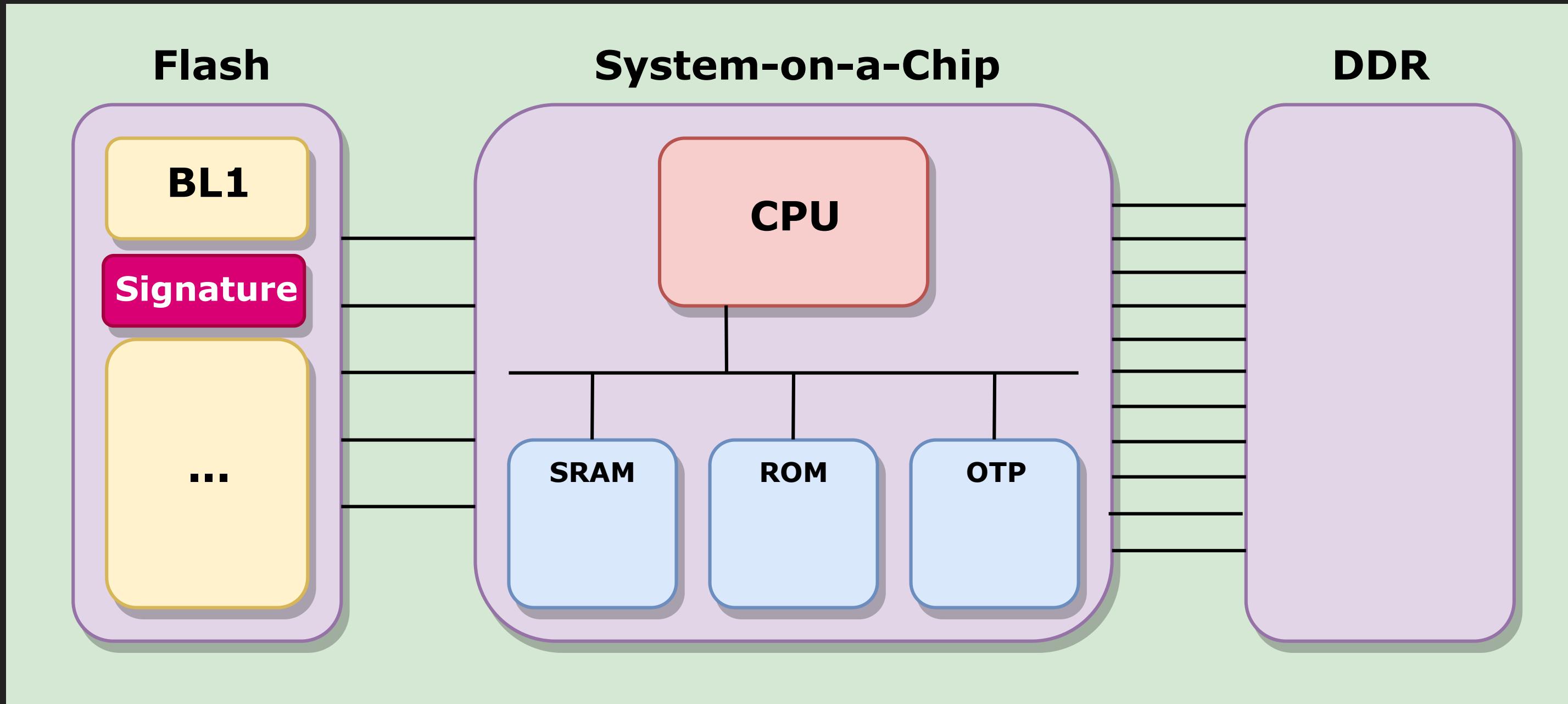
- Root of trust embedded in hardware
  - i.e. immutable code and data (e.g. ROM, OTP)
- Authentication of all code/data
- (Optional): Decryption of all images

# SECURE BOOT



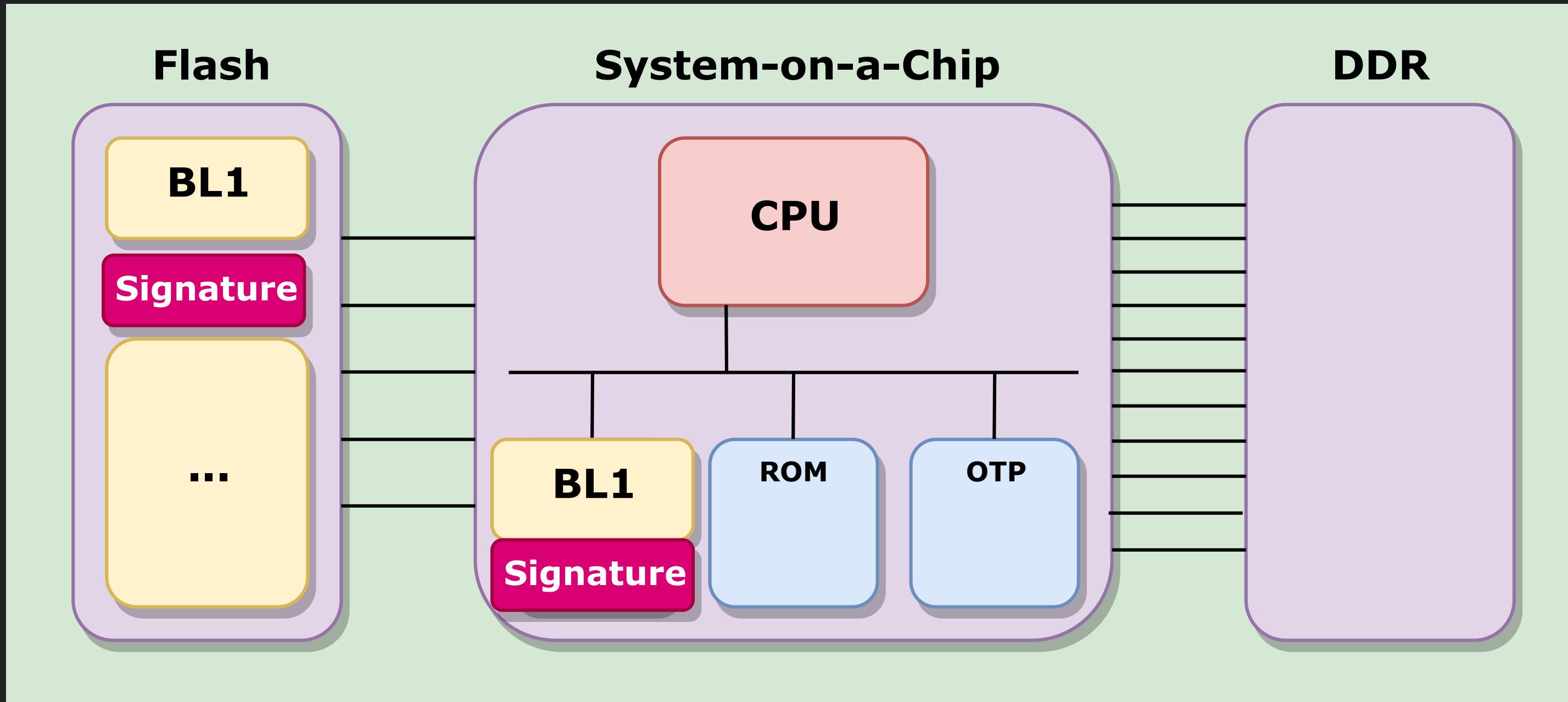
Device is turned off

# SECURE BOOT



Next to BL1 a signature is stored

# SECURE BOOT



ROM verifies integrity of BL1

# MITIGATING THREATS

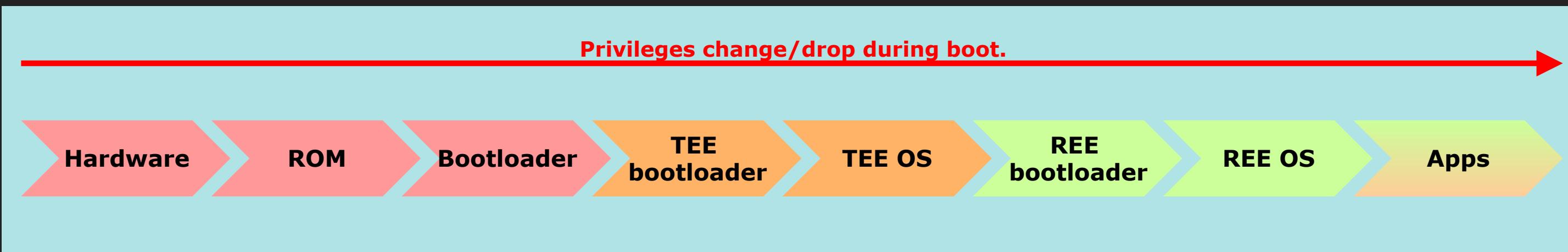
- Modifying code/data in flash
- Creating a persistent foothold
- Escalating privileges (e.g. REE to TEE)
  - Access to keys, code and crypto engines
- Bypassing secure update mechanisms

THE REAL WORLD IS A LITTLE MORE COMPLEX...

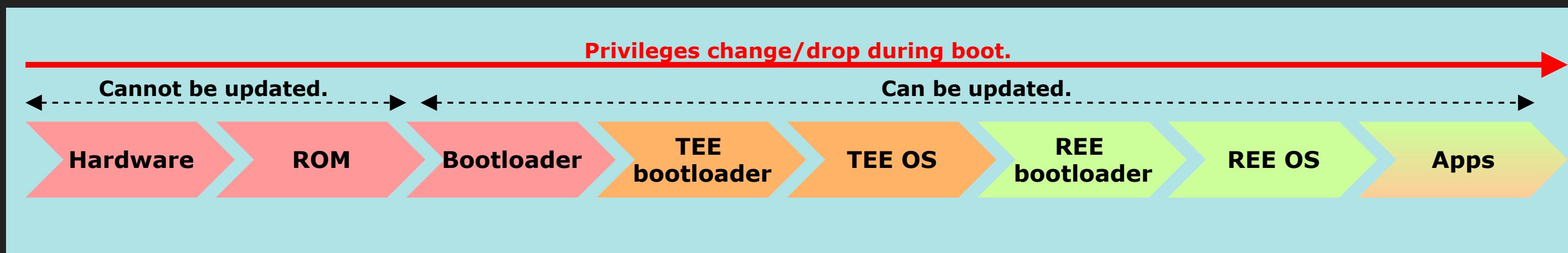
# SECURE BOOT FLOW



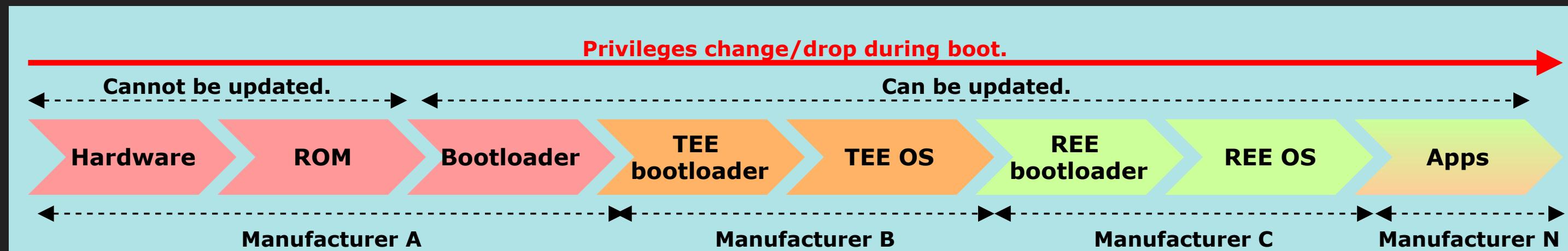
# SECURE BOOT FLOW



# SECURE BOOT FLOW



# SECURE BOOT FLOW



Securing the entire chain is complex...

# CONSTRAINTS...

Initializing, and interfacing with, hardware

Performance and code size

Customer needs

Recoverability

Keeping engineering cost low

**IT'S IMPORTANT TO GET IT RIGHT**

# BAD SECURITY IS EXPENSIVE!

Tape out

Crisis management

PR damage

Recall of devices/unsold inventory

Time to market

Additional engineering time

**SO... WHERE DO YOU START?**

## Designing Secure Boot in a Nutshell

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Cristofaro Mune  
*Pulse Security*  
[c.mune@pulse-sec.com](mailto:c.mune@pulse-sec.com)

- [SBG-01]: Keep it simple
- [SBG-02]: Hardware root of trust
- [SBG-03]: Authenticate everything
- [SBG-04]: Decrypt everything
- [SBG-05]: No weak crypto
- [SBG-06]: No "wrong" crypto
- [SBG-07]: Limit options
- [SBG-08]: Lock hardware down
- [SBG-09]: Drop privileges asap
- [SBG-10]: Make software exploitation hard
- [SBG-11]: Make hardware attacks hard
- [SBG-12]: Stack your defenses
- [SBG-13]: Continuous review and testing
- [SBG-14]: Anti-rollback

**LET'S DESIGN SECURE BOOT SECURELY!**

BUT... BEFORE WE DO...

LET'S HAVE SOME FUN FIRST!

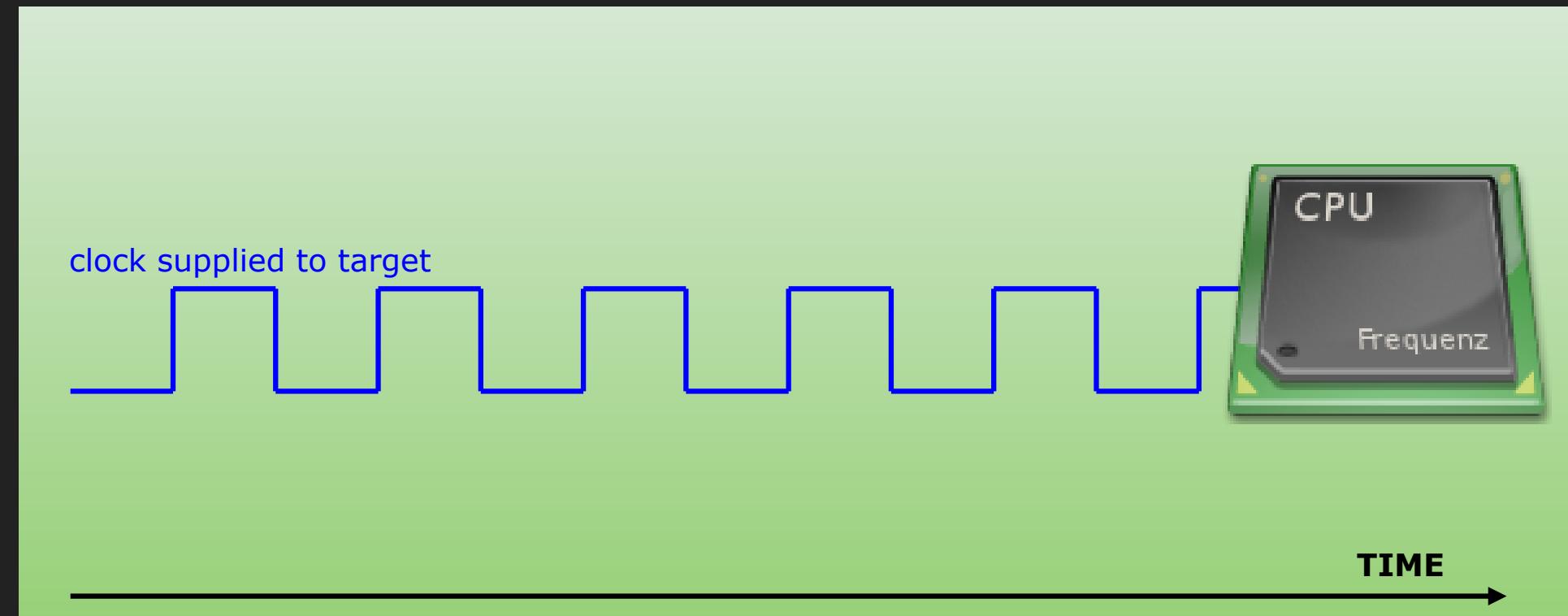
# FAULT INJECTION

*"Introducing faults into a target in order to change its intended behavior."*



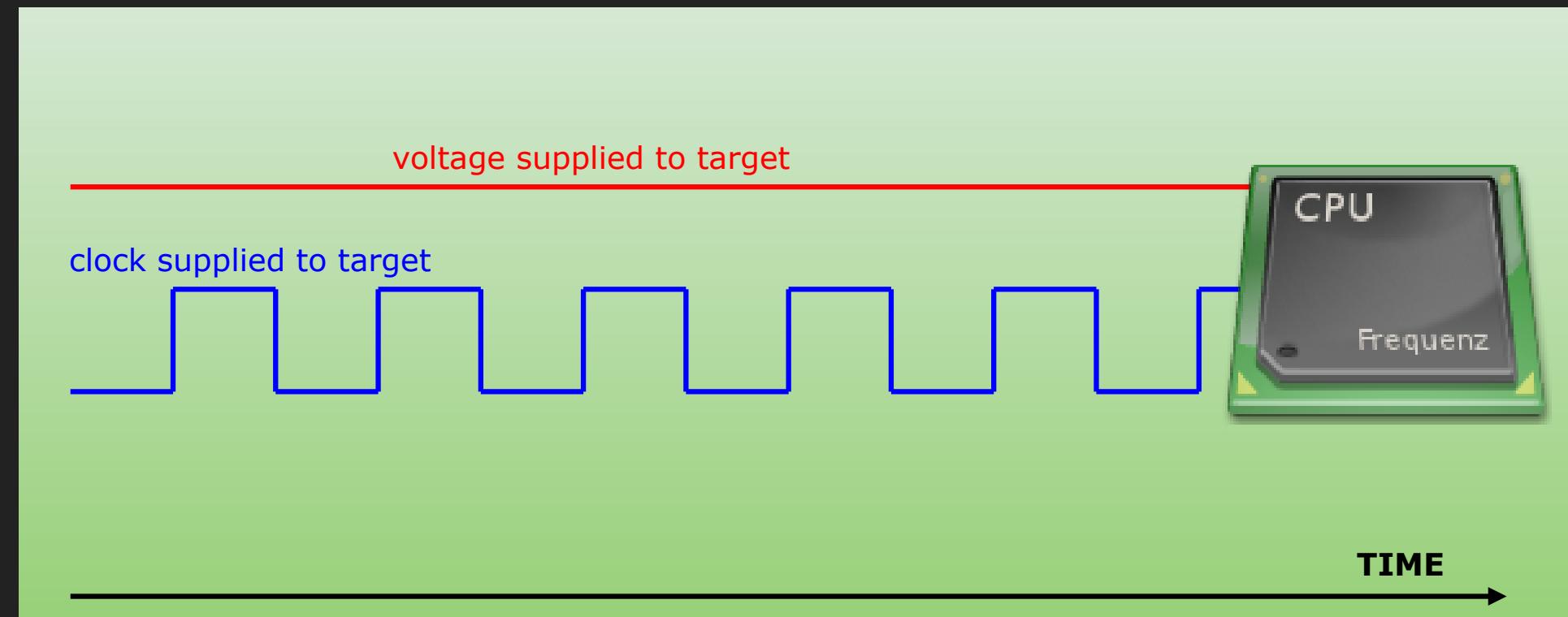
# FAULT INJECTION

*"Introducing faults into a target in order to change its intended behavior."*



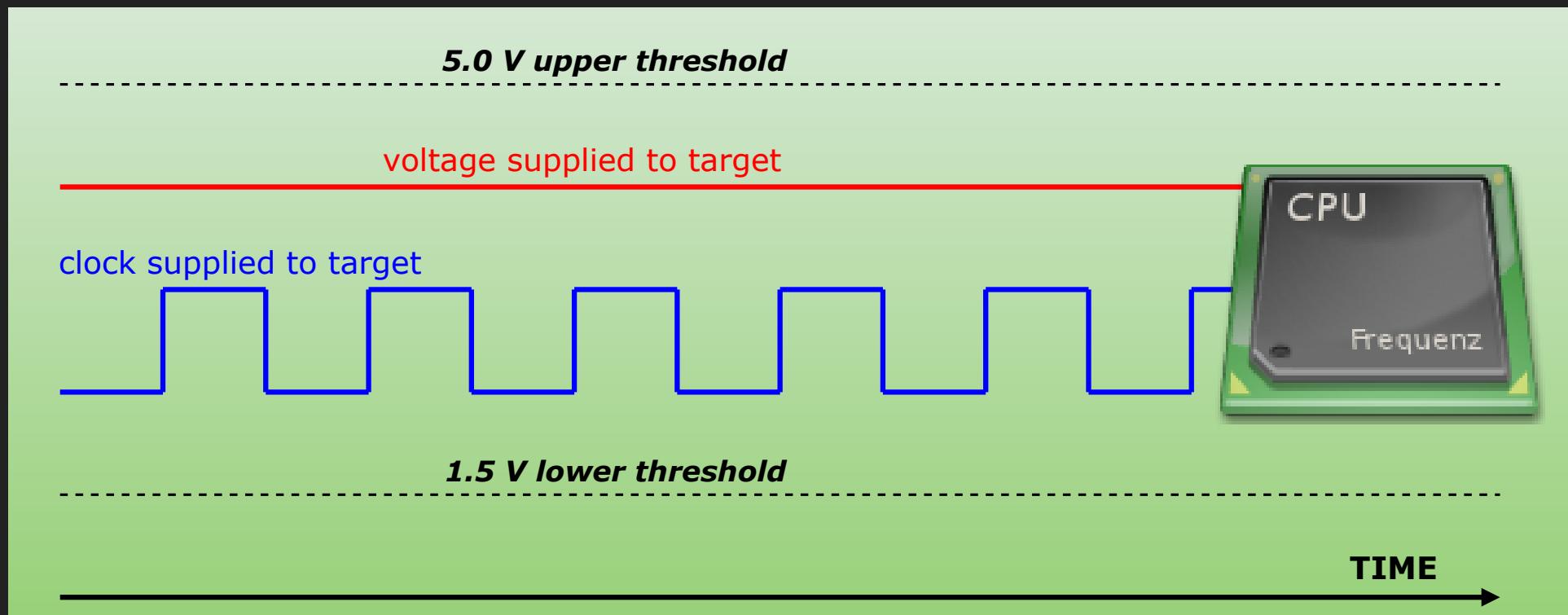
# FAULT INJECTION

*"Introducing faults into a target in order to change its intended behavior."*



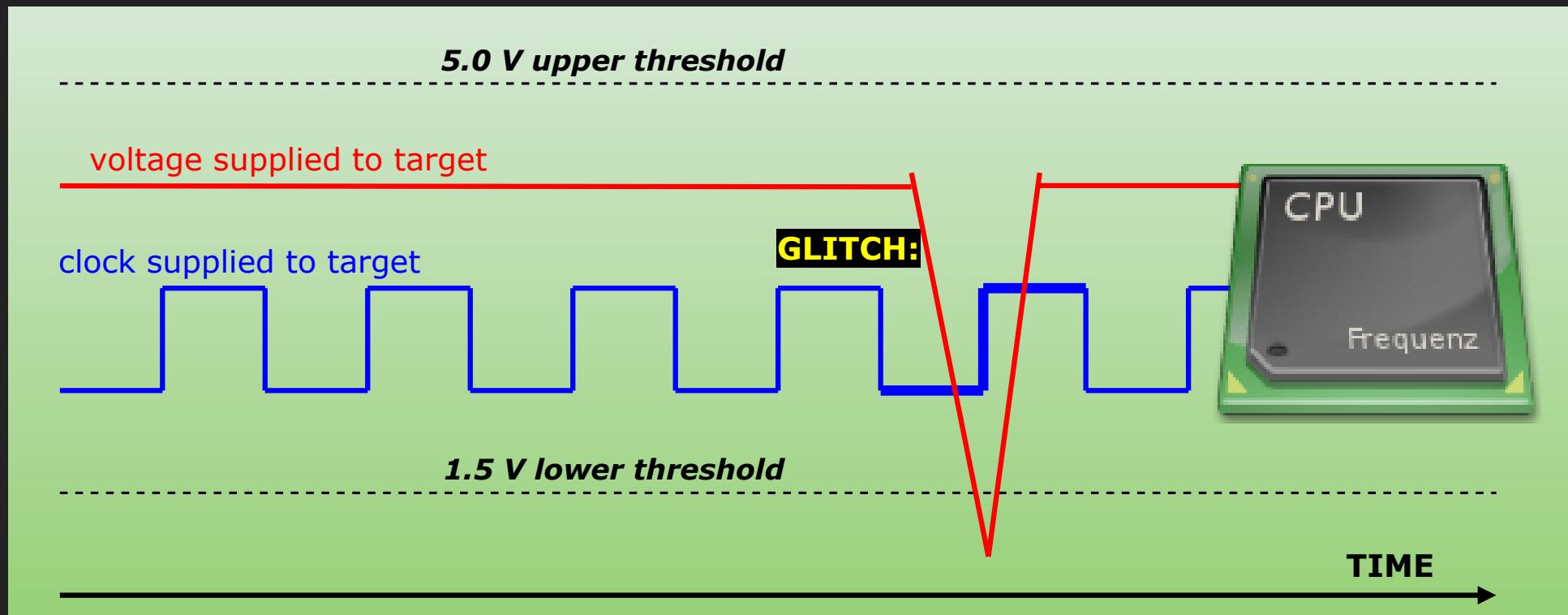
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*"Introducing faults into a target in order to change its intended behavior."*

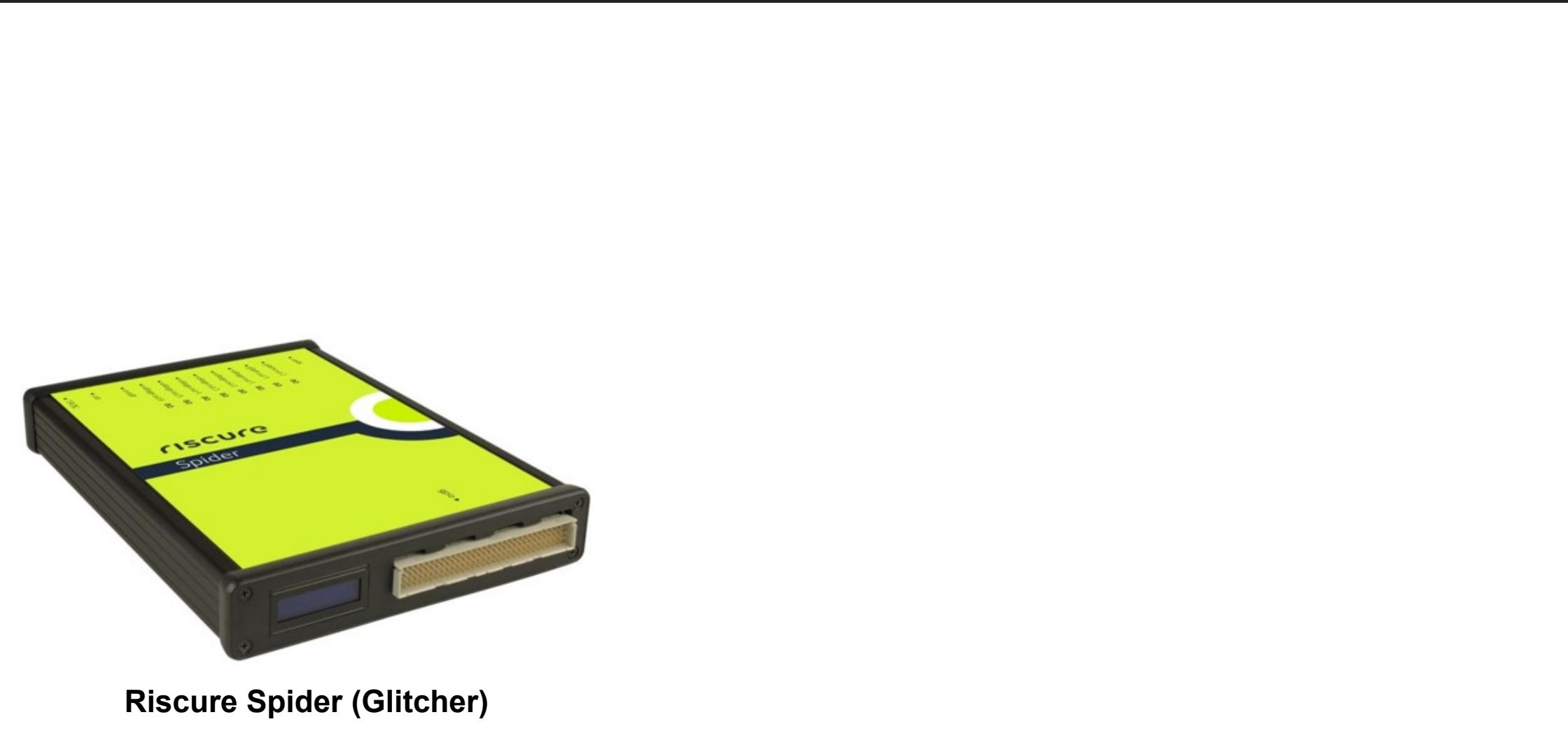


# FAULT INJECTION

*"Introducing faults into a target in order to change its intended behavior."*

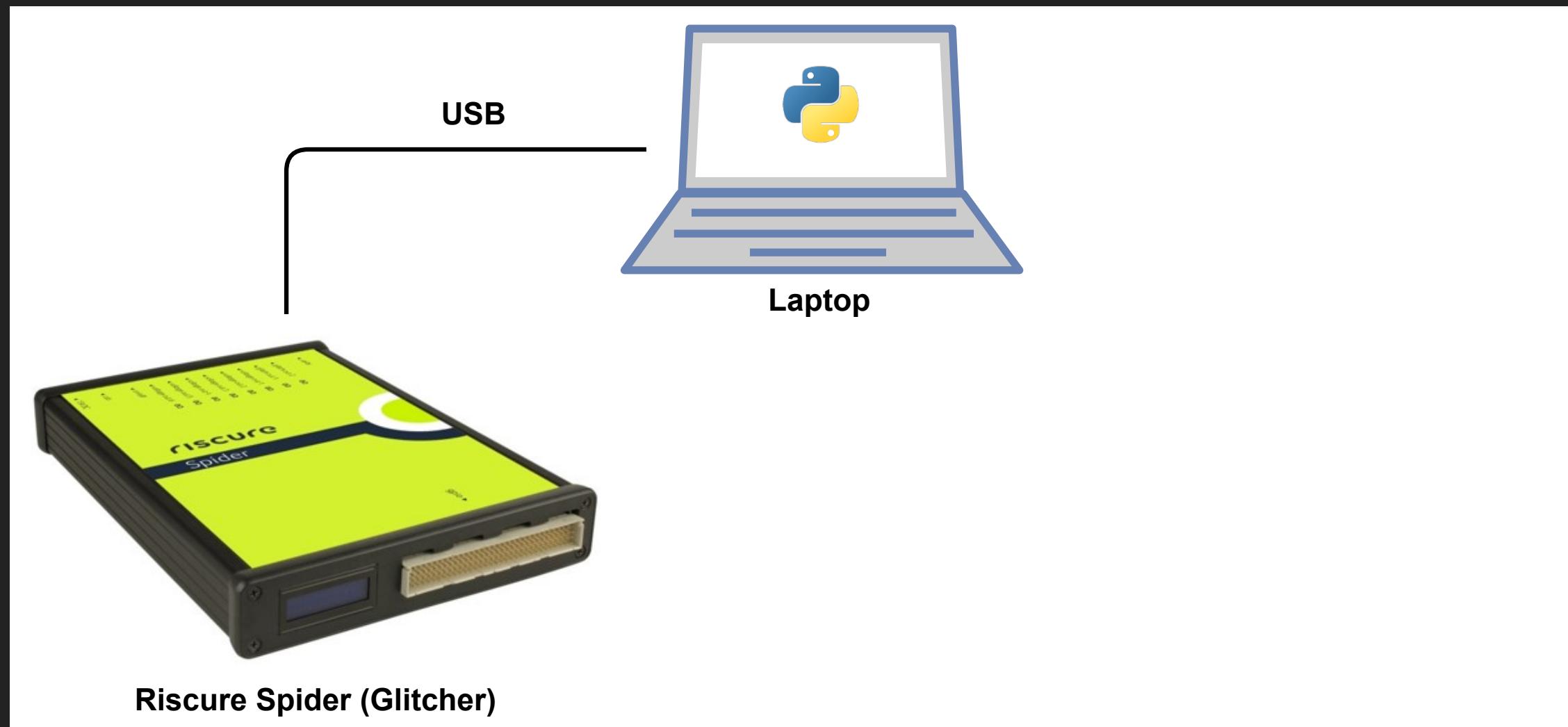


# FAULT INJECTION SETUP



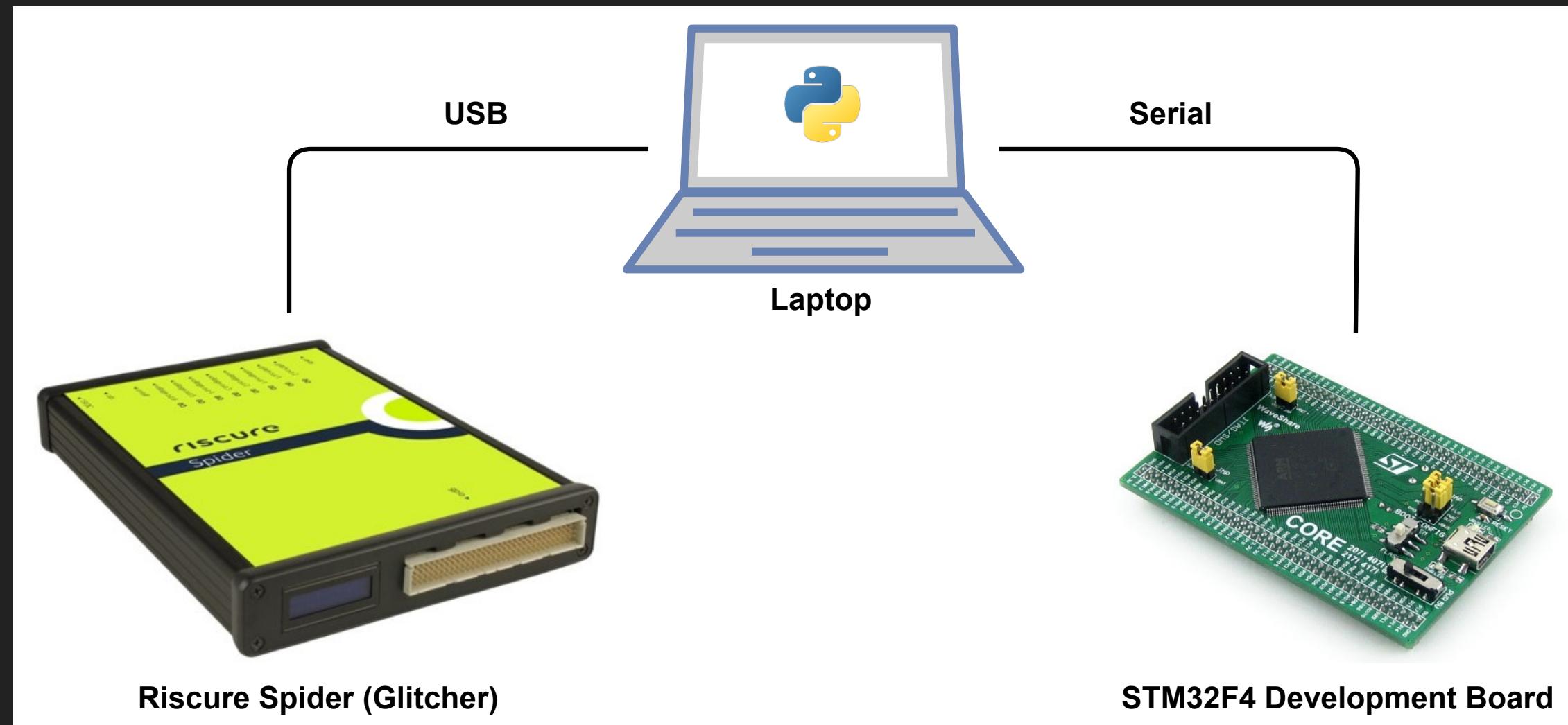
You can use NewAE's [ChipWhisperer](#) too!

# FAULT INJECTION SETUP



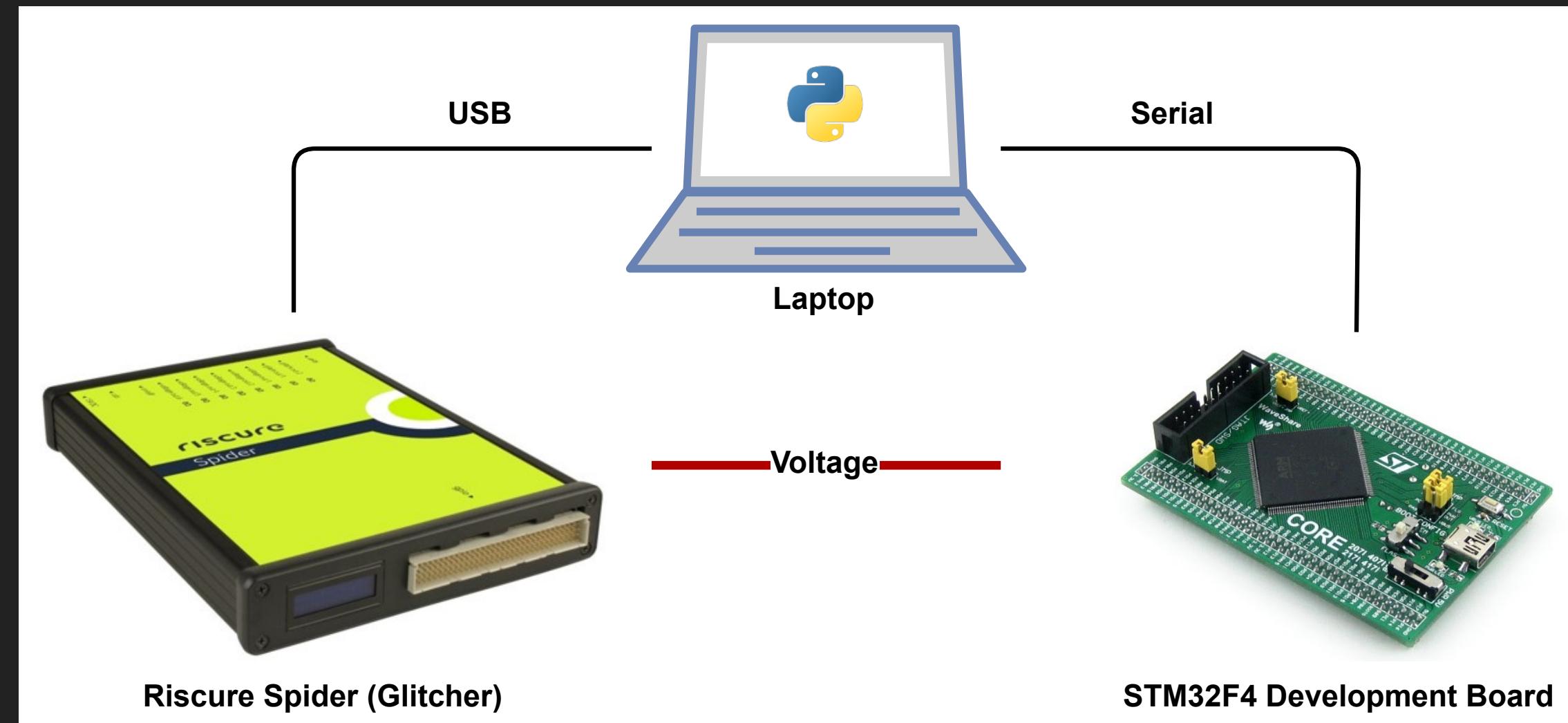
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# FAULT INJECTION SETUP



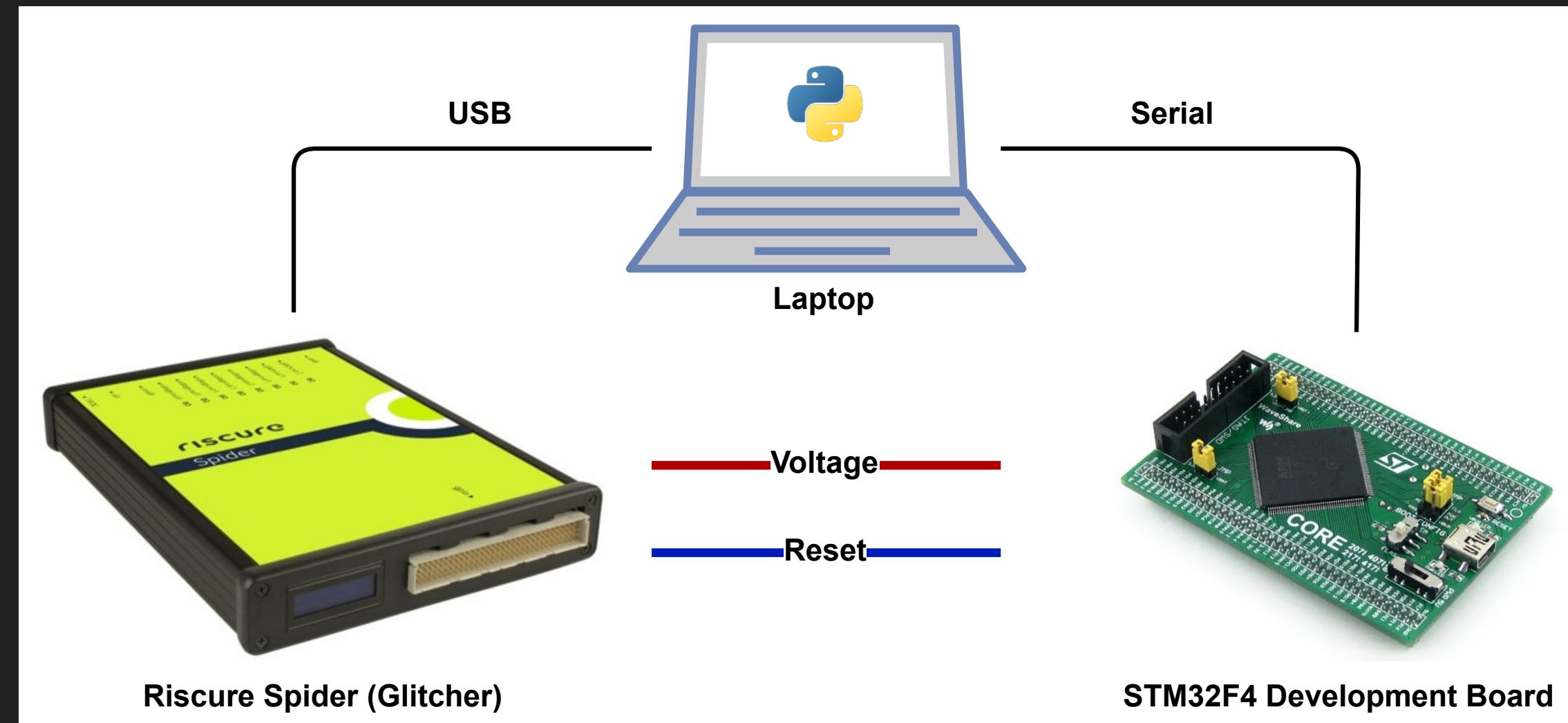
You can use NewAE's ChipWhisperer too!

# FAULT INJECTION SETUP



You can use NewAE's ChipWhisperer too!

# FAULT INJECTION SETUP



You can use NewAE's ChipWhisperer too!

# REAL WORLD SETUP



Even for simple setups there are cables everywhere...

# FAULT INJECTION FAULT MODEL

*Instruction corruption.*

- Glitches can modify instructions
- Great for modifying code and getting control
- Breaks any software security model

Original instruction:

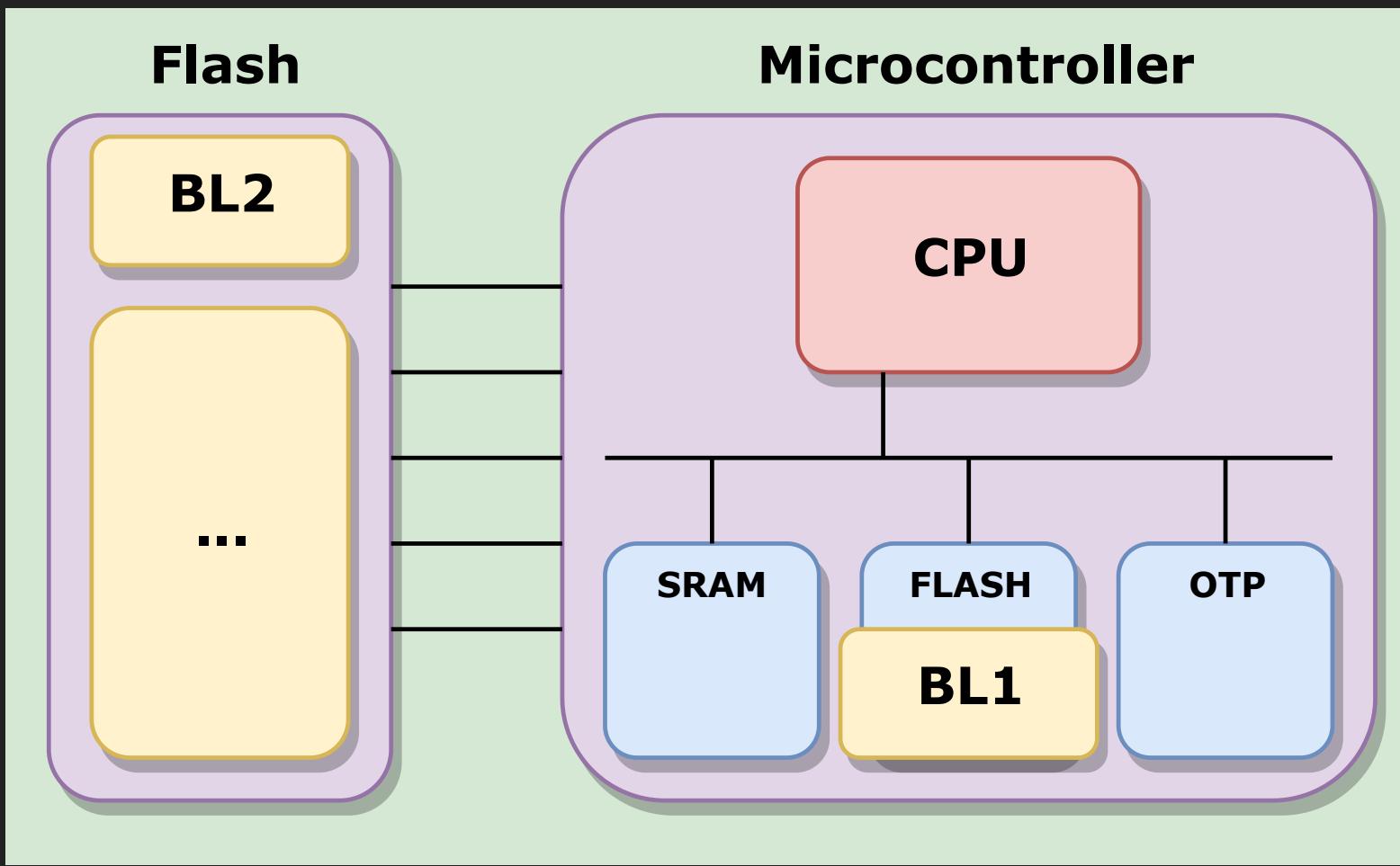
```
add r0, r1, r3 1110 1011 0000 0001  
          0000 0000 0000 0011
```

Glitched instruction:

```
add r0, r1, r2 1110 1011 0000 0001  
          0000 0000 0000 0010
```

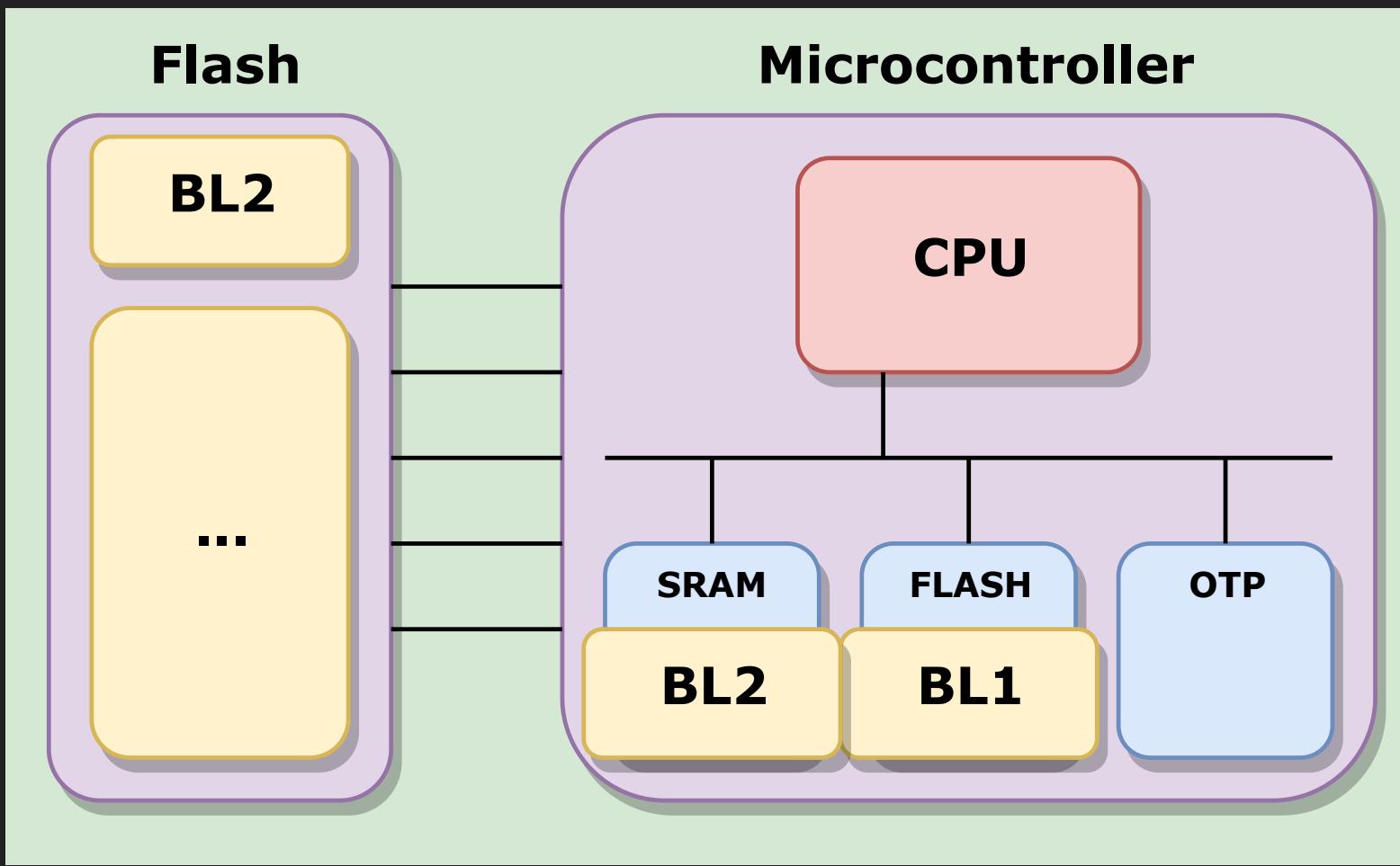
**LET'S USE IT TO BYPASS ENCRYPTED SECURE BOOT!**

# ENCRYPTED SECURE BOOT DESIGN



BL1 is executed from internal flash

# ENCRYPTED SECURE BOOT DESIGN



BL1 loads, decrypts and verifies BL2

# ENCRYPTED SECURE BOOT IMPLEMENTATION

```
memcpy(IMG_RAM, IMG_FLASH, IMG_SIZE); // 1. Copy image
decrypt(IMG_RAM, IMG_SIZE, KEY);
memcpy(SIG_RAM, SIG_FLASH, SIG_SIZE);

sha(IMG_RAM, IMG_SIZE, IMG_HASH);
rsa(PUB_KEY, SIG_RAM, SIG_HASH);

if(compare(IMG_HASH, SIG_HASH) != 0) {
    while(1);
}

((void *)())(IMG_RAM);
```

# ENCRYPTED SECURE BOOT IMPLEMENTATION

```
memcpy(IMG_RAM, IMG_FLASH, IMG_SIZE); // 1. Copy image
decrypt(IMG_RAM, IMG_SIZE, KEY);      // 2. Decrypt image
memcpy(SIG_RAM, SIG_FLASH, SIG_SIZE);

sha(IMG_RAM, IMG_SIZE, IMG_HASH);
rsa(PUB_KEY, SIG_RAM, SIG_HASH);

if(compare(IMG_HASH, SIG_HASH) != 0) {
    while(1);
}

( (void *)() ) (IMG_RAM)();
```

# ENCRYPTED SECURE BOOT IMPLEMENTATION

```
memcpy(IMG_RAM, IMG_FLASH, IMG_SIZE); // 1. Copy image
decrypt(IMG_RAM, IMG_SIZE, KEY); // 2. Decrypt image
memcpy(SIG_RAM, SIG_FLASH, SIG_SIZE); // 3. Copy signature

sha(IMG_RAM, IMG_SIZE, IMG_HASH);
rsa(PUB_KEY, SIG_RAM, SIG_HASH);

if(compare(IMG_HASH, SIG_HASH) != 0) {
    while(1);
}

( (void *)() ) (IMG_RAM)();
```

# ENCRYPTED SECURE BOOT IMPLEMENTATION

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memcpy(IMG_RAM, IMG_FLASH, IMG_SIZE); // 1. Copy image
decrypt(IMG_RAM, IMG_SIZE, KEY); // 2. Decrypt image
memcpy(SIG_RAM, SIG_FLASH, SIG_SIZE); // 3. Copy signature

sha(IMG_RAM, IMG_SIZE, IMG_HASH); // 4. Calculate hash from image
rsa(PUB_KEY, SIG_RAM, SIG_HASH);

if(compare(IMG_HASH, SIG_HASH) != 0) {
    while(1);
}

( (void *) () ) (IMG_RAM)();
```

# ENCRYPTED SECURE BOOT IMPLEMENTATION

```
memcpy(IMG_RAM, IMG_FLASH, IMG_SIZE); // 1. Copy image
decrypt(IMG_RAM, IMG_SIZE, KEY); // 2. Decrypt image
memcpy(SIG_RAM, SIG_FLASH, SIG_SIZE); // 3. Copy signature

sha(IMG_RAM, IMG_SIZE, IMG_HASH); // 4. Calculate hash from image
rsa(PUB_KEY, SIG_RAM, SIG_HASH); // 5. Obtain hash from signature

if(compare(IMG_HASH, SIG_HASH) != 0) {
    while(1);
}

( (void *) () ) (IMG_RAM)();
```

# ENCRYPTED SECURE BOOT IMPLEMENTATION

```
memcpy(IMG_RAM, IMG_FLASH, IMG_SIZE); // 1. Copy image
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if(compare(IMG_HASH, SIG_HASH) != 0) { // 6. Compare hashes
    while(1);
}

( (void *) () ) (IMG_RAM)();
```

# ENCRYPTED SECURE BOOT IMPLEMENTATION

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memcpy(IMG_RAM, IMG_FLASH, IMG_SIZE); // 1. Copy image
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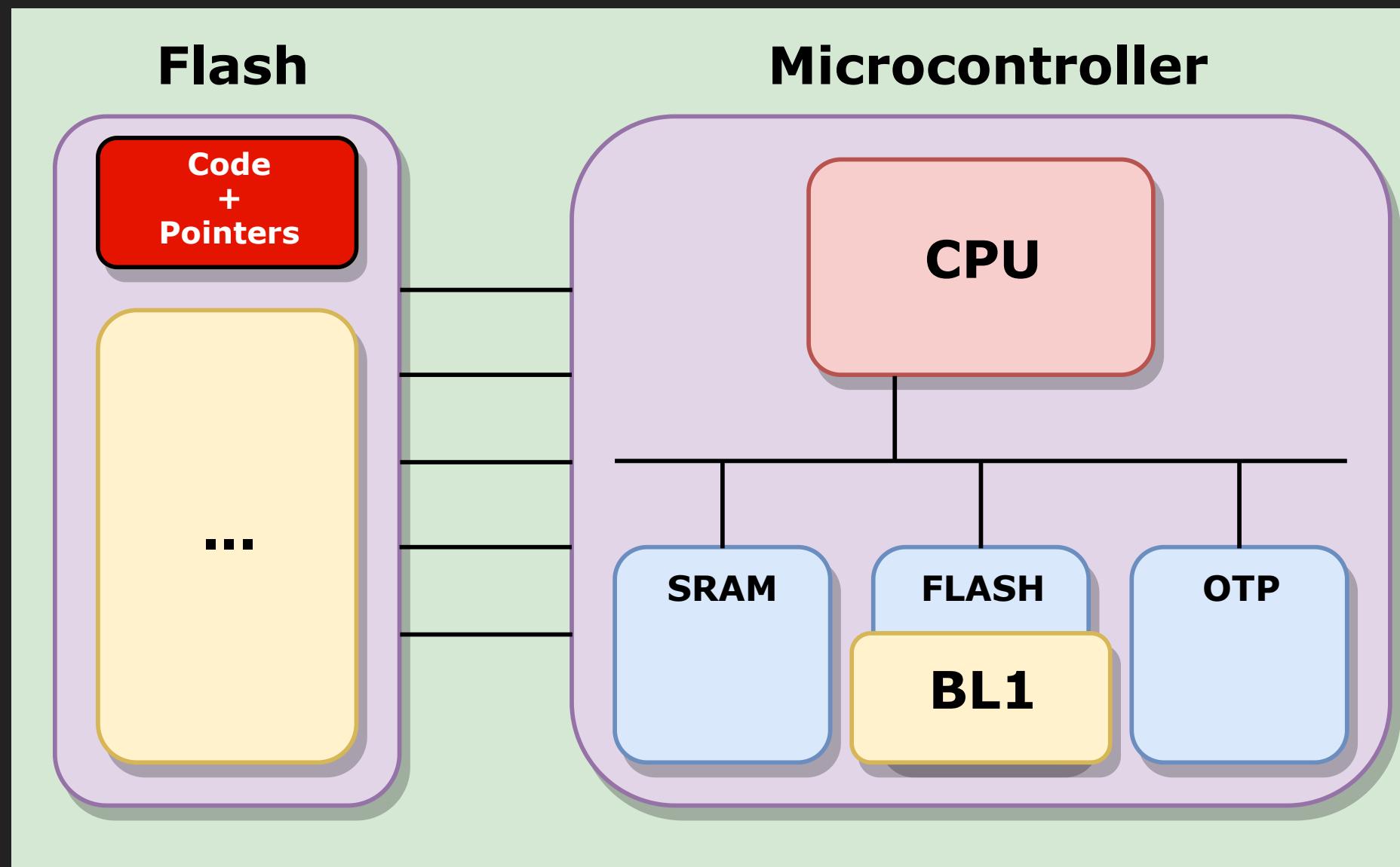
sha(IMG_RAM, IMG_SIZE, IMG_HASH); // 4. Calculate hash from image
rsa(PUB_KEY, SIG_RAM, SIG_HASH); // 5. Obtain hash from signature

if(compare(IMG_HASH, SIG_HASH) != 0) { // 6. Compare hashes
    while(1);
}

((void *)())(IMG_RAM); // 7. Jump to next image
```

# HOW DO WE ATTACK?

# BYPASSING ENCRYPTED SECURE BOOT



BL2 is replaced with code and pointers to SRAM

# FLASH IMAGE MODIFICATION AND BEHAVIOR

Valid BL2 image

```
00040000 E8 62 1C 31 8B 51 72 BC 48 06 0C 1B 4C 38 D9 B7 èb.1<Qr4H...L8Ù·  
00040010 7D E3 38 44 95 28 03 94 73 21 8D 44 90 FE 52 6B }ä8D• (."s!.D.pRk  
00040020 FB 0A B5 A4 84 6B E5 0D 05 16 97 76 0F 6C 1F 6F ü.µ„,kå...-v.1.o  
00040030 2A C3 61 9A AE FC 0E 55 3D E5 8B 77 3F 4D 61 23 *Ãaš@Ü.U=å<w?Ma#  
00040040 D1 B5 46 BE 6B 62 16 B7 07 CA 84 0C 37 09 9F 84 ÑµF%kb. .-È..7.Ý,  
00040050 2F 3E 77 C7 7C D7 0F A2 29 69 BD 46 82 C4 B2 3C />wÇ|x.c)iñF,Ä^<  
00040060 78 36 82 32 DD 0A 02 E6 51 F3 82 80 8D C4 A9 0C x6,2Ý..æQó,€.Ä@.  
00040070 32 E2 A4 AE 09 77 C5 E0 B7 00 CE 19 01 49 8F 84 2â¤@.wÅà..Í..I..  
00040080 E1 53 B4 83 74 A6 0C 96 6D 00 C1 BC 20 BF E6 7D áS'ft;.-m.Á4 {æ}  
00040090 3D 55 F5 48 AA C4 35 F5 FD 31 7B 9A C1 CA 86 96 =UÖH^Ä5öý1{šÁÈt-  
000400A0 32 E8 4E D6 98 F4 64 7B EE 35 58 AF 76 41 7B 2B 2èNÖ~öd{i5X~vA{+  
000400B0 4D 7F 16 F1 84 AC 96 E5 BD 56 1B 42 14 4E 14 99 M..ñ..-åV.B.N.^  
000400C0 0D 93 4C A5 83 E4 9D D7 59 7C D1 BC 2E 17 63 3C ."LYfä..xY|Ñ4..c<  
000400D0 C6 F5 21 86 A2 D8 C7 7F 2D 4F 98 58 AB 5A FD 48 Èö!tçØç.-O~X«ZÝH  
000400E0 73 FE 4D D5 34 7A 3D 42 C4 3C 48 85 39 B2 9F 2F spMÖ4z=BÄ<H..9^Ý/  
000400F0 7E 4E B0 30 D2 52 23 5C BE 17 74 C2 D5 15 38 FC ~N°0ÒR#\%tÅÖ.8ü
```

Malicious BL2 image

```
00040000 00 46 00 46 00 46 00 46 00 46 00 46 00 46 00 46 00 46 .F.F.F.F.F.F.F.F  
00040010 00 46 00 46 00 46 00 46 00 46 00 46 00 46 00 46 00 46 00 46 .F.F.F.F.F.F.F  
00040020 00 46 00 46 00 46 00 46 00 46 00 46 00 46 00 46 00 46 00 46 .F.F.F.F.F.F.F  
00040030 00 46 00 46 00 46 00 46 00 46 00 46 00 46 00 46 00 46 00 46 .F.F.F.F.F.F.F  
00040040 01 78 00 29 1D 00 4F6 00 03 C1 F0 00 03 1B 68 .x.).ÐDö..Äò...h  
00040050 4F EA D3 13 0F0 02 02 02 02 02 02 02 02 02 02 02 02 02 OèÓ..ö...+ôÐDö..  
00040060 C4 F2 00 03 99 80 00 F1 01 00 E9 E7 7A 46 A2 F1 Äò..”€.ñ..éçzFçñ  
00040070 03 02 10 47 0A 0A 54 68 61 6E 6B 20 79 6F 75 20 ...G..Thank you  
00040080 66 6F 72 20 69 6E 76 69 74 69 6E 67 20 75 73 21 for inviting us!  
00040090 21 21 21 21 21 0A 00 00 00 46 00 46 00 46 00 46 00 46 !!!!!....F.F.F.F  
000400A0 00 46 00 46 00 46 00 46 00 46 00 46 00 46 00 46 00 46 00 46 .F.F.F.F.F.F.F  
000400B0 ED 3A 00 20 ED 3A 00 20 ED 3A 00 20 ED 3A 00 20 i:. i:. i:. i:.  
000400C0 ED 3A 00 20 ED 3A 00 20 ED 3A 00 20 ED 3A 00 20 i:. i:. i:. i:.  
000400D0 ED 3A 00 20 ED 3A 00 20 ED 3A 00 20 ED 3A 00 20 i:. i:. i:. i:.  
000400E0 ED 3A 00 20 ED 3A 00 20 ED 3A 00 20 ED 3A 00 20 i:. i:. i:. i:.  
000400F0 ED 3A 00 20 ED 3A 00 20 ED 3A 00 20 ED 3A 00 20 i:. i:. i:. i:.
```

Valid BL2 image UART output

```
[BL1] : Successfully started.  
[BL1] : Loading BL2 successful.  
[BL1] : Decrypting BL2 successful.  
[BL1] : Authenticating BL2  
successful.  
[BL1] : Jumping to BL2...  
[BL2] : Successfully started.
```

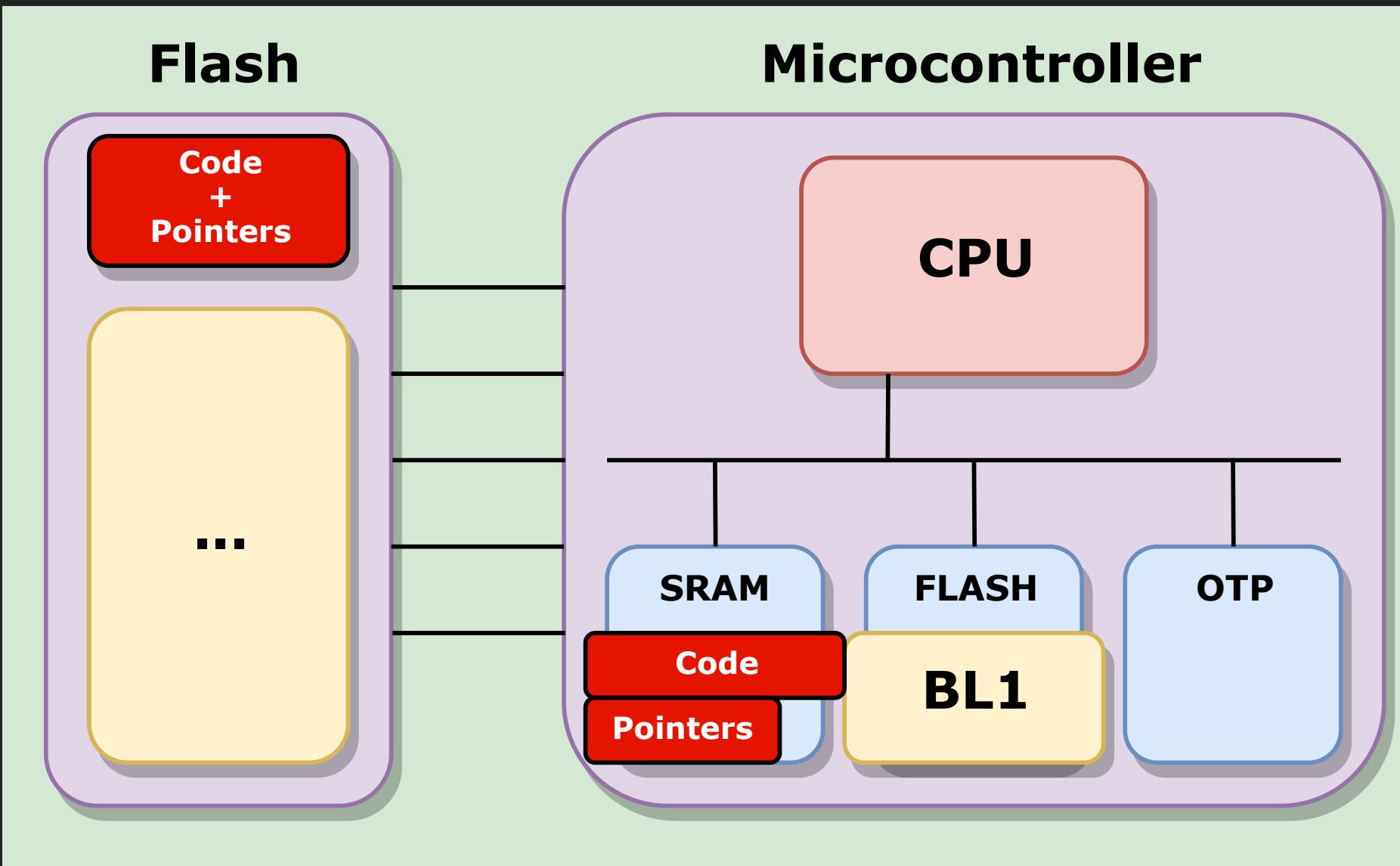
Malicious BL2 image UART output

```
[BL1] : Successfully started.  
[BL1] : Loading BL2 successful.  
[BL1] : Decrypting BL2 successful.  
[BL1] : Authenticating BL2  
unsuccessful. Stopping!
```

CODE  
POINTERS

**WHEN DO WE INJECT THE GLITCH?**

# BYPASSING ENCRYPTED SECURE BOOT



Glitch is injected after code is copied and while pointers are being copied.

# BYPASSING ENCRYPTED SECURE BOOT

Control flow is hijacked. The decryption and verification of the image is bypassed!

# LET'S DO THIS!

On another laptop...

CONCRETELY SAID...

WE TURN

*ENCRYPTED SECURE BOOT*

INTO

*PLAIN TEXT UNPROTECTED BOOT*

USING

*A SINGLE GLITCH AND NO KEY!*

# WHY DOES THIS WORK?

# CONTROLLING PC

- Glitch controlled value into PC directly (see: [paper](#))
- LDM/STM instructions used for copying memory

Original:

```
08008102
08008102 loop_0
08008102 LDMIA.W R1!, {R4-R11}
08008106 STMIA.W R0!, {R4-R11}
0800810A SUBS    R2, #0x20
0800810C BNE     loop_0
```

Glitched:

```
08008102
08008102 loop_0
08008102 LDMIA.W R1!, {R4- PC.}
08008106 STMIA.W R0!, {R4-R11}
0800810A SUBS    R2, #0x20
0800810C BNE     loop_0
```

- Demonstrated attack is 32-bit ARM specific
- Variants of this attack applicable to all architectures

IS THIS THE ONLY FI ATTACK ON SECURE BOOT?

# ENUMERATION OF FI ATTACKS ON SECURE BOOT

## Bypassing Secure Boot using Fault Injection

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Albert Spruyt

*Freelance*

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Cristofaro Mune

*Pulse Security*

*c.mune@pulse-sec.com*

Please see our offensive paper!

**IT'S TIME TO DESIGN SECURE BOOT SECURELY...**

**LET'S GET THE FUNDAMENTALS RIGHT!**

# SECURE BOOT FUNDAMENTALS

- Hardware root of trust
- Authenticate everything
- Encrypt everything
- Use strong crypto
- Use crypto correctly

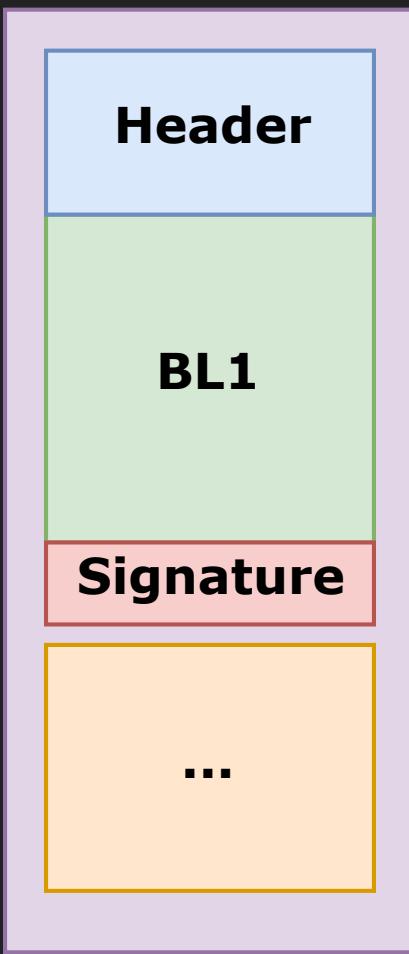
We assume you all agree. But... it goes often wrong!

# HARDWARE ROOT OF TRUST

How many devices do you know without ROM/OTP?

Real world Secure Boot bypass:  
Intel's [Root of Trust](#) using SPI flash.

# AUTHENTICATE EVERYTHING



How does the ROM know how large the image is?

# AUTHENTICATE EVERYTHING



```
struct header {  
    uint32_t  BL1_length;  
    uint32_t  BL1_destination;  
    uint32_t  BL1_entry_point;  
} _header;
```

Length and destination are used before verification

# AUTHENTICATE EVERYTHING



```
struct header {  
    uint32_t BL1_length;  
    uint32_t BL1_destination;  
    uint32_t BL1_entry_point;  
    uint8_t BL1_header_sig[0x100];  
} _header;
```

Header needs its own signature

# AUTHENTICATE EVERYTHING

- Authenticate all security relevant code and data
- Try to prevent mistakes:
  - Design should enforce authenticating everything

Real world Secure Boot bypass:  
[AMD Secure Boot](#) by CTSLabs

# ENCRYPT EVERYTHING

*Are u proposing security by obscurity?*

- There will be software vulnerabilities
- Make analyzing the firmware hard
- Attacks may be more difficult to perform

**FUNDAMENTALS MAKE SENSE... WHAT ELSE?**

## KEEP IT SIMPLE

- Nobody wants complex parsing during boot
- Do not support the world (especially in ROM)
- Make auditing the code easier

Real world Secure Boot bypass:  
[U-Boot vulnerability](#) in file system parser

# DROP PRIVILEGES ASAP

- Not just operating modes:
  - Monitor, Hypervisor, Kernel, User
- But also access to:
  - Keys, ROM, crypto engines

**LET'S ASSUME THE DESIGN IS GREAT!**

**BUT CONTAINS SOFTWARE VULNERABILITIES...**

# EXPLOITATION MITIGATIONS AT RUNTIME

STACK CANARY	NX	PIE	RPATH	RUNPATH	Symbols	FORTIFY
Canary found	NX enabled	PIE enabled	No RPATH	No RUNPATH	No Symbols	Yes

- Binaries are hardened by the compiler
- Operating system makes exploitation difficult too
- Stack cookies, W^X, ASLR, CFI, etc.

**DO YOU THINK THAT'S DONE AT EARLY BOOT?**

## MOST EARLY BOOT STAGES DO:

- not have stack cookies
- not have ASLR
- not have CFI
- not have the MPU/MMU enabled/configured
- not have IOMMU/SMMU enabled/configured

**COME ON! IT'S 2019...**

## YOU MAY GET THESE ALMOST FOR FREE:

- Stack cookies
- Control flow integrity (CFI)

## MEMORY PROTECTION MAY BE MORE CHALLENGING:

- MPU/MMU
  - W<sup>^</sup>X
- IOMMU/SMMU
  - Prevent DMA from overwriting code/data

BUT WAIT...

WHAT ABOUT HARDWARE HACKERS?

EVERYTHING APPLIES!

PLUS SOME MORE...

# PCB LEVEL ATTACKS

- An attacker can tamper with signals on the PCB
- Copy data from external memory once
  - Operate only on the internal copy
  - Prevent TOCTOU / Double Fetch vulnerabilities
- Flash emulator



## LOCK DOWN YOUR HARDWARE

- Disable peripherals that are not used
  - e.g. external memories, USB, etc.
  - No access to external flash; no TOCTOUs
- Disable or protect JTAG/DEBUG ports
- Disable debug messages on serial ports

# WHAT ABOUT ATTACKERS WITH MORE THAN A:

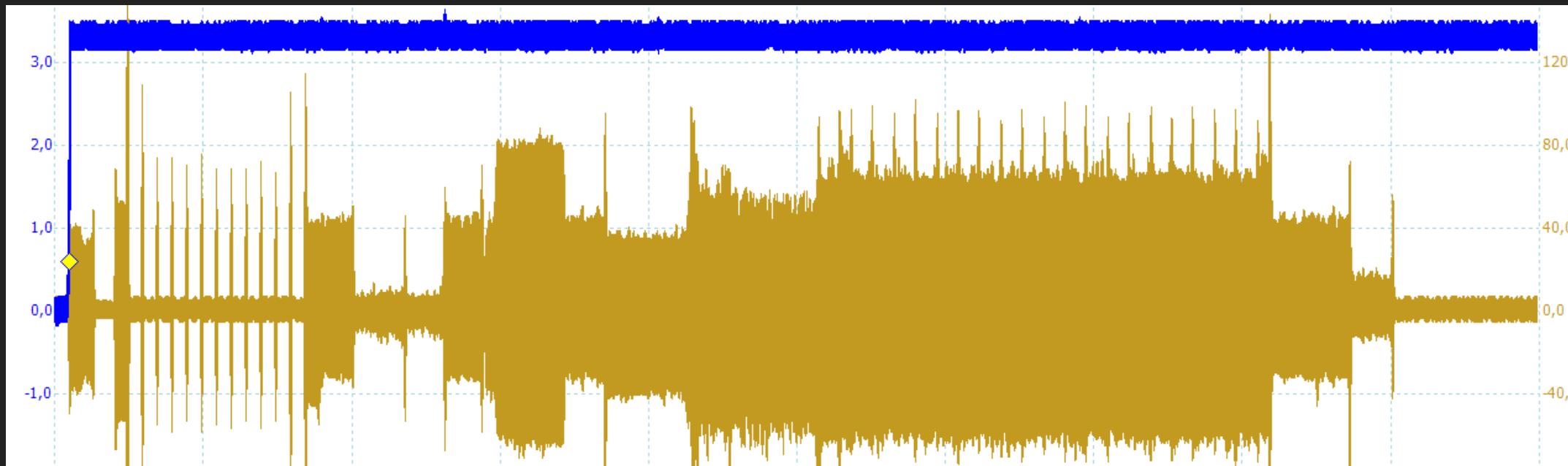


**ONLY PEW PEW PEW LIKE IN THE DEMO?**

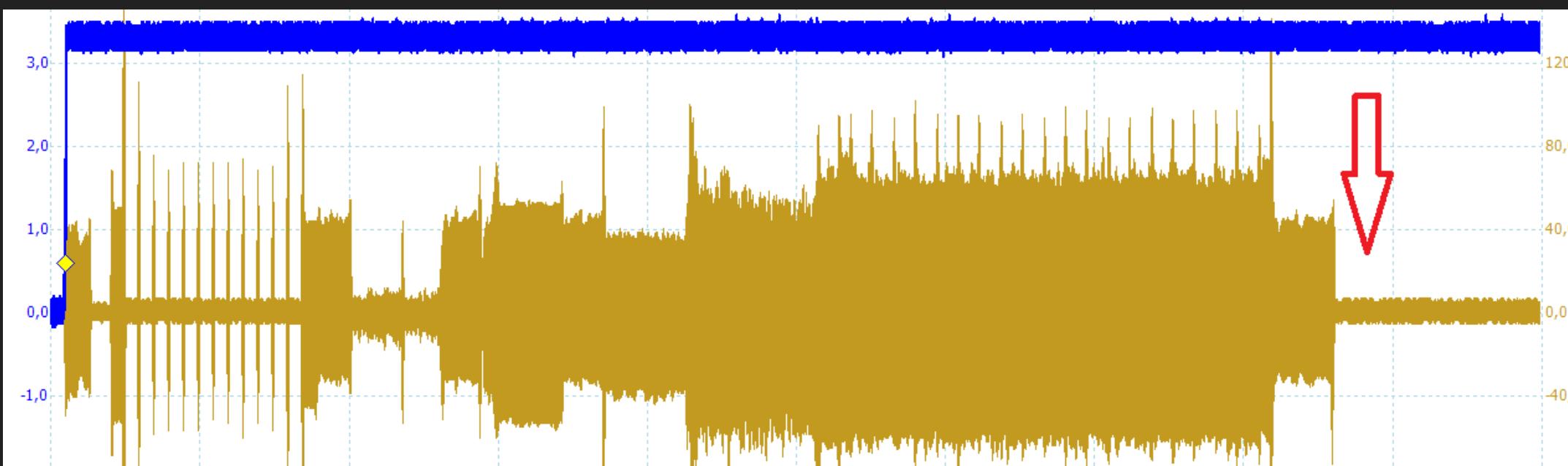
# SIDE CHANNEL ATTACKS

# WHAT ARE SIDE CHANNEL ATTACKS? 1/2

Power consumption of a valid image



Power consumption of an invalid image



**IS THIS THE ONLY SIDE CHANNEL?**

## WHAT ARE SIDE CHANNEL ATTACKS? 2/2

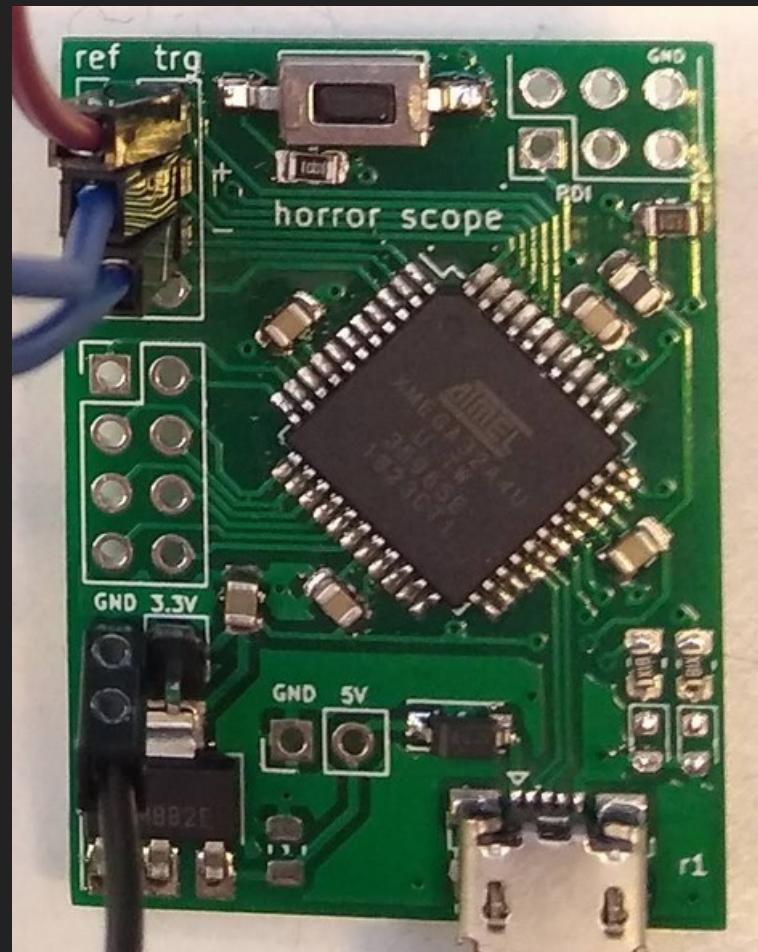
- Timing attacks to recover HMAC/CMAC
  - Real world example: [Xbox 360](#)
- DPA attack to recover encryption keys

Do not expect secrets (i.e. keys) will be secret forever!

ARE FI AND SCA ATTACKS EXPENSIVE?

# FI AND SCA ARE NOT (ALWAYS) EXPENSIVE!

- The HorrorScope (\$5)
  - By Albert and Alyssa ([@noopwafel](#))
  - FI and SCA
- Alternatives:
  - Any board with fast ADC/GPIO (free?)
  - ChipWhisperer Nano (~\$50)



Please see our presentation at Troopers 2019!

SCA AND ESPECIALLY FI ARE REAL THREATS!

LET'S MAKE FAULT INJECTION HARDER!

## GOALS WHEN MITIGATING FI

- Lower the probability of success
- Low enough probability equals infeasible
  - infeasible equals *takes too much time*

## HARDENING HARDWARE (ICS) AGAINST FI

- Redundancy
- Checksums
- Clock jitter
- Glitch/Fault detectors

Lots of academic research e.g.:

The Sorcerer's Apprentice Guide to Fault Attacks

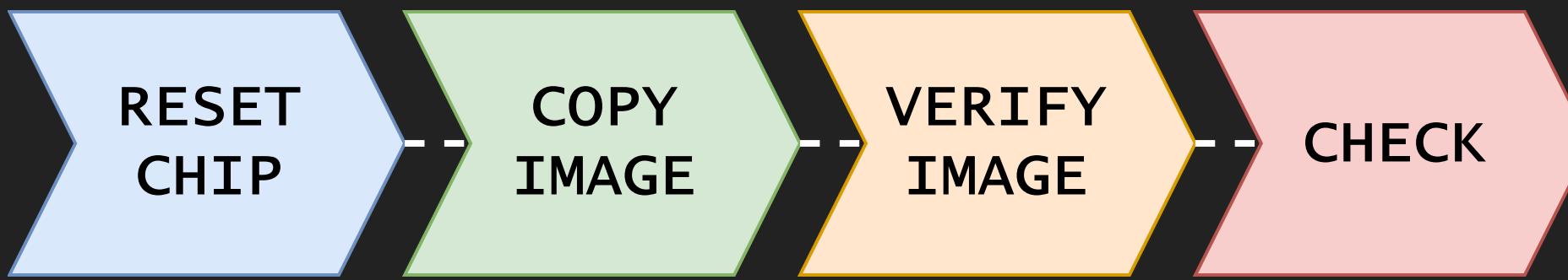
## CHALLENGES FOR HARDWARE MITIGATION

- Hardware is fixed
- Adding hardware is costly
- Detectors need calibration

*FI resistant hardware is not yet realistic for most devices!*

WHAT CAN BE DONE  
WITHOUT MODIFYING HARDWARE?

# LET'S MAKE BYPASSING A CHECK HARD



USING STANDARD HARDWARE AND SOFTWARE!

# MAKING BYPASSING A CHECK HARD

*Multiple checks*

- Identify all critical checks in your code
- Perform these checks multiple times

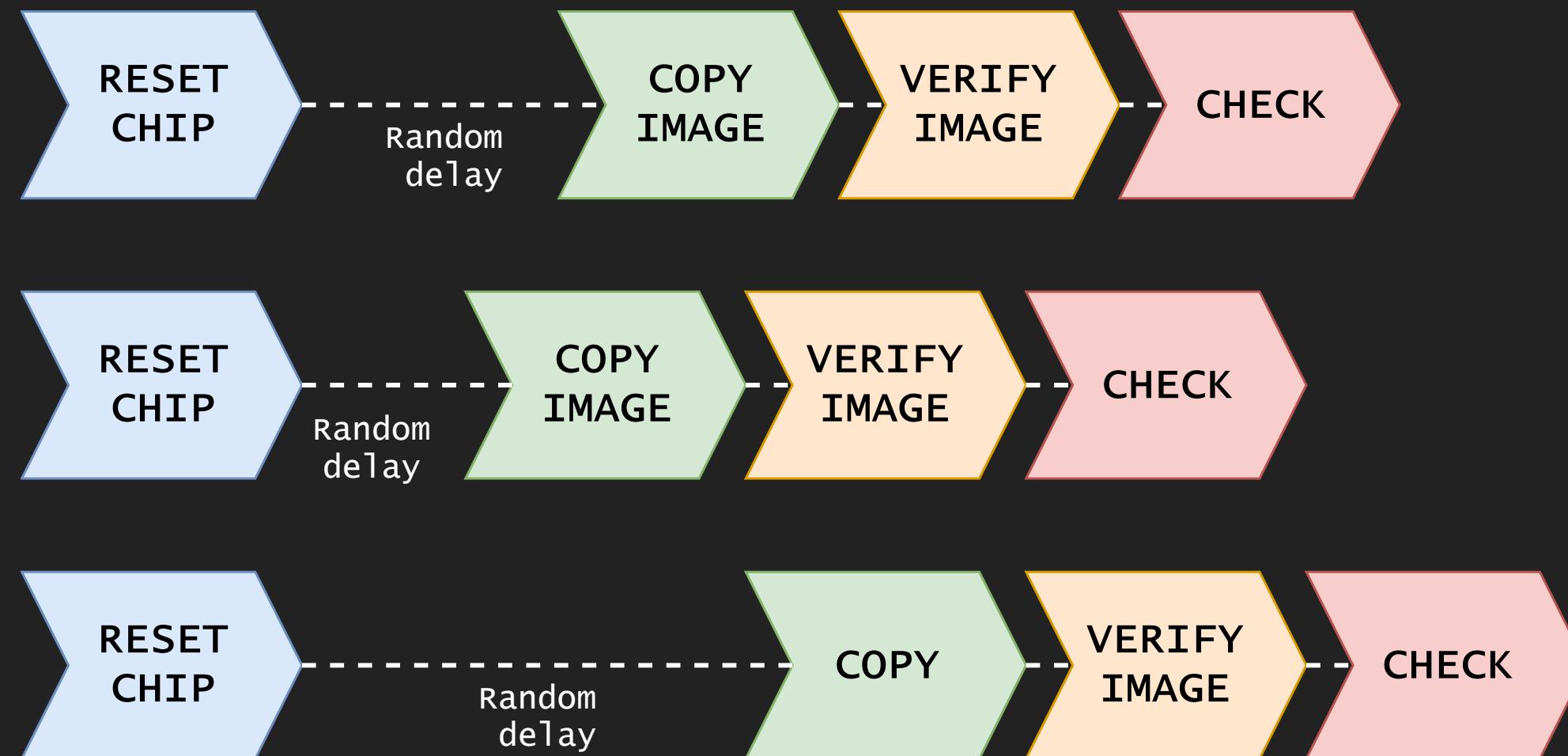


- Probability for success will likely drop

# MAKING BYPASSING A CHECK HARD

*Random delays*

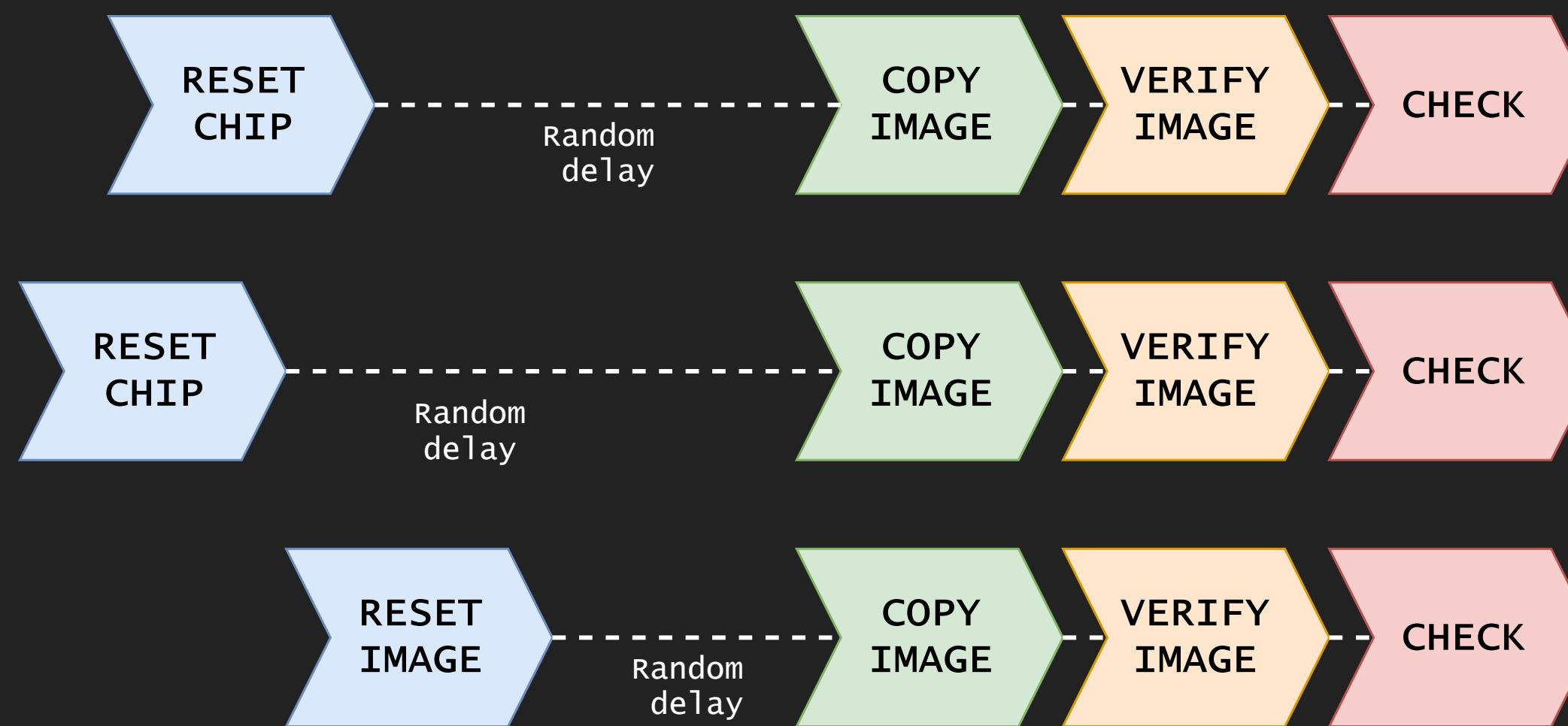
- Randomize critical checks in time



- Probability for success will likely drop more

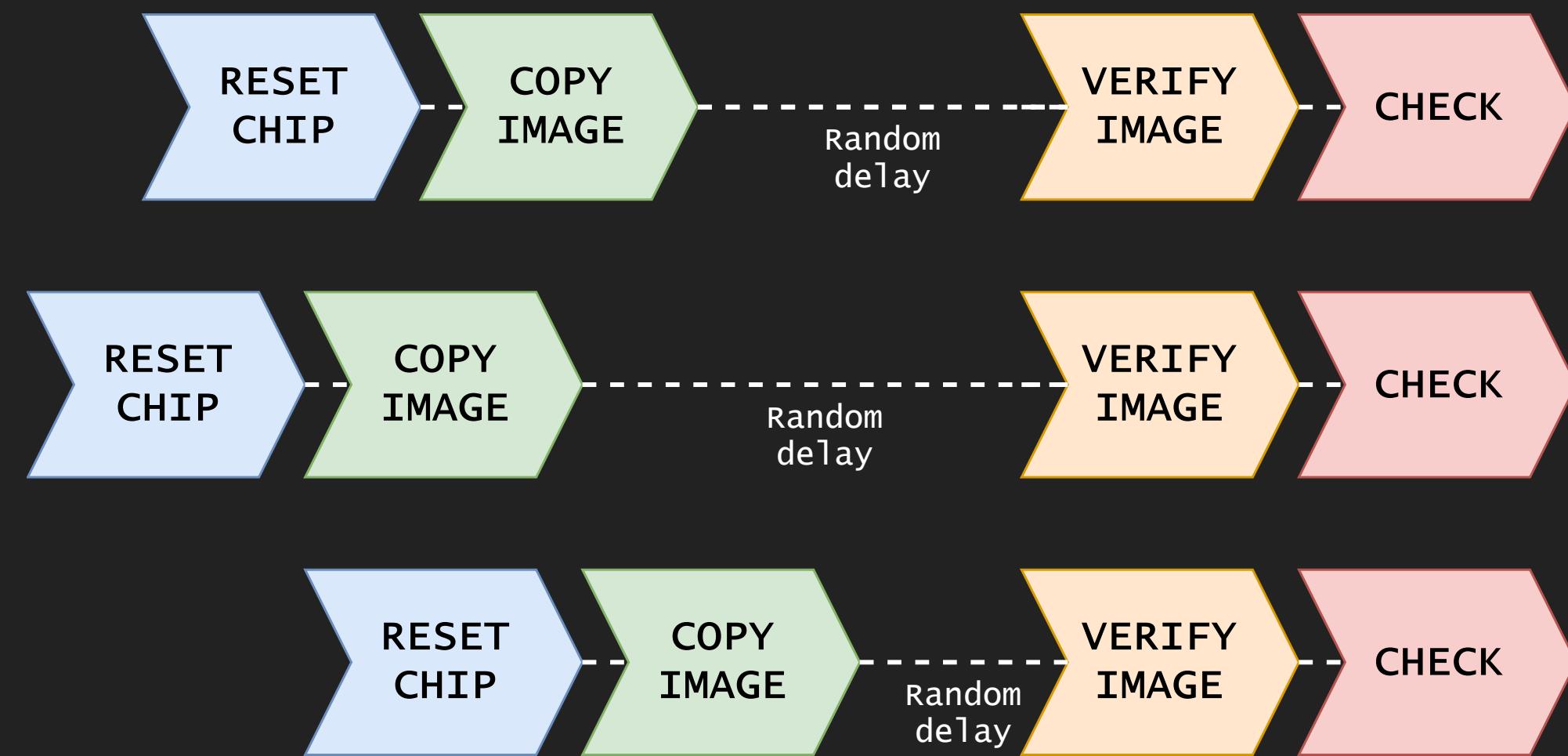
**WHAT GOES WRONG?**

# BAD RANDOM DELAY #1



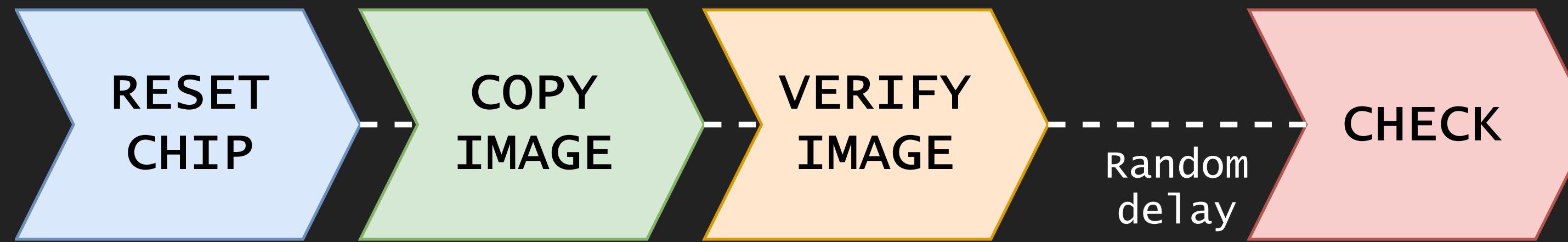
External SPI communication can be used for timing!

## BAD RANDOM DELAY #2



Power consumption can be also used for timing!

# GOOD RANDOM DELAY!

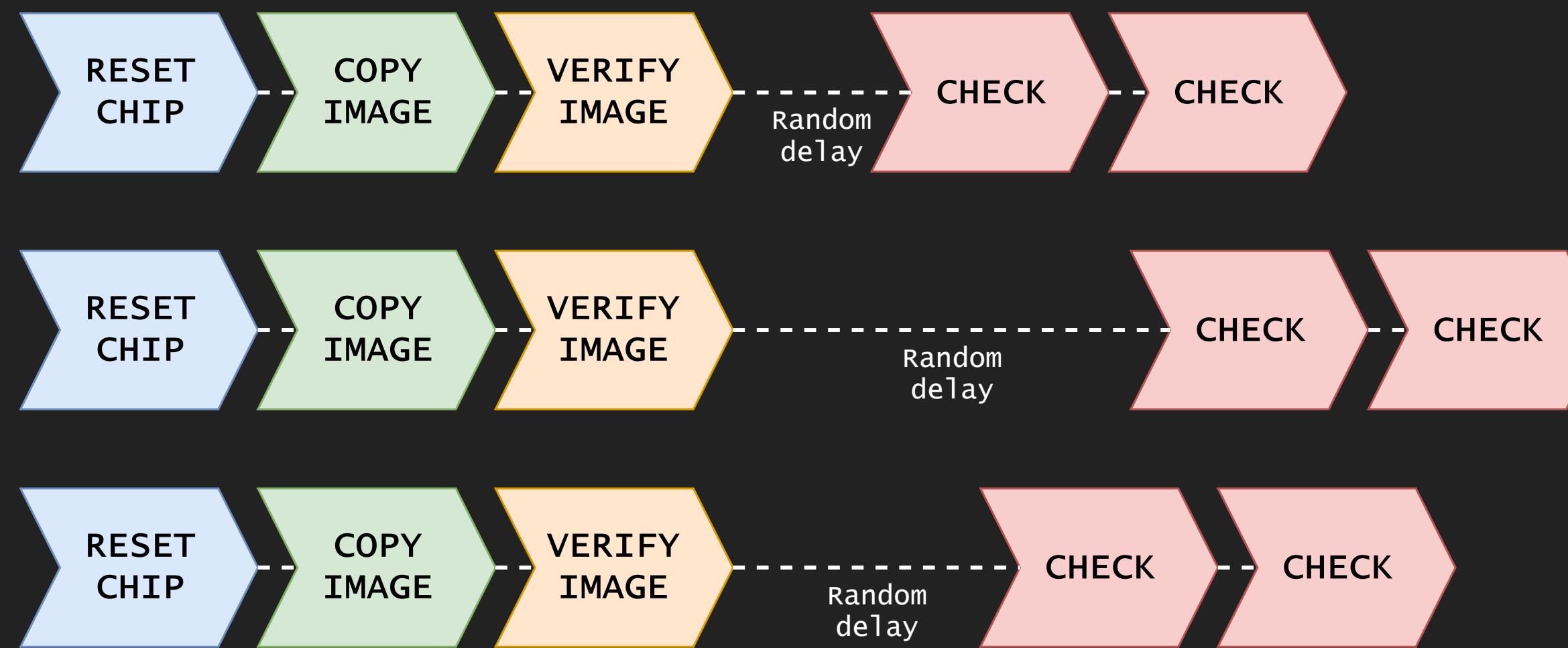


Little time after random delay to inject glitch

WHAT ABOUT COMBINING  
MULTIPLE CHECKS AND RANDOM DELAYS?

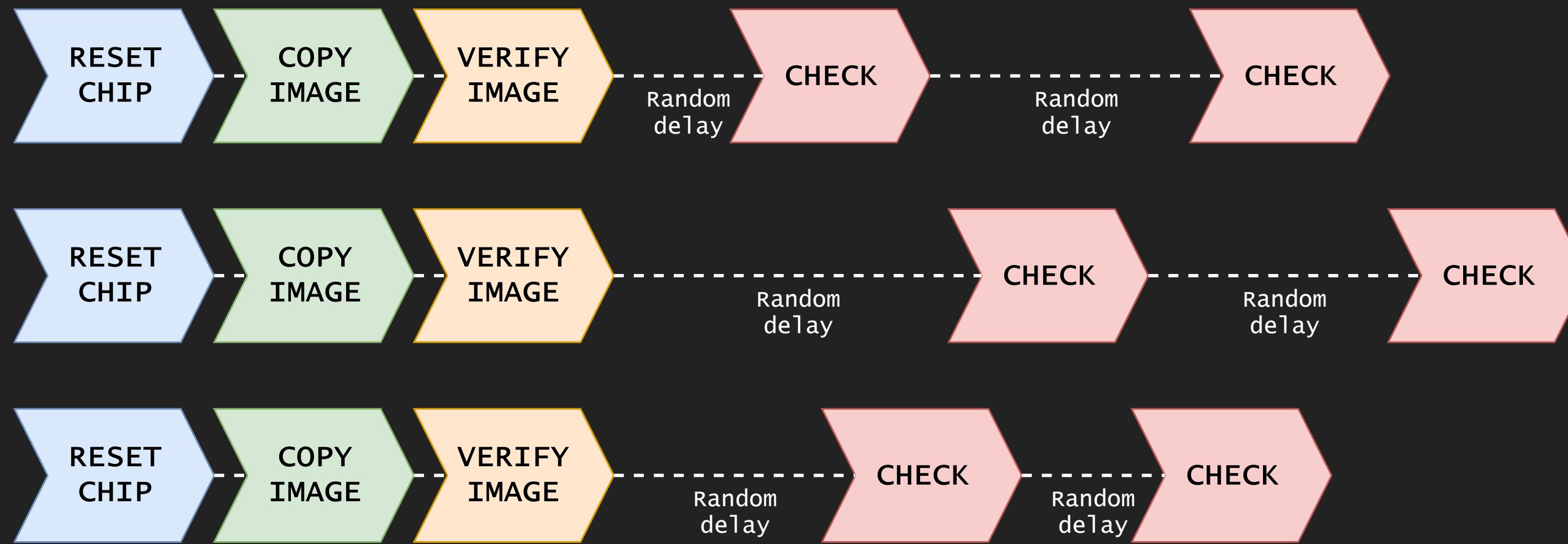
# COMBINED MITIGATION #1

*Random delays + Multiple checks*



What could be improved?

# BETTER COMBINATION!



Probability for success drops significantly!

Let's combine some more...

## COMBINED MITIGATION #2

*W^X + Multiple checks*

Let's use it to mitigate the attack from the demo!

## COMBINED MITIGATION #2: MULTIPLE CHECKS

```
memcpy(IMG_RAM, IMG_FLASH, IMG_SIZE);
memcpy(SIG_RAM, SIG_FLASH, SIG_SIZE);
sha(IMG_RAM, IMG_SIZE, IMG_HASH);
rsa(PUB_KEY, SIG_RAM, SIG_HASH);

if(compare(IMG_HASH, SIG_HASH) != 0) { // Compare hashes
    while(1);
}
if(compare(IMG_HASH, SIG_HASH) != 0) { // Compare hashes again
    while(1);
}
((void *)())(IMG_RAM)();
```

## COMBINED MITIGATION #2: MULTIPLE CHECKS + W^X

```
makeWritable(IMG_RAM, IMG_SIZE);           // Make IMG_RAM read-write

memcpy(IMG_RAM, IMG_FLASH, IMG_SIZE);
memcpy(SIG_RAM, SIG_FLASH, SIG_SIZE);

sha(IMG_RAM, IMG_SIZE, IMG_HASH);
rsa(PUB_KEY, SIG_RAM, SIG_HASH);

if(compare(IMG_HASH, SIG_HASH) != 0) {
    while(1);
}

makeExecutable(IMG_RAM, IMG_SIZE);           // Make IMG_RAM executable

if(compare(IMG_HASH, SIG_HASH) != 0) {
    while(1);
}

((void *)())(IMG_RAM);
```

## COMBINED MITIGATION #2

*W^X + Multiple checks*

- Control flow cannot be hijacked at the memcpy
- The code needs to be made executable
- Multiple glitches required to bypass secure boot

**THESE ARE JUST SOME EXAMPLES... BE CREATIVE!**

# KEY TAKEAWAYS

1. Secure boot design is hard (even for experts)
2. Smart secure boot design saves money
3. Software mitigations can be cheap
4. Stacking different mitigations can be effective
5. Testing is essential to verify the implementation

# THANK YOU. QUESTIONS?

*Do you think Secure Boot implementations can be improved significantly without significant costs?*

**riscure**

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*Riscure is **hiring** and visit our booth!*