

University of São Paulo
Institute of Mathematics and Statistics
Bachelor in Computer Science

Rafael Mota Gregorut

**Synthesising formal properties
from statechart test cases**

São Paulo
December 2015

Synthesising formal properties from statechart test cases

Final monograph for the course
MAC0499 – Final Graduation Project.

Supervisor: Ana Cristina Vieira de Melo

São Paulo
December 2015

Abstract

This project explores the theme of software quality through the perspectives of testing and formal methods. Testing is a crucial activity in the software development cycle to provide the reliability of systems. In order to validate the system requirements, the test cases are created based on the requirements contained in the specification. Also concerned with systems quality assurance, formal methods provides several techniques and tools to be used during specification, design and verification phases. Statecharts are a particular kind of formal specification based on finite state machines mostly used to model reactive system behaviours. Testing and formal methods can be seen as complementary approaches to assure software quality. Testing can show the presence of errors, while formal verification, such as model checking, can prove their absence. These two techniques are very costly in software development and each one requires that developers are specially trained to each activity. For the software formal verification, one needs to define properties in a certain specification language, which requires strong mathematical background. On the other hand, generating test case from specifications is time consuming and very error prone if it is manually executed. To facilitate the creation of test cases and the specification of formal properties by developers, two techniques were studied and implemented. First, we automatically generate test cases for a given Statechart model. Second, we automatically synthesise properties based on the test cases previously obtained. This monograph was prepared for the course MAC0499 - Final Graduation Project, at the Institute of Mathematics and Statistics of the University of São Paulo.

Keywords: Statecharts, Testing, Test cases, Test case mining, Formal properties

Contents

1	Introduction	1
1.1	Motivation	2
1.2	Goals	2
1.3	Organization	2
2	Concepts	3
2.1	Statecharts and Automata	3
2.1.1	Nondeterministic finite automata	3
2.1.2	Statechart models	4
2.1.3	A statechart and its corresponding automaton	6
2.2	Tests	6
2.2.1	Testing goals	7
2.2.2	The process of testing	7
2.2.3	Functional and Structural tests	8
2.3	Test cases	10
2.3.1	Designing test cases	10
2.3.2	Automatic generation of test cases	13
2.4	Model checking	15
2.4.1	Property definition	16
2.4.2	Specification patterns	17
2.5	Sequential pattern mining	18
2.5.1	An example: Web usage mining	18
2.5.2	Sequential pattern mining algorithms	18
3	Test case generation for statecharts	21
3.1	Test cases for simple statecharts	21
3.2	Test cases for complex statecharts: hierarchy	24
3.3	Test cases for complex statecharts: orthogonality	30
4	Formal property extraction from test cases	33
4.1	Test case mining	33
4.1.1	The SPMF framework	34

4.1.2	Test case mining example	34
4.2	Generation of properties from most frequent test case patterns	34
4.2.1	Response property specification	35
4.2.2	Existence property specification	37
4.3	Generation of properties for specific events	37
5	Tools demonstration	41
5.1	Test case generation tool	42
5.2	Formal property specification tool	42
6	Conclusion	49
7	Subjective chapter	51
7.1	Learning	51
7.2	Challenges	51
7.3	Courses	51
A	Linear Temporal Logic (LTL)	53
A.1	Syntax	53
A.2	Semantics	53
	Bibliography	55

Chapter 1

Introduction

Testing is one of the most used techniques to assure the quality of systems in software engineering and it typically consumes from 40% up to 50% of the software development effort [18]. As presented in [2], the goal of testing depends on the maturity level of the organization that is executing the tests: from debugging code, at a more inexperienced level, to a mental discipline that helps all professionals in the software industry to increase quality, at a higher level. In the literature, the testing techniques are categorized in structural (white-box) and functional (black-box), tests. The first category considers the code as the source for test cases and contributes mainly to code maintenance. Functional tests make usage of the specified requirements in order to create the test cases and are useful to validate the observed behaviour of the system against what was expected in the requisites.

Thus, it is notable that the requirement specification is an important source of material not only for the development of the code, but for testing as well. Statecharts are a type of formal software specification based on finite states machines (FSM) specially used in complex systems modeling, such as reactive systems[15]. They contain states, transitions and input events similar to an automaton, but offer resources to model hierarchy of states, concurrency and communication between process.

Formal methods are a set of techniques and tools which supports not only rigorous specification, but also design and verification of computer systems [11]. They are mostly used in critical components of safety critical systems and some approaches can be used to apply them in systems: apply formal methods to requirements and high-level designs where most of the details are abstracted away, apply them to only the most critical components, automate as much of the verification as possible [23].

Formal methods and testing can be considered complementary techniques in software engineering, both with the target to reduce the number of errors and increase the reliability of the system[6]. Even though testing is the activity that is most commonly used in industry to assure the software quality, it cannot guarantee the absence of bugs in the code, as stated by Dijkstra [7]. Formal approaches, such as formal verification, on the other hand, can prove the correctness of the code regarding a specification. The model checking technique, for instance, proves that certain user defined properties are true for a given model of the system.

This project proposes a study on the set of test cases generated from specifications in order to guide formal properties definitions. First, a technique to automatically generate test cases from statechart specifications is presented. Then, all test cases are observed and a technique to synthesize formal properties (written in linear temporal logic[16, 29]) from the acquired test cases is proposed.

1.1 Motivation

In the model driven development, test cases to validate systems can be generated from a model that represents how the system must behave. For instance, if the services to be provided by a system are described in Statecharts models, one can automatically generate test cases for programs from such models. However, properties to be proved in a system are not synthesized from such models. In general, developers take the system specification and observe its restrictions to then define which properties should be satisfied. However, creating such properties requires a strong mathematical background and translating system requirements to formal properties is not trivial process[13].

1.2 Goals

- Implement a technique to automatically generate test cases based on statechart specifications
- Analyse a set of test cases and extract the events, and sequences of events, that are found in such whole set
- Using the previous information, implement a technique to synthesize properties that can be used in formal verification
- Develop tools to help in these tasks

1.3 Organization

In chapter 2, we present the concepts studied to make this project possible. In chapter 3, we describe a technique implemented to automatically generate test cases from statechart models. In chapter 4, we propose a technique to synthesize formal properties based on the previously obtained test cases. Chapter 5 presents a demonstration of the implemented tools. Our conclusions regarding this project can be found in chapter 6. In chapter 7, subjective aspects of the project that were relevant to the author are presented. Finally, the syntax of linear temporal logic, used in the generated formal properties, can be found in apedix A.

Chapter 2

Concepts

2.1 Statecharts and Automata

A software specification is a reference document which contains the requirements the program should satisfy. It can also be understood as a model of how the system should behave. A specification can be developed with natural language, with user cases for example, or using formal software engineering techniques.

Statecharts are a type of formal software specification based on finite states machines (FSM) specially used in complex systems modelling, such as reactive systems, and were defined in [15]. Similar to an automaton, a statechart has sets of states, transitions and input events that cause change of state. However, a statechart has additional features: orthogonality, hierarchy, broadcasting, guard conditions and history. Due to these additional features, statecharts have been used in industry to specify mission critical systems.

2.1.1 Nondeterministic finite automata

To understand how statecharts can be used to specify systems, we must first have the basic automata background.

Definition[17]: A nondeterministic finite automaton is a quintuple $M = (K, \Sigma, \Delta, s, F)$, where:

- K is the set of states
- Σ is the input alphabet
- Δ is the transition relation, a subset of $K \times (\Sigma \cup e) \times K$, where e is the empty string
- $s \in K$ is the initial state
- $F \subseteq K$ is the set of final states

Each triple $(q, a, p) \in \Delta$ is a transition of M . If M is currently in state q and the next input is a , then M may follow any transition of the form (q, a, p) or (q, e, p) . In the later case, no input is read. A configuration of M is an element of K^* and the relation \vdash (yields one step) between two configurations is defined as: $(q, w) \vdash (q', w') \Leftrightarrow$ there is a $u \in \Sigma \cup e$ such that $w = uw'$ and $(q, u, q') \in \Delta$.

For further information and formalisms regarding finite automata the reader is referred to [17].

A nondeterministic finite automaton example

Find below an example of a nondeterministic finite automaton extracted from [17].

- $K = \{q_0, q_1, q_2\}$ (the set of states)
- $\Sigma = \{a, b\}$ (the alphabet used)
- $\Delta = \{(q_0, a, q_1), (q_1, b, q_2), (q_2, a, q_0), (q_2, e, q_0)\}$ (the transitions)
- $s = q_0$ (the initial state)
- $F = \{q_0\}$ (the set of final states)

The corresponding graphical representation is given in Figure [?].

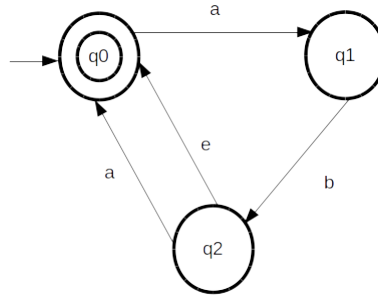


Figure 2.1: A nondeterministic finite automaton that accpets language $L = (ab \cup aba)^*$.

2.1.2 Statechart models

A statechart model can be considered as an extension of finite state machines. The syntax of statechart is defined over the set of states, transitions, events, actions, conditions, expressions and labels [15]. In short terms:

- Transitions are the relations between states
- Events are the input that might cause a transition to happen
- Actions are generally triggered when a transition occurs
- Conditions are boolean verifications added to a transition in order to restrict the occurrence of that transition
- Expressions are composed of variables and algebraic operations over them
- A label is a pair made of an event and an action that is used to name a transition

Furthermore, it is worth to note that statechart models have additional features, described afterwards in this chapter, that do not appear in an ordinary finite state machine. These extra resources are useful, for instance, to model concurrency and different abstraction levels in a more practical way.

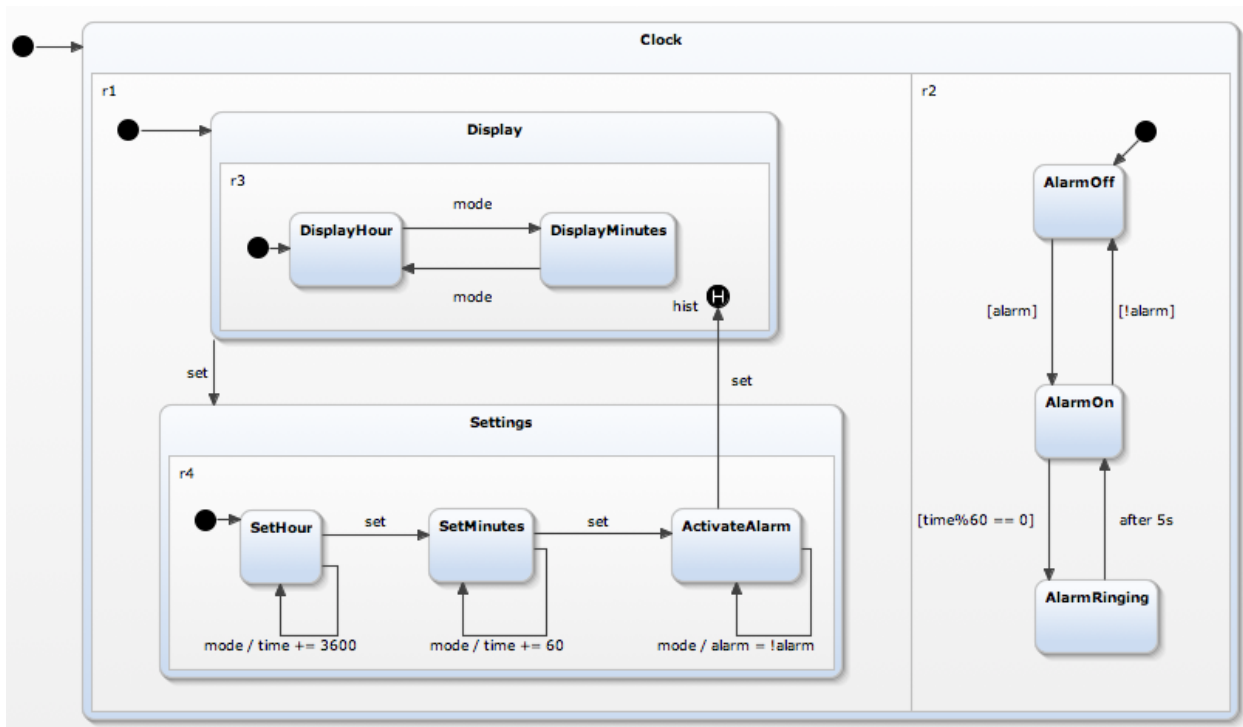


Figure 2.2: Statechart example. A clock model.

Orthogonality

More than one state can be active at the same time in a statechart, this is called orthogonality. It can be used to model concurrent and parallel situations. The set of active states in a certain moment is called configuration. In Figure 2.2, we have parallel regions inside the state *Clock* (*r1* and *r2*) are defined. This means that states *Display* and *AlarmOff* can be active at the same time, depending on the events given.

Hierarchy

It is possible that a state contains other states, called sub-states, and internal transitions, increasing the abstraction and encapsulation level of the model. In Figure 2.2, we have that *Display*, *Settings*, *AlarmOff*, *AlarmOn* and *AlarmRinging* are sub-states of *Clock*. *Display* also contains the sub-states: *DisplayHour*, *DisplayMinutes* and *hist*. And the states *SetHour*, *SetMinutes* and *ActivateAlarm* are inside state *Settings*. We can have as many nested states as required, giving distinct abstraction views of the system.

Guard conditions

In a transition, a guard condition is always verified before the change of states take place. If the condition is satisfied (if its expression returns true), the transition will be engaged. Otherwise, that transition is not allowed to happen. A expression in a guard condition involve variables and operations. In Figure 2.2, the transition from *AlarmOff* to *AlarmOn* is guarded by the condition *[alarm]*, meaning that the transition will only happen if the value of the variable *alarm* is *true*.

Broadcasting

A transition might have an action that is triggered once the transition is performed. An action may cause another transitions to happen, which is called broadcasting. This feature allows that chain reactions occur in a statechart model. Considering Figure 2.2, if we are currently in states *ActivateAlarm* and *AlarmOff* and the *mode* event occurs, we will have the following situation: the transition will be execute and we will stay at state *ActivateAlarm*, the value of variable *alarm* will be changed to its opposite (let's supposed it was *false*, so now it will be *true*) and we will also go to state *AlarmOn* since the condition [*alarm*] guarding the transition between these last two state is satisfied. Therefore, not only the transition starting at *ActivateAlarm* happened when *mode* occurred, but also the transition between *AlarmOff* and *AlarmOn*. The change of the value of variable *alarm* was broadcasted to the whole statechart and a chain reaction happened.

History

A statechart is capable of remembering previously visited states by accessing the history state. Consider figure 2.2, suppose we are in state *DisplayMinutes* and event *set* happened three times in a row. So, we went to *SetHour*, and then *SetMinutes* and now we are at *ActivateAlarm*. If there is another occurrence of *set* event, we will be directed to the history state *hist*. Since the last active sub-state in *Display* was *DisplayMinutes*, we will actually be directed to *DisplayMinutes*.

2.1.3 A statechart and its corresponding automaton

It is possible to obtain an automaton from a statechart by flattening the statechart. The hierarchy and concurrency need to be eliminated. In addition, statechart elements such as guard conditions and actions are not present in an automaton.

The following examples were extracted from [15]. In Figure 2.3 we have a statechart and in Figure 2.4 its flat version that would correspond to an automaton.

To flat a statechart and get the corresponding automaton, we need to pass transitions from the composed states to their sub-states to eliminate hierarchy. To remove orthogonality, we need to do the Cartesian product of the states in each concurrent region. Such operation causes a reasonable number of state and transition explosions in the automaton, if the original statechart model has concurrent states. Besides, in the statechart we can use guard transitions and broadcasting, enriching the model with more information, but both features are absent in the automaton. Note that, in the automaton, the initial state is the product of the initial states from each parallel region in the statechart.

2.2 Tests

Since code has been written, programs have been tested. Testing is one of the most important means of assessing the software quality and it typically consumes from 40% up to 50% of the software development effort [18]. Therefore, it constitute an important area in software engineering.

Testing evolved from an activity related to debugging code to way of checking specification, design as well as implementation[18]. It can be considered a process not only to detect bugs, but also to prevent them [4].

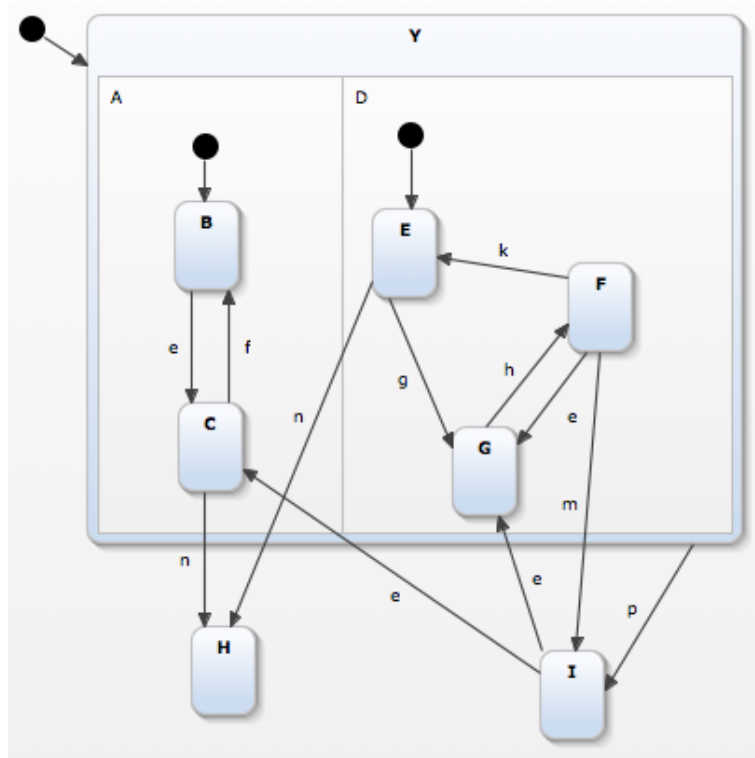


Figure 2.3: *A statechart model with hierarchy and concurrent regions*

2.2.1 Testing goals

In an organization, the goals of testing vary depending on the level of maturity of the team [2]: At a more inexperienced approach, testing could be viewed as the same as debugging code. A further step would be to consider testing as the process to make sure that a software does what it is supposed to do. But, in this case, if a team runs a test suite and every test case passes it, should the team consider that the software is correct or could it be that the test cases were not enough? How does the team decide when to stop?

A different perspective, then, would be to understand testing as the process of finding errors. Therefore a successful test case would be the one that indicated an error in the system [24]. Although discovering unexpected results and behaviours is a valid goal, it might put tester and developers in an adversarial relationship, which certainly damages the team interaction.

Testing shows the presence, but not the absence of errors. Hence, realizing that when executing a software we are under some risk that may have unimportant or great consequences leads to another way to see the intention of the test process: to reduce the risk of using the program, an effort that should be performed by both testers and developers.

Thus, testing can be seen as a mental discipline that helps all professionals in the software industry to increase quality. [2]. The whole software development process could benefit from that thinking: Design and specification would be more clear and precise and the implementation would have fewer errors and would be more easily validated, for example.

2.2.2 The process of testing

Since testing is a time consuming activity, the creation of test cases can be done during each stage of the development process, even though their execution will only be possible after some part of the code is implemented.

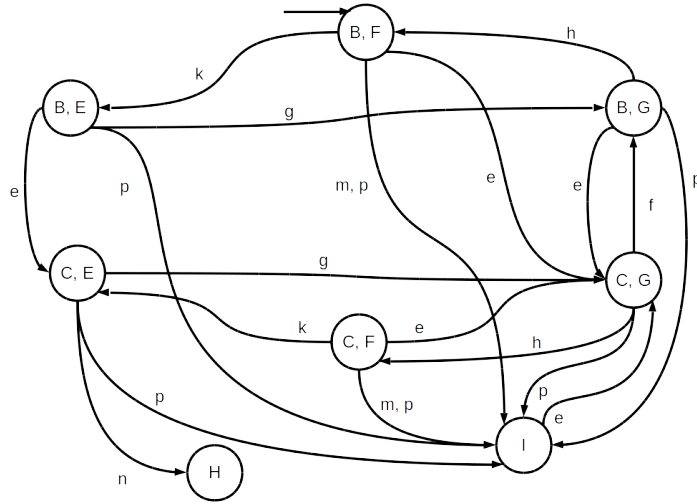


Figure 2.4: *The statechart from 2.3 after flattening to the corresponding automaton*

V-Model

The V-model in figure 2.5 associates each level of testing to a different phase in the development process. This model is typically viewed as an extension of the waterfall methodology, but it does not mandatorily implies the waterfall approach, since the synthesis and anlysis activities generically apply to any development process [2].

- Requirement analysis phase: Customer's needs are registered. **User acceptance tests (UAT)** are constructed to validate that the delivered system meets the customer's requirements.
- System design phase: Technical team analyzed the captured requirements and studied the possibilities to implement them and document a technical specification. **System tests** are designed at this stage assuming that all pieces work individually and checks if the system works as a whole.
- Architectural design phase: Specify interface relationships, dependencies, structure and behaviour of subsystems. **Integration tests** are developed, with the assumption that each module works correctly, in order to verify all interfaces and communication among subsystems.
- Detailed design phase: A low-level design is done to divide the system in smaller pieces, each one of them with the corresponding behaviour specified so that the programmer can code. **Module testing** is designed to check each module individually.
- Implementation phase: Code is actually produced. **Unit tests** are designed to test if every smallest unit of code, such as a method, can work correctly when isolated.

2.2.3 Functional and Structural tests

Testing techniques can be divided in functional and structural categories.

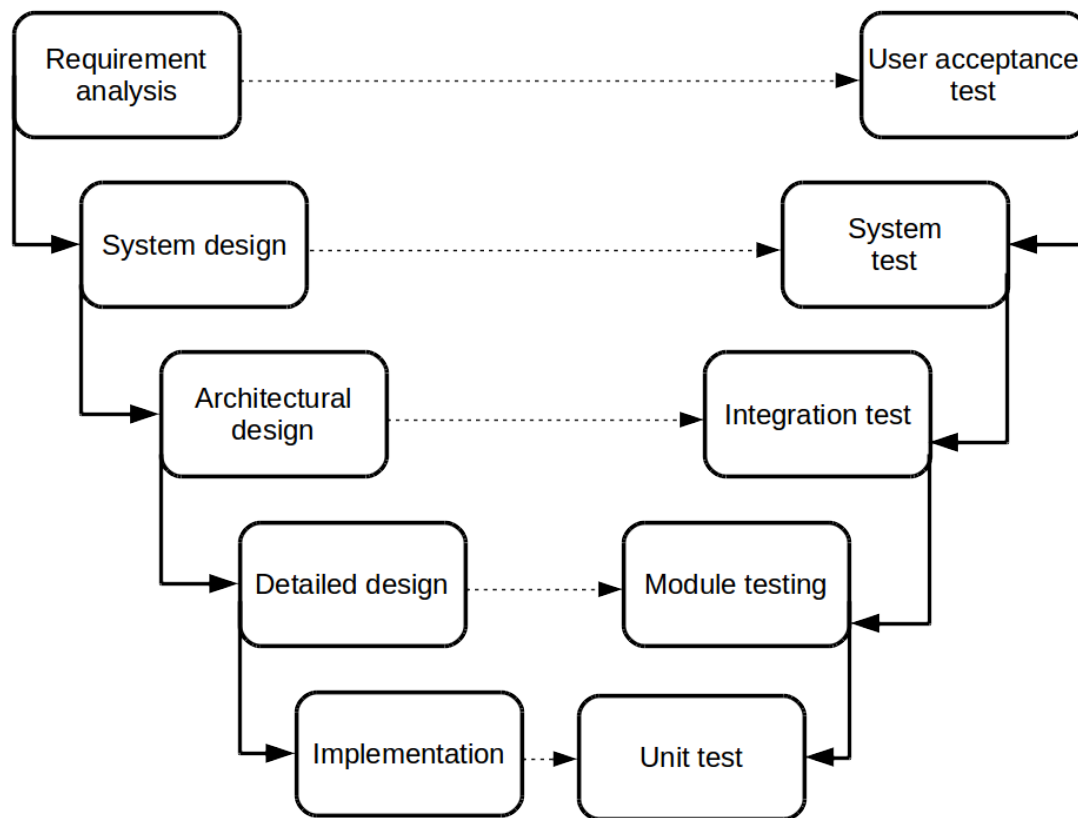


Figure 2.5: *The V-model*

Functional technique (black box)

Functionalities described in the specification are considered to create the test cases. It is necessary to study the behaviour and functionalities presented, develop the corresponding test cases, submit the system to the test cases and analyze the results observed comparing them to what was expected according to the description in the specification. Examples of criteria in this technique are[20]:

- **Equivalence classes partitioning**

Input data are partitioned into valid and invalid classes according to the conditions specified. The test cases are created based on each class by selecting an element in each class as a representative of the whole class. Using this criterion, the test cases can be executed systematically according to the requisites.

- **Analysis of the boundary value**

Similar to the previous criterion, but the selection of the representative is done based on the classes' boundary. For that reason, the conditions associated with the limit values are exercised more strictly.

Since many specifications are written in a descriptive way, the test cases developed using the functional technique can be informal and not specific enough, which makes it difficult

to automate their execution and human intervention becomes necessary. However, this technique only requires that requisites, input and corresponding output are identified. Thus, it can be applied during several testing phases, such as integration and system.

Structural technique (white box)

The structure of the code implementation is considered to create the test cases in this category. In this technique, the program is represented by an oriented graph of flow control, in which each vertex corresponds to a block of code, a statement in the code for instance, and each edge corresponds to a transition between the blocks. The criteria related to this technique are generally concerned with the coverage of the program graph, such as [2]:

- **Node coverage**

Every reachable node in the graph must be exercised by the test set. Node coverage is implemented by many testing tools, most often in the form of statement coverage

- **Edge coverage**

Every edge in the graph must be tested in the test set.

- **Complete path coverage**

All paths in the graph must be tested by the test set. This criterion is infeasible if there are cycles in the graph due to the infinity number of paths.

- **Prime path coverage**

A path is a prime path if it is a simple path and it does not appear as a proper subpath of any other simple path. In this criterion, all prime paths should be tested.

- **Specified Path Coverage**

A specific set S of paths is given and the test set must exercise all paths in S . This situation might occur if the use case scenarios provided by the customer are converted to paths in the graph and the team wishes to test them.

Tests obtained via the structural technique contribute specially to code maintenance and increase the reliability of the implementation. But, they do not represent a way to validate the system against the customer's requirements, since the specified requisites are not considered in their design.

2.3 Test cases

In this section, we will be concerned with test case creation from the functional perspective. The structure of the code will not be analyzed. Instead, the specifications with the requisites are going to be the source to derive the test cases.

2.3.1 Designing test cases

For simple programs, a test case is a pair composed by the software input and the expected output. But, for more complex software, such as web applications or reactive systems, a test case can be a series of consecutive steps or events and their expected consequence.

Given a specification, a test engineer can systematically design test cases: for each functionality defined, use the equivalence class partitioning technique described in 2.2.3 to select the input representatives; enumerate the steps necessary to activate the functionality; and add the expected outputs. Note that requisites to each step can be added as well, specially to guarantee that steps are not performed out of order and that the whole flow will be completely executed and tested.

A good design of test cases is essential to the entire testing activity since it will guide testers through the scenarios they should execute and check. The created test cases will determine which functionalities will be tested and how they will be tested. Besides, the whole test plan tends to be planned around the test cases, considering the amount of cases and their complexity to estimate deadlines and necessary resources. Moreover, managers usually use test cases and their execution rate to give the client feedback about testing progress.

Furthermore, when defects are identified, they can be associated to the test case that caused their detection. Since each test case is associated with a functionality, the team can easily keep track of how many defects each functionality has, what the most problematic scenarios are as well as what the most critical bugs are.

But manually writing test cases is not a simple task. The engineer must read through all the specification, identify functionalities and the possibly many scenarios in which they can be executed. It requires experience and time to project test cases that have more probability to find defects and, therefore, will decrease the risk of using the application. We present in chapter 3 a method to automatically generate test cases for well defined statechart specifications.

To illustrate, one test case example to test a signing in functionality in a website could be the following:

Step	Requisites	Description	Expected result
1	Credentials are valid and user is able to access home page	Access home page	Home page is loaded
2	Step 1 was successful	Click on Sing in link	Sign in page is displayed with the sign in form
3	Step 2 was successful	Check the sign in form	It should contain fields login, password and a button sign in
4	Step 3 was successful	Type the user's email in field login	Login field will hold the data
5	Step 4 was successful	Type the user's password in password field	Password field will hold the data
6	Step 5 was successful	Click on button sign in of the form	Credentials should be successfully validated by the system
7	Step 6 was successful	Check the page that was loaded	It should the user's personal home page

It is also interesting to project test cases for negative scenarios, in which the program should handle an incorrect action done by the user. A test engineer could consider using the following test case to test the negative scenario for the sign in functionality:

Step	Requisites	Description	Expected result
1	Credentials are invalid and user is able to access home page	Access home page	Home page is loaded
2	Step 1 was successful	Click on Sing in link	Sign in page is displayed with the sign in form
3	Step 2 was successful	Check the sign in form	It should contain fields login, password and a button sign in
4	Step 3 was successful	Type the user's email in field login	Login field will hold the data
5	Step 4 was successful	Type the user's password in password field	Password field will hold the data
6	Step 5 was successful	Click on button sign in of the form	Credentials should not be validated and error message should be displayed
7	Step 6 was successful	Check the error message displayed	It should be "Wrong email or password."

Use cases vs Test cases

More detailed use cases provide a series of steps to perform flows related to a determined functionality. Each step might contain the description of system behaviour and user actions. There are also preconditions and an actor is associate with the case. Besides, one use case might describe more than one flow for the functionality by extending the basic flow. Below, we can find an use case example of signing in of a website:

Use case: Website sign in

Actor: Registered user

Preconditions: User has a register in the website and is able to access the home page.

Basic flow:

- 1 System loads the home page of the website, which contains a link to sign in
- 2 The user clicks on the sign in link and is redirected to the sign in form
- 3 The user fills in the sign in form and clicks on button 'sign in'
- 4 The user is able to sign in and their personal home page is displayed

Alternative flows:

a Sign in fails

- 1 System loads the home page of the website, which contains a link to sign in
- 2 The user clicks on the sign in link and is redirected to the sign in form
- 3 The user fills in the sign in form with invalid information and clicks on button 'sign in'
- 4 The user is not able to sign in and system displays error messages

In a test case, however, one step have to specify one single action and its corresponding consequence in the system. We can add preconditions to each step in a test case, even if the precondition is that the previous step was executed successfully. In addition, it is not possible to have more than one flow in one test case, otherwise the tester would have no clear direction during the test run. Therefore, we need to create test cases for each flow.

An use case serves as a guide during the implementation process used by developers. It is a way to understand how the functionality that is being coded is going to be used. An use case will not be executed directly. Test cases are used to direct tester regarding the precise actions that should be performed in each flow and are a way to detect errors. They are directly executed during the test phase.

Use cases are a great source for the creation of test cases because they detail main flows and contain action steps. However, they are different in structure and purpose; therefore, one cannot replace the other.

2.3.2 Automatic generation of test cases

A model, a statechart for instance, can also be used to specify certain scenarios and functionalities relevant to the software. Considering that such model is correct and its interpretation is well defined, one can use it to automatically generate functional test cases [21]. This technique, in which test cases are automatically derived from a model, is called Model Based Test.

Since models are commonly based on finite state machines, the test cases in Model Based Test are often paths in the model. A path corresponds to a sequence of consecutive transitions. There are several ways to explore the model to obtain the paths. Depending on the complexity of the model and the exploration mode chosen, the number of test cases found will be huge. In fact, if one searches for all possible paths, the number of test cases will be infinite if the model contains cycles.

Hence, there are several criteria intended to guide the model exploration and generate the paths as test cases. Some of the them are described below and with further details in [30]:

- **All transitions**

A criterion that can be used to obtain test cases from statechart specifications. It requires that every transition should be exercised at least once during testing.

- **All simple paths**

Another criterion can be used with statecharts. Since all paths is an impossible achievement given the possibility that an infinite number of paths may exist, it is possible to require that every simple path in the model is exercised at least once during testing.

It is important to note that even though this two previous criteria seem similar to the ones described in 2.2.3, they are distinct. The ones described in this section are based on functional requirements and therefore constitute a kind of black-box test. The ones described in 2.2.3 are based on the code implementation and are a type of white-box test.

- **Distinguishing Sequence**

This criterion should be used for finite state machine models. Besides, the finite state machine must be deterministic, complete, strongly connected and minimal.

β - sequence	Transition	β - sequence + SD
A	(0, 3)	A B B
B	(0, 0)	B B B
A A A A A	(1, 4)	A A A A A B B
A A A A B	(1, 2)	A A A A B B B
A A A A	(2, 1)	A A A A B B
A A A B	(2, 3)	A A A B B B
A A	(3, 4)	A A B B
A B	(3, 3)	A B B B
A A A	(4, 2)	A A A B B
A A B	(4, 0)	A A B B B

Table 2.1: *Distinguishing Sequence cases for automaton 2.6. SD = "B B".[30]*

First, a distinguishing sequence, SD, is searched. SD is an input sequence such that when applied to each state in the machine, the output produced is different, making it possible to identify the initial state to which SD was applied. The distinguishing sequence may not exist, in this case the criterion cannot be applied.

Second, for each transition t , an input sequence from the initial state up to and including t is generated. That sequence is called a β - sequence.

We can then concatenate SD to end of each β - sequence to obtain the test cases. When one of these test cases is executed, it will be specifically testing a transition and checking if this transition reached the expected state.

For the automaton in figure 2.6, we have one possible SD = B B. In table 2.1 we have the complete set of β - sequences to demonstrate the Distinguishing Sequence criterion.

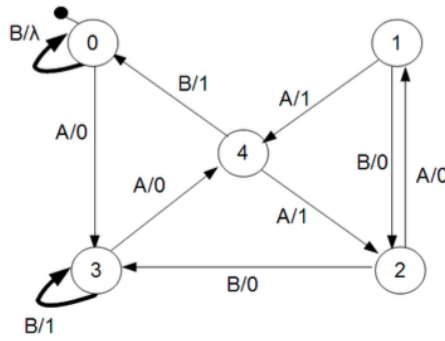


Figure 2.6: *Automaton extracted from [30]*

- **Unique Input-Output**

As mentioned in the previous criterion, the machine might not have a distinguishing sequence. Even in this case, it is still possible to identify each state based not only on the input, but on the output as well.

This criterion should be used for finite state machine models. Besides, the finite state machine must be deterministic, complete, strongly connected and minimal.

State	UES
0	B/ λ
1	A/1 A/1
2	B/0
3	B/1 B/1
4	A/1 A/0

Table 2.2: *UES sequences for automaton 2.6.[30]*

β – sequence	Transition	β – sequence + UES
A	(0, 3)	A B B
B	(0, 0)	B B
A A A A A	(1, 4)	A A A A A A A
A A A A B	(1, 2)	A A A A B B
A A A A	(2, 1)	A A A A A A
A A A B	(2, 3)	A A A B B B
A A	(3, 4)	A A A A
A B	(3, 3)	A B B B
A A A	(4, 2)	A A A B
A A B	(4, 0)	A A B B

Table 2.3: *Unique Input-Output cases for automaton 2.6.[30]*

First, for each state, an unique input-output sequence, UES, is searched. In each state, a breadth search is done and at every step, it is checked if the input and output is unique in comparison to the other states.

Second, a process to find β – sequences is performed in the same way as the previous criterion.

Then, we can check to which state each final transition of the β – sequences leads to by applying the UES sequences and observing their output.

For the automaton in 2.6, table 2.2 shows the possible UES for each state. In table 2.3 we the whole set of β – sequences and their concatenation with the respective UES. Both tables can be used to illustrate the Unique Input-Output criterion.

2.4 Model checking

Formal methods refers to the use of precise logical and mathematical methods to reason about properties of the system [10]. They contribute to the software and hardware engineering fields with formal specification and formal verification techniques, for example, aiming to increase the reliability of the system or hardware. Formal verification techniques, such as model checking, have the goal to verify the correctness, or absence of faults, of some given program code or design against its formal specifications[28].

Model checking is an automated technique that, given a finite-state model of a system and a formal property, systematically checks whether this property holds for that model[3]. Typically, there is a hardware or software system specification containing the requirements,

and we wish to verify that certain properties, such nonexistence of deadlocks are valid for the model of the system. The specification is the basis for what the system should and should not do, therefore it generally is the source for the process of creating properties.

Testing and model checking have the common target of finding bugs. But as stated by Dijkstra, "program testing can at best show the presence of errors but never their absence". On the other hand, if there is a violation of a given specification, it will be found by model checking, which is supposed to be a rigorous method that exhaustively explores all possible behaviours of the system under consideration [25]. This is the main feature that distinguishes testing and model checking: the last one can prove the absence of bugs[14].

According to [3], the model checking process can be divided in the following phases:

1 Modeling phase

Model the system under consideration using the model description language of the model checker and specify the properties to be verified using the property specification language.

2 Running

Run the model checker to check the validity of the properties in the system model.

3 Analysis

In case a property was violated, then one should analyze the counter example generated by the model checker. It might be the case that the property does not truly reflect the requirement and it needs to be specified correctly. One other possible conclusion, if the property was defined appropriately, is that the model actually contains an error and it needs improvement. Then, verification has to be restarted with the improved model. But, in case the model contains no errors and it represents the system design, the violation of a property indicates that the design is incorrect and needs fixes. The verification, then, will be redone with a new model based on an improved design.

Otherwise, if no violation was detected, the model is concluded to possess all desired properties.

We will focus on the definition of properties in the modeling phase, since property specification is one of the goals of this project.

2.4.1 Property definition

The formal properties to be validated are mostly obtained from the system's specification[3]. Hence, a property specifies a certain behaviour of the system that is being considered. One has to manually read through the specification and manually define relevant properties to the system. Identifying which scenarios and behaviours should be considered when specifying properties tends to be a difficult creative process, in which human intervention becomes necessary.

The person responsible to write the formal property specification must have a mathematical background and knowledge of the specification language [13]. Many model checkers such as SPIN and NuSMV accept as input properties written in Linear Temporal Logic (LTL), which is difficult to write, read and validate. Therefore, translating system requirements to formal properties is not easy.

The classic example for property definition is the absence of deadlocks, but properties can also specify safety protocols[22], occurrence and order of events. Let's consider, for example,

that one of the requirements of a system is that always after a call to the method *open*, to open a file, there should be a call to the method *close*, to close the file. One could specify the following LTL property to verify this requirement:

$$\Box(open \rightarrow \Diamond close)$$

The LTL syntax can be found in appendix A.

2.4.2 Specification patterns

The effectiveness of the assurance offered by model checking depends on the quality of the formal properties that were defined [13]. To assist this process, a set of property specification patterns were proposed in [8]. A property specification pattern is a high-level property template that can be adapted based on the requirements to be verified. The patterns were obtained from a survey of 555 specifications collected from 35 different sources, including literature and several projects [9].

A hierarchy was established to facilitate browsing through the patterns and picking the most appropriate one to one's need. The first level of the hierarchy is composed by the occurrence and order categories described in more details in the next subsections.

For each pattern, a scope is required to define an interval of the specification in which the property should hold. The available scopes are: global, before Q , after Q , between Q and R , after Q until R , where Q and R are states or events in the specification.

Occurrence patterns

Occurrence patterns establish the occurrence or absence of a determined event or state in a certain scope of the specification. They can be further classified into:

- Absence: a given state or event does not occur within the scope.
- Existence: a given state or event must occur within the scope
- Bounded existence: a given state or event must occur k times within the scope
- Universality: a given state or event occurs throughout the scope

Order patterns

Order patterns provide descriptions for the order in which events or states occur. They are divided in categories:

- Precedence: a state or event P should always be preceded by a state or event Q within the scope
- Response: a state or event P should always be followed by a state or event Q within the scope
- Chain precedence: a sequence of states or events P_1, \dots, P_n should always be preceded by a sequence of states or events Q_1, \dots, Q_n
- Chain response: a sequence of states or events P_1, \dots, P_n should always be followed by a sequence of states or events Q_1, \dots, Q_n

2.5 Sequential pattern mining

Sequential pattern mining is a topic in data mining that discovers frequent subsequences as patterns in a sequence database [19]. Each sequence in a sequence database is called data-sequence and contains typically an ID and transactions ordered generally by time, where each transaction is a set of items.

The problem is to find all sequential patterns with a user specified minimum support, where the support of a sequential pattern is the percentage of data-sequences that contain the pattern [27]. In other words, if a user inputs a percentage p and a sequence database D is given, then the mining will return the set of patterns that are present in at least $p\%$ of the data-sequences. The formal definition can be found in [19] and in [26].

There are several applications for the problem, such as analysis of customer behaviour, purchase patterns in a store and study of DNA sequences. In section 2.5.1, a practical example is described based on [19].

Notation

A data-sequence S that has ID T and $n \geq 1$ ordered transactions t_1, t_2, \dots, t_n is denoted by $S = [T < t_1, t_2, \dots, t_n >]$.

Each transaction t_i that is a set of $m \geq 1$ items l_{i_1}, \dots, l_{i_m} and is denoted by $t_i = (l_{i_1}, \dots, l_{i_m})$. Thus, $S = [T < (l_{1_1}, \dots, l_{1_m}), \dots, (l_{n_1}, \dots, l_{n_m}) >]$.

To simplify the notation in the case in which each transaction contains only one item, we can avoid the parenthesis, for example: if $t_i = (l_i)$ for all $1 \leq i \leq n$ then it is possible to write $S = [T < l_1 l_2 \dots l_n >]$.

2.5.1 An example: Web usage mining

Web usage mining, also known as web log mining, is an important application of sequential pattern mining. It is concerned with finding frequent patterns related to user navigation from the information presented in web system's log. Considering that a user is able to access only one page at a time, the data-sequences would only have transitions with a single event each.

In a ecommerce application, for instance, we can have the set of items $I = a, b, c, d, e, f$ representing products that can be purchased. The occurrence of one of these items in a transaction means that a user accessed the page of such item.

Suppose the sequence database contains the following data-sequences extracted from the log: $[T1 < abdac >]$, $[T2 < eaebcac >]$, $[T3 < babfaec >]$ and $[T4 < abfac >]$. In this case, the analysis of the first transaction allows us to conclude that user $T1$ accessed the pages of products a, b, d, a and c in this order. By applying the web usage mining technique with support of 90%, a manager would notice that $abac$ is a frequent pattern, indicating that 90% of the users who visit product a then visit b , then return to a and later visit c . Hence, an offer could be placed in product a , which is visited many times in sequence, to increase the sales of other products.

2.5.2 Sequential pattern mining algorithms

There are several algorithms to perform the sequential pattern mining, but they generally differ in two aspects[19]:

- The way in which candidate sequences are generated and stored. The goal is to reduce the amount of candidates created as well as decrease I/O costs.

- The way in which support is counted and how candidate sequences are tested for frequency. The goal is to eliminate data structures used for support or counting purposes only.

Considering these topics, algorithms for sequential pattern mining can be divided in two categories: apriori-based, pattern-growth

Apriori-based algorithms

These algorithms mainly rely on the property that states that if a sequence s is infrequent, then any other sequence that contains s is also infrequent. An example for the category would be the *GSP*[27] algorithm.

Apriori-based algorithms use the *generate-and-test* method to obtain the candidate patterns: the pattern is grown one item at a time and tested against the minimum support. By taking this approach, they have to maintain the support count for each candidate and test it at each iteration of the algorithm.

Generally, algorithms in this category generate an explosive number of candidate sequences, consuming a lot of memory. *GSP*, for instance, generates a combinatorially explosive number of candidates when mining long sequential patterns. That may be the case of a DNA analysis application, in which many patterns are long.

In addition, since they need to check at each iteration for the support count, multiple scans of the database are performed, which requires a lot of processing time and I/O cost. In general, to find a sequential pattern of length l , the apriori-based method must scan the database at least l times, which leads to great cost when long patterns exist.

Pattern-growth algorithms

Pattern-growth algorithms try to use a certain data structure to prune candidates early in the mining process. Besides, the search space is partitioned for efficient memory management. *PrefixSpan*[26] is an example of such algorithm and, since it was used in our implementations, we will provide more information about it in this subsection.

The general idea behind *PrefixSpan* is as follows: Instead of repeatedly scanning the entire database, generating and testing large sets of candidate sequences, one can recursively project a sequence database into a set of smaller databases associated with the set of patterns mined so far and, then, mine locally frequent patterns in each projected database[26].

To reduce the projections size and the number of access, *PrefixSpan*, sorts the items inside each transaction and creates the projected databases based on patterns' prefixes. In order to do so, the algorithm assumes that the order of items in a transaction is irrelevant, and only the order of the whole transactions matters to the problem.

Differently than apriori-based algorithms, *PrefixSpan* does not generate or test candidate sequences. Patterns are grown from the shorter ones. Besides, projected databases keep shrinking, which is relevant in practice because usually, only a small set of sequential patterns grow long in a database and, thus, the number of sequences in a projected database usually reduces when prefix grows.

In the worst case, *PrefixSpan* constructs a projected database for every sequential pattern. With the intention to reduce the number of projected databases and improve the performance of the algorithm, [26] proposes a technique called *pseudo partition* that may reduce the number and size of projected databases.

Chapter 3

Test case generation for statecharts

In this project, we implemented the test case generation for statecharts based on the criteria described in [5]. We test all transitions by visiting every state and triggering events for every transition that starts in it.

In the next sections of this chapter we describe the techniques and algorithms used for automatically create test cases for statecharts. The Java code can be found in our Github repository¹. Our implementation receives as input a statechart created using Yakindu Statechart Tools [1] and outputs the test cases.

3.1 Test cases for simple statecharts

For this section, we consider only statecharts that do not contain hierarchy and concurrency. Statecharts with hierarchy and concurrency will be explained later.

We start by making sure every reachable state in the statechart is covered. In order to do so, for each state s in the statechart, we construct a path p from the initial state to s . The path p is said to be the *coverage path of s* . All coverage paths generated are stored in a set called *State Cover*, which is denoted by C . Therefore, C is a set of sequence of transition labels, such that we can find an element from this set to reach any desired state starting from the initial one [5].

Since there is no hierarchy or concurrency in the statechart considered at this point, the construction of C is similar to covering states of an automaton and it can be done through a depth search in the states. Find below a pseudocode for the method:

```
//Wrap method to construct the State Cover  
//It receives as argument the initial state of a simple statechart  
Set constructSetC(State initialState) {  
  
    Set setC = new Set();  
  
    Path emptyPath = "";  
  
    List visited = new List();  
  
    return constructSetCRec(initialState, emptyPath, setC);  
}
```

```
//Recursive function that will do all the work  
//returns the State Cover set, or set C
```

¹<https://github.com/rafaelgregorut/StatechartTests>

```

Set constructSetCRec(State s, Path p, Set setC, List visited) {
    visited.add(s);
    setC.add(s,p);
    for (Transition t in s.getOutgoingTransitions()) {
        State nextState = t.getDestiny();
        if (!visited.contains(nextState)) {
            Path nextCoveragePath = p + t.getLabel();
            constructSetCRec(nextState, nextCoveragePath, setC, visited);
        }
    }
    return setC;
}

```

Now that we have the coverage for every reachable state, we need to trigger each transition on each state and create the test cases. For each transition there will be a test case, thus every transition in the diagram will be exercised at least once during testing.

Consider the transition $t = (s, l, q)$, where s is the original state, l is the event label that triggers t and q is the destiny state. Previously, we computed that s has coverage path p such that $p \in C$ and p is a sequence of labels. The test case TC to t would be the concatenation of the event label l to the end of p expecting to get to state q . The process is repeated to every transition in the statechart. Find below the algorithm for the test case creation:

```

//Function that prints the test cases for all transitions in a statechart
void generateTestCases(Statechart sc, Set setC) {
    for (State s in sc.getStates()) {
        print("At state "+s.getName());
        Path coveragePath = setC.getCoveragePath(s);
        for (Transition t in s.getOutgoingTransitions()) {
            print("Test case for "+t.getLabel());
            Path testPath = coveragePath + t.getLabel();
            print("Test path: "+testPath);
            State expectedState = t.getDestiny();
            print("Expected state: "+expectedState);
        }
    }
}

```

Consider the following example in figure 3.1 where we model the verifications in a purchase flow of a telco ecommerce. The flow is done by a user who wishes to change their cellphone plan. They will be able to change plan if they are not employees from the telco company and are not committed to a loyalty contract. Besides, they must perform the login

so that the system is able to retrieve their information.

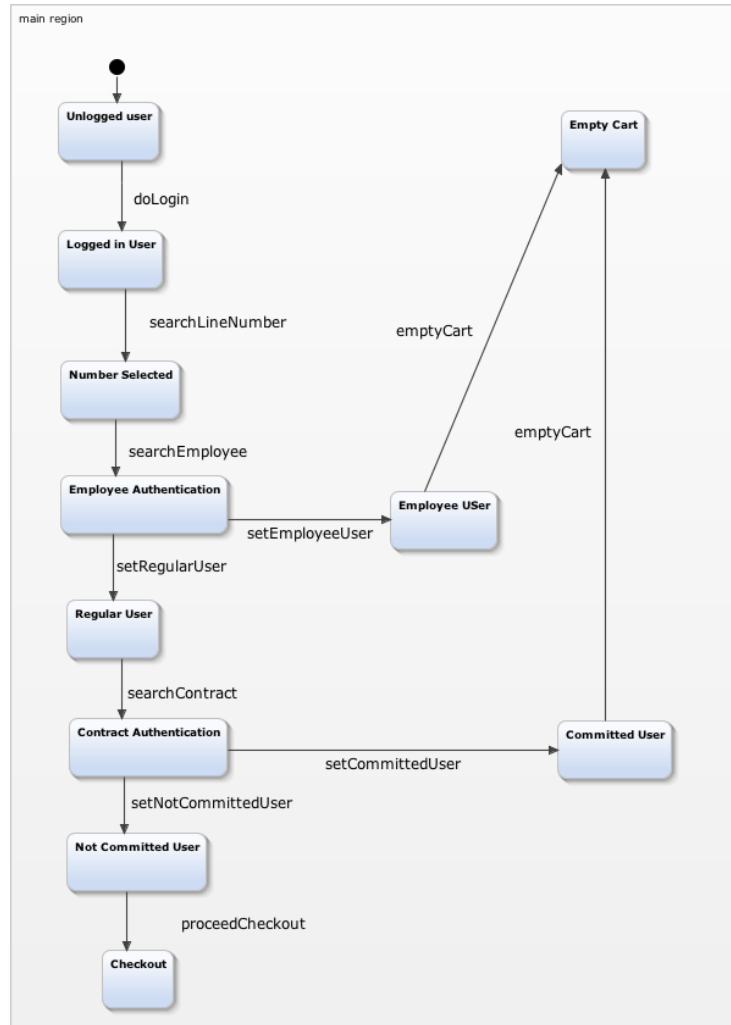


Figure 3.1: A statechart for the plan change flow in a telco ecommerce

Hierarchy and orthogonality are not found in statechart 3.1. Hence, we can apply the technique just presented.

The construction of the *State Cover* set, or C , goes as follows:

We start at the initial state *Unlogged User*. Since it is the initial state, its coverage path is the empty string, denoted by e .

Next, we recursively visit the states that can be reached from *Unlogged User*. In our example, we get to state *Logged in User*. In order to get to this state, it was necessary to go through transition *doLogin*. Therefore, the coverage path for *Logged in User* is $e \text{ doLogin}$. Note that we concatenate the coverage path of the previous state to the transition taken.

When the construction of C is concluded, we have the following coverage paths:

State	Coverage path
Unlogged user	<i>e</i>
Logged in User	<i>e doLogin</i>
Number Selected	<i>e doLogin searchLineNumber</i>
Employee Authentication	<i>e doLogin searchLineNumber searchEmployee</i>
Employee User	<i>e doLogin searchLineNumber searchEmployee setEmployeeUser</i>
Empty Cart	<i>e doLogin searchLineNumber searchEmployee setEmployeeUser emptyCart</i>
Regular User	<i>doLogin searchLineNumber searchEmployee setRegularUser</i>
Contract Authentication	<i>doLogin searchLineNumber searchEmployee setRegularUser searchContract</i>
Committed User	<i>doLogin searchLineNumber searchEmployee setRegularUser searchContract setCommittedUser</i>
Not Committed User	<i>doLogin searchLineNumber searchEmployee setRegularUser searchContract setNotCommittedUser</i>
Checkout	<i>doLogin searchLineNumber searchEmployee setRegularUser searchContract setNotCommittedUser proceedCheckout</i>

Notice the empty string *e* is present due to the unlabeled transition leaving the initial state.

Then, we have to create a test case for every transition leaving each state. Let's take state *Employee Authentication* for example. It has two leaving transitions: *setEmployeeUser* and *setRegularUser*. To guarantee they are exercised at least once during testing and that they go to their appropriate destiny states, we need the following test cases:

- Test case for *setEmployeeUser*

Path: *e doLogin searchLineNumber searchEmployee setEmployeeUser*

Expected state: *Employee User*

- Test case for *setRegularUser*

Path: *e doLogin searchLineNumber searchEmployee setRegularUser*

Expected state: *Regular User*

Note that the *path to test* is the coverage path concatenated with the label of the tested transition. The analogous is done for all other transitions in the model.

3.2 Test cases for complex statecharts: hierarchy

In this section, the test cases generated will be obtained from statecharts that have hierarchy: a state may contain many substates and so on. We do not limit the level of nested hierarchy for the automatic generation. Consider states *a* and *b*, such that *a* contains *b*. We will say that *a* is the superstate of *b* and *b* is the substate of *a*.

One way to deal with hierarchy is to eliminate it from the model by flattening the statechart as shown in 2.1.3. The statechart would become an automaton and the techniques for simple statecharts explained in the previous section could be used to generate test cases.

But, the approach taken in this project, as in [5], was to keep the structure of the statechart and create the test cases incrementally.

Similarly to the previous simpler case, for statecharts with hierarchy we still need to cover all states by constructing the set C and then test all transitions in the model. The construction of C , however, needs to take into consideration substates in order to cover them as well. It is important to note that we considered only statecharts that do not have transitions between different hierarchy levels.

When we get to a state, we should check if it contains substates. If it does, we can compute substates' coverage paths going deeper in the hierarchy level. Later, we concatenate the coverage path of the superstate to the beginning of each coverage path of the substates. Then, the coverage path of the super state should be removed from C and the paths to the substates will be kept in C instead. Besides, consider the case in which the coverage path p of a certain state s passes through a superstate q . We need to mark in p that it passed by q and that the coverage paths of q 's substates should be used when creating test cases for transitions leaving s . To mark that, we will use the notation Δ_q

The algorithm to construct C needs some changes. Regarding the pseudocode presented in the previous section, we need to modify the recursive function:

```
//Recursive function that will do all the work
//returns the State Cover set, or set C
Set constructSetCRec(State s, Path p, Set setC, List visited) {

    visited.add(s);

    setC.add(s,p);

    if (s.containsSubstates()) {

        Set subSetC = constructSetC(s.getInitialSubstate());

        s.addSubpaths(subSetC);

        setC.remove(s,p);

        for (State substate in s.getSubstates()) {

            Path partialPath = subSetC.getPath(substate);

            Path substateCoveragePath = p + partialPath;

            setC.add(substate,substateCoveragePath)
        }

    }

    for (Transition t in s.getOutGoingTransitions()) {

        State nextState = t.getDestiny();

        if (!visited.contains(nextState)) {

            if (s.containsSubstates()) {

                Path nextCoveragePath = p +  $\Delta_s$  + t.getLabel();

            } else {
```

```

        Path nextCoveragePath = p + t.getLabel();
    }

    constructSetCRec(nextState, nextCoveragePath, setC, visited);
}
}

return setC;
}

```

After the set C is complete, we need to in fact create the test cases based on every transition that leaves each state. In states that do not have substates and whose coverage did not pass by any superstate, the process is the one presented in the previous section.

In case, the state's coverage path p went through a superstate, we need to expand the path with the coverage paths of the substates. In other words, for each substate with coverage path u , there will be a p' with u replacing the notation Δ_s , where s is the superstate. Follows the algorithm for the expansion:

```

//Pseudocode for an expansion function
//Receives the original, not expanded, path and the super state it passes through
//Returns the set of paths resulting from the expansion
Set pathExpansion(Path originalPath, State super) {

    Set subPaths = super.getSubpaths();

    Set expansionResults = new Set();

    for (State substate in super.getSubstates()) {

        Path subPath = subPaths.getPath(substate);

        Path expanded = originalPath.replace( $\Delta_{super}$ , subPath);

        expansionResults.add(expanded);
    }

    return expansionResults;
}

```

If a state contains substates, however, we must transfer the origin of every transition that leaves it to each one of its substates. Consider the case that a state s has a transition $t = (s, l, q)$ and contains substates s_1, s_2 and s_3 . When creating the test case to t , we will actually consider three new transitions: $t_1 = (s_1, l, q)$, $t_2 = (s_2, l, q)$ and $t_3 = (s_3, l, q)$. The pseudocode to transfer transitions from the superstate to the substates is presented below:

```

//Add all transitions of a superstate in its substates
//Receives the superstate as argument
void transferFromSuperToSub(State super) {

    for (Transition t in super.getOutgoingTransitions()) {

        for (State sub in super.getSubstates()) {

            sub.addOutgoingTransition(t);
        }
    }
}

```



```

    }
}

```

The pseudocode to create the final test cases is the same as the one presented in 3.1.

Let's take as an example the statechart in figure 3.2. It models an application that receives order files in a specific format and converts them into an well formatted xml. Each order file contains several lines, and each line contains a product and its quantity. The application also has an integration layer, that will receive the xml file and then send it to the management system.

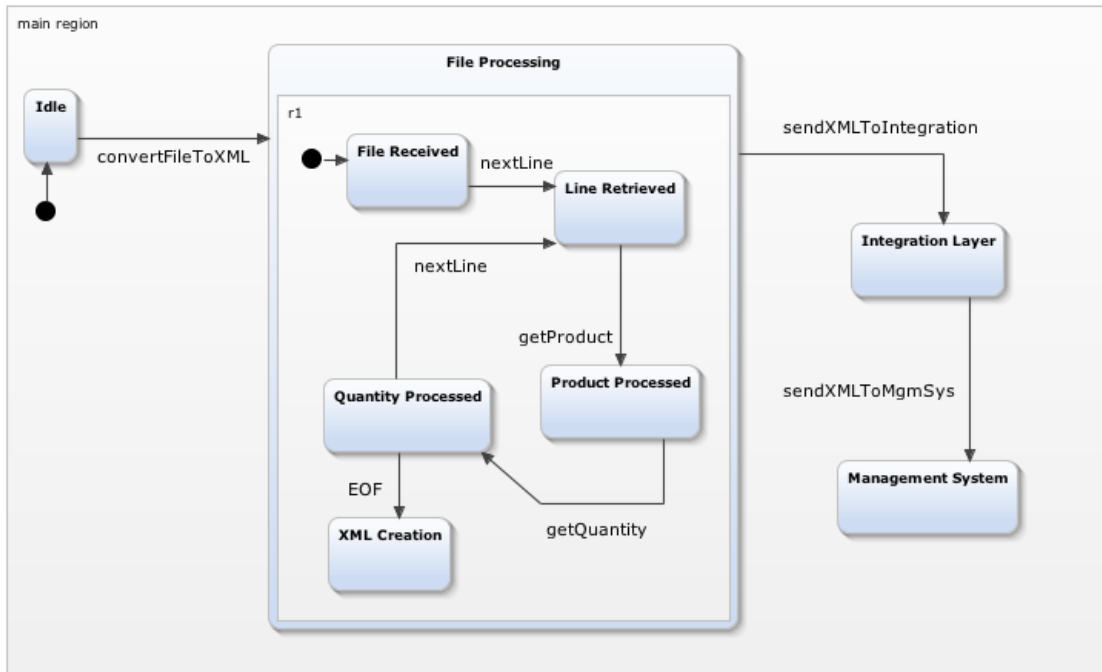


Figure 3.2: Statechart for order file processing and transference

In figure 3.2, to construct the coverage path to *Idle* we do not need to worry about hierarchy, so approach described in the prior section (3.1) is enough.

State	Coverage path
Idle	e

Notice that once again the empty string *e* is present due to the unlabeled transition leaving the initial state.

When creating the coverage path for state *File processing*, we notice that it actually is a superstate. So, we go deeper in the hierarchy level to obtain the coverage paths of substates *File received*, *Line retrieved*, *Product processed*, *Quantity processed* and *XML creation*. We should add the following paths to set *C*:

State	Coverage path
File received	e convertFileToXML e
Line retrieved	e convertFileToXML e nextLine
Product processed	e convertFileToXML e nextLine getProduct
Quantity processed	e convertFileToXML e nextLine getProduct getQuantity
XML creation	e convertFileToXML e nextLine getProduct getQuantity EOF

Notice that the coverage path for the superstate *File processing* is removed from C , because we are considering its substates only. Therefore, the testcases that would be created based on the superstate will be created based on the substates. Besides, the empty string e is added twice in each of these paths. That is because they pass through two initial states with unlabeled transitions: the first one is the general initial state, and the second one is the initial state inside *File processing*.

We still need to cover states *Integration layer* and *Management system*. Observe that to cover these states, we need to pass by *File processing*, a superstate. Therefore, in their coverage path, we need to use a specific notation to guide the later expansion with the substates coverage paths. We will use the notation $\Delta_{File\ processing}$ to indicate that, when creating the test cases, we need to expand the path considering the coverage of *File processing*'s substates. Thus, the coverage paths for *Integration layer* and *Management system* are the following:

State	Coverage path
Integration layer	$e\ convertFileToXML\ \Delta_{File\ processing}\ sendXMLToIntegration$
Management system	$e\ convertFileToXML\ \Delta_{File\ processing}\ sendXMLToIntegration\ sendXMLToMgmSys$

At this point, that we have the complete set C , we start to in fact create the test cases. As an example, we will examine transitions *getProduct*, *sendXMLToIntegration* and *sendXMLToMgmSys* more closely.

For *getProduct*, a transition leaving a substate, the process will be the same one presented in the previous section (3.1). Hence, we have the following test case:

- Test case for ***getProduct***

Path: $e\ convertFileToXML\ e\ nextLine\ getProduct$

Expected state: *Product processed*

In the case of *sendXMLToIntegration*, a transition that leaves a superstate, we need to use the information regarding the substates to create the test cases. For each substate's coverage path p , there will be a test case for *sendXMLToIntegration* making usage of p . We should append *sendXMLToIntegration* to each p in order to obtain the test paths:

- Test case #1 for ***sendXMLToIntegration***

Path: $e\ convertFileToXML\ e\ sendXMLToIntegration$

Expected state: *Integration layer*

- Test case #2 for ***sendXMLToIntegration***

Path: $e\ convertFileToXML\ e\ nextLine\ sendXMLToIntegration$

Expected state: *Integration layer*

- Test case #3 for ***sendXMLToIntegration***

Path: $e\ convertFileToXML\ e\ nextLine\ getProduct\ sendXMLToIntegration$

Expected state: *Integration layer*

- Test case #4 for ***sendXMLToIntegration***
 Path: *e convertFileToXML e nextLine getProduct getQuantity sendXMLToIntegration*
 Expected state: *Integration layer*
- Test case #5 for ***sendXMLToIntegration***
 Path: *e convertFileToXML e nextLine getProduct getQuantity EOF sendXMLToIntegration*
 Expected state: *Integration layer*

As for transition *sendXMLToMgmSys*, the expansion of *Integration layer*'s coverage path is pending. Similarly to what we did for the transition *sendXMLToIntegration*, we also need to consider the paths of *File processing*'s substates. Therefore, the test cases for *sendXMLToMgmSys* are:

- Test case #1 for ***sendXMLToMgmSys***
 Path: *e convertFileToXML e sendXMLToIntegration sendXMLToMgmSys*
 Expected state: *Integration layer*
- Test case #2 for ***sendXMLToMgmSys***
 Path: *e convertFileToXML e nextLine sendXMLToIntegration sendXMLToMgmSys sendXMLToMgmSys*
 Expected state: *Integration layer*
- Test case #3 for ***sendXMLToMgmSys***
 Path: *e convertFileToXML e nextLine getProduct sendXMLToIntegration sendXMLToMgmSys*
 Expected state: *Integration layer*
- Test case #4 for ***sendXMLToMgmSys***
 Path: *e convertFileToXML e nextLine getProduct getQuantity sendXMLToIntegration sendXMLToMgmSys*
 Expected state: *Integration layer*
- Test case #5 for ***sendXMLToMgmSys***
 Path: *e convertFileToXML e nextLine getProduct getQuantity EOF sendXMLToIntegration sendXMLToMgmSys*
 Expected state: *Integration layer*

Notice that, in each case, $\Delta_{File\ processing}$ in *Integration layer*'s coverage was replaced by a substate's coverage path.

3.3 Test cases for complex statecharts: orthogonality

Now we shall consider statecharts that possess orthogonality, in other words, states in concurrent regions.

One first method to generate test cases dealing with orthogonality is to eliminate it by flattening the statechart as explained in 2.1.3. The elimination of orthogonality would be done with the cartesian product of all states and transitions causing an explosion in the number of result states and transitions [5].

To avoid state and transition explosion and still be able to cover all states and test all transitions, [5] offers the alternative to refine the concurrency requisites. In this project, we chose the strong concurrency refinement:

- **Strong concurrency**

This refinement allows us to test concurrent components separately. Transitions from each concurrent region are triggered one-by-one in different steps. In this case, we consider that the concurrent regions are placed in units which either run in parallel or in different processors. So no concurrent region may cause missing transitions in another one or misdirected transitions.

The communication resources, as broadcasting, should be disabled during testing since it could cause series of transitions to occur. A chain reaction would be an example of such series and would not be expected by the test cases listed using this implementation.

We first compute the coverage paths for each concurrent region separately. Then, similarly to the case with hierarchy, we combine these paths with the coverage path of the state that contains the concurrent regions. After obtaining the coverage path for all states, set C is complete.

In the next pseudocode, we consider that substates are in a region inside the superstate. To apply the next pseudocode for statecharts with hierarchy discussed in the previous section (3.2) we should consider that the superstate have only one internal region, which will contain the substates. In statecharts with concurrency, the concurrent elements would be in different regions inside a superstate; figure 3.3 serves as an example. Therefore, the solution can be applied to states with orthogonality as well as to ones with hierarchy only. Find below the pseudocode for the construction of the *State Cover* set, set C :

```
//Recursive function that will do all the work
//returns the State Cover set, or set C
Set constructSetCRec(State s, Path p, Set setC, List visited) {

    visited.add(s);

    setC.add(s,p);

    if (s.containsSubRegions()) {

        for (Region r in s.getSubregions()) {

            Set subSetC = constructSetC(r.getInitialSubstate());

            r.addSubpaths(subSetC);

            setC.remove(s,p);

            for (State substate in s.getSubstates()) {
```

```

    Path partialPath = subSetC.getPath(substate);

    Path substateCoveragePath = p + partialPath;

    setC.add(substate, substateCoveragePath)
  }
}

for (Transition t in s.getOutgoingTransitions()) {

  State nextState = t.getDestiny();

  if (!visited.contains(nextState)) {

    if (s.containsSubstates()) {

      Path nextCoveragePath = p +  $\Delta_s$  + t.getLabel();

    } else {

      Path nextCoveragePath = p + t.getLabel();

    }

    constructSetCRec(nextState, nextCoveragePath, setC, visited);
  }
}

return setC;
}

```

Next, we again need to generate the test cases for each transition of the model. This process is the same as the one described in the case with hierarchy since concurrent states are inside a super state. The algorithm for this phase can be found in pseudocode format in 3.1.

To illustrate this case, we can look at the example provided in figure 3.3. It models an online store in which the administrator is allowed to execute two jobs: one to clear all reservations in every product (region *Clear reservations job* in the figure) and one to send emails to customers letting them know certain products are back in stock (region *Email job* in the figure). Both jobs can run in parallel if the administrator wishes, thus their regions are modeled in a concurrent way in the statechart.

According to our refinement, the coverage paths will be created for substates in *Clear reservations job* and *Email job* separately. First, the substates in *Clear reservations job* concurrent region are inside state *Stock update email*, hence we need to apply the algorithm presented in 3.3. Note that since the coverage path to *Stock update email* is just the empty string e , it will not have a great impact in the substates paths. Let's consider *Product retrieved* and *Stock level updated* to exemplify the results:

State	Coverage path
Product retrieved	$e \ e \ \text{startClearJob} \ \text{retrieveReservedProducts} \ \text{nextProduct}$
Stock level updated	$e \ e \ \text{startClearJob} \ \text{retrieveReservedProducts} \ \text{nextProduct} \ \text{updateStockLevel}$

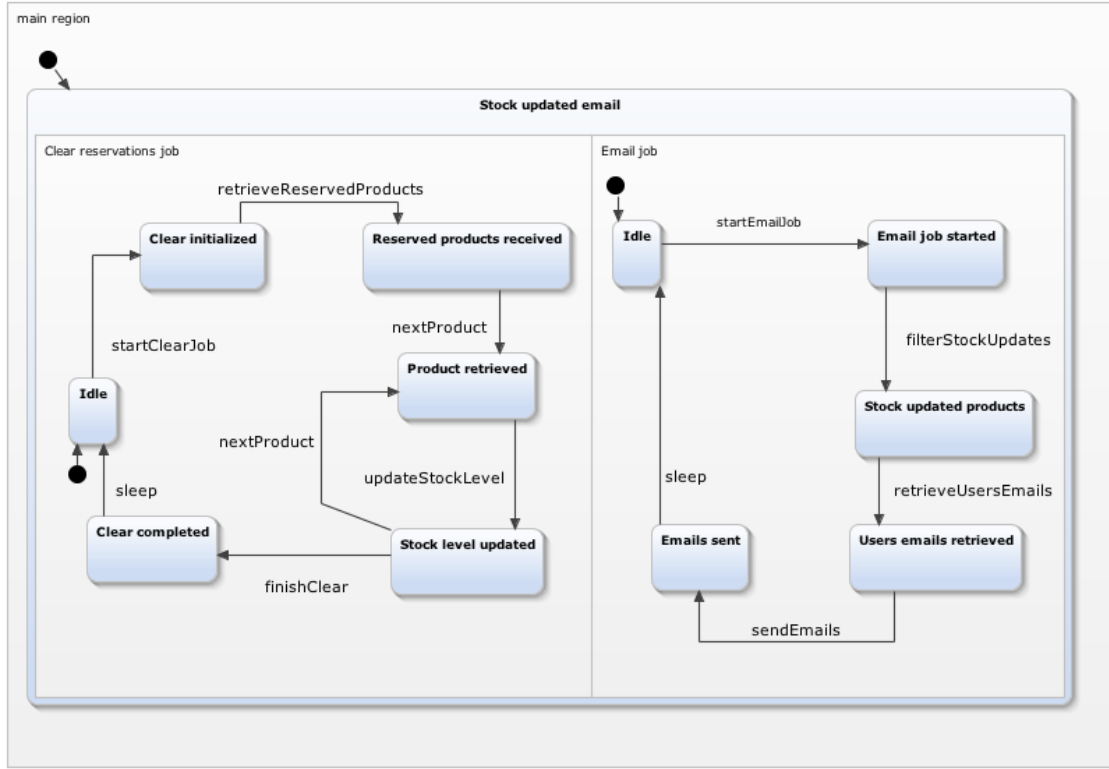


Figure 3.3: Statechart model for concurrent jobs: clear reservations job and send email job

Secondly, we can construct the coverage paths for substates in region *Email job* by an analogous process. For instance, the coverage path for *Email sent* would be:

State	Coverage path
Email sent	e e startEmailJob filterStockUpdates retrieveUsersEmails sendEmails

The generation of the test cases, then, is similar to the one presented in the previous section (3.2). But, when there is orthogonality, we need to consider the coverage paths of substates from all concurrent regions in a state during the expansion phase. For the example in 3.3, because we do not have a state after *Stock updated email*, no expansion of coverage paths will be needed.

To demonstrate, we will consider the test cases for transitions *nextProduct*, from state *Stock level updated*, and *sleep*, from state *Email sent*. We need to concatenate these transition labels to the end of the coverage path of their origin state. Therefore, *nextProduct* will be appended to the coverage path of *Stock level updated* and *sleep*, to the coverage path of *Email sent*:

- Test case for *nextProduct*

Path: e e startClearJob retrieveReservedProducts nextProduct updateStockLevel nextProduct

Expected state: *Product retrieved*

- Test case for *sleep*

Path: e e startEmailJob filterStockUpdates retrieveUsersEmails sendEmails sleep

Expected state: *Idle*

Chapter 4

Formal property extraction from test cases

In this chapter we propose a technique to automatically specify formal properties from the test cases generated in the previous chapter. Since a great number of test cases might be generated, we should be able to identify the more relevant patterns among them. In order to do so, we will apply the mining concepts and *PrefixSpan* algorithm presented in section 2.5 to extract patterns, which will later be used to derive the properties.

In the next sections of this chapter we describe the techniques and algorithms used to automatically specify formal properties in LTL, linear temporal logic. Our Java implementation can be found in our Github repository¹.

4.1 Test case mining

In chapter 3, we presented a method to automatically generate test cases from statecharts specifications. Each test case presented consisted of a transition t we wished to test, a test path p to activate the transition and a state s which was the expected state t would redirect us to.

The test path p is basically a sequence of consecutive events. Therefore, it is possible to analyze it with the concepts and notation shown in section 2.5. Let's say that p is composed by events e_1, e_2, \dots, e_n in this order. We can associate a sequence s_p to the test path p such that $s_p = [T_p < e_1 e_2 \dots e_n >]$, where T_p is an arbitrary unique ID.

The set of test cases automatically generated from the statechart, can then be seen as sequence database. Hence, we are able to apply sequential pattern mining algorithms, such as *PrefixSpan* (2.5), to acquire the most frequent patterns in the set of test cases. The user defines the minimum support for the mining algorithm and then obtains the most frequent subsequences in the set of test paths.

The most frequent subsequences returned by the mining play an important role during testing, due to the fact that they are the ones that are stressed the most. If a subsequence $< abc >$ is considered a frequent pattern with minimum support of 60%, it means that events a, b and c will be executed in this order at least 60% of the time during the test activity.

Furthermore, mining also indicates which event subsequences most test cases rely on, so a defect in any of them would block a considerable amount of test case execution. Consider the previous example with pattern $< abc >$ and 60% of minimum support. If there is bug in the system that damages the execution of events a, b and c in this order, then it implies that

¹<https://github.com/rafaelgregorut/ExtractingStateSequences>

at least 60% of the test case execution would be harmed as well, impacting on the system delivery to the client.

In conclusion, the advantage of using a mining technique is that, besides reducing the amount of sequences to be analyzed, it provides subsequences that are more relevant to the testing process. Since these subsequence patterns are important to testing, they should also be important to the system execution as a whole.

4.1.1 The SPMF framework

In our implementaion, we used the *Sequential Pattern Mining Framework (SPMF)*[12] to perform the test case mining. *SPMF* is an open-source data mining library written in Java, specilized in sequential mining. It was easily integrated with our Java code, even though it can be used as a standalone application.

It offers several mining algorithms implementations, not only for sequential mining, but for association rule and clustering classification, for instance, as well. For the purpouse of this project, we chose the provided *PrefixSpan* algorithm due to the empirical analysis presented in [26] demonstrating that it would be more efficient than other classic sequential pattern mining algorithms such as *GSP*.

The algorithm receives as input the sequence database and the minimum support value as the user wishes. It then computes the most frequent subsequence patterns, which are internally stored and used during the creation of the formal properties. Note that our implementaion does not output the discovered patterns, since this is not the final goal of the project. We use the subsequence patterns returned by *SPMF* for the property generation.

4.1.2 Test case mining example

To ilustrate the mining technique, let's consider the test paths gerenated for the state-chart in 3.1 presented in table 4.1. The *PrefixSpan* implementaion in *SPMF* requires an input file such that there must be one sequence per line, each event must be delimited by -1 and each line must end with -2 .

Inputing sequences in table 4.1 to *PrefixSpan* with minimum support of 70%, we get as output the patterns presented in table 4.2. We can then notice, for instance, that events *doLogin*, *searchLineNumber* and *searchEmployee* occur in this order in at least 70% of the generated test cases.

4.2 Generation of properties from most frequent test case patterns

Based on the patterns obtained through the usage of *PrefixSpan* shown previously, we can automatically define formal properties to be verified. For this project we chose two property specification patterns, explained in subsection 2.4.2, from [8]: the response and existence patterns. First, we go through the most frequent patterns based on a user minimum support and compute the formal response properties. Secondly, we try to find patterns that are common to all of the test cases to be able to apply the existence specification.

Test paths for 3.1
doLogin -1 -2
doLogin -1 searchLineNumber -1 -2
doLogin -1 searchLineNumber -1 searchEmployee -1 -2
doLogin -1 searchLineNumber -1 searchEmployee -1 setEmployeeUser -1 -2
doLogin -1 searchLineNumber -1 searchEmployee -1 setEmployeeUser -1 emptyCart -1 -2
doLogin -1 searchLineNumber -1 searchEmployee -1 setRegularUser -1 -2
doLogin -1 searchLineNumber -1 searchEmployee -1 setRegularUser -1 searchContract -1 -2
doLogin -1 searchLineNumber -1 searchEmployee -1 setRegularUser -1 searchContract -1 setCommittedUser -1 -2
doLogin -1 searchLineNumber -1 searchEmployee -1 setRegularUser -1 searchContract -1 setCommittedUser -1 emptyCart -1 -2
doLogin -1 searchLineNumber -1 searchEmployee -1 setRegularUser -1 searchContract -1 setNotCommittedUser -1 -2
doLogin -1 searchLineNumber -1 searchEmployee -1 setRegularUser -1 searchContract -1 setNotCommittedUser -1 proceedCheckout -1 -2

Table 4.1: Test paths obtained from the test cases generated for statechart 3.1

doLogin -1
doLogin -1 searchEmployee -1
doLogin -1 searchLineNumber -1
doLogin -1 searchLineNumber -1 searchEmployee -1
searchLineNumber -1
searchLineNumber -1 searchEmployee -1
searchEmployee -1

Table 4.2: Patterns extracted from sequences in 4.1 with minimum support of 70%.

4.2.1 Response property specification

The creation of response properties is done using as input the patterns mined by *PrefixSpan* in the previous step. In each sequence pattern, we obtain pairs of consecutive events and establish a property that the second event must respond to the first event of the pair. The global scope was used. The pseudocode can be found below:

```
//Method to write the specification of formal response properties
//It receives as argument a sequence pattern

Set propertySet = new Set();

Set extractResponseProperties(Sequence pattern) {

    for (i = 0; i < pattern.length - 1; i++) {
        Event P = pattern.getEvent(i);
        Event S = pattern.getEvent(i+1);

        Property responseProperty =  $\Box(P.getName() \rightarrow \Diamond S.getName())$ ;
    }
}
```

```

    if (!propertySet.contains(responseProperty))
        propertySet.add(responseProperty);
}

return propertySet;
}

```

To illustrate this phase, take as example the test cases automatically generated for statechart 3.1 presented in table 4.1. With a minimum support of 70%, the most frequent patterns returned are displayed in table 4.2. Applying each one of these patterns as input to the method *extractResponseProperties*, we obtain the following response properties:

Informal description	Formal specification
searchLineNumber responds to doLogin	$\Box(doLogin \rightarrow \Diamond searchLineNumber)$
searchEmployee responds to doLogin	$\Box(doLogin \rightarrow \Diamond searchEmployee)$
searchEmployee responds to searchLineNumber	$\Box(searchLineNumber \rightarrow \Diamond searchEmployee)$

Table 4.3: Properties automatically extracted from patterns in 4.2.

These properties reflect some of the requirements of the specification. The fact that the login has to be performed so that we can access the users information, such as their line number or check if they are employees of the company, for example. Besides, according to the flow described by the statechart 3.1, the employee verification should be done after the line number is retrieved, as stated by the third property.

It is possible to realize that some of the generated properties contain events in common. Therefore, we can combine them in order to reduce the number of properties that should be verified by the model checker. Let's consider, for example, the first and second properties described in table 4.3:

$$\begin{aligned}
 &\Box(doLogin \rightarrow \Diamond searchLineNumber) \\
 &\Box(doLogin \rightarrow \Diamond searchEmployee)
 \end{aligned}$$

Events *searchLineNumber* and *searchEmployee* respond to the same event *doLogin*. Hence, we synthesize both properties in a new more concise one:

$$\Box(doLogin \rightarrow \Diamond(searchLineNumber \wedge searchEmployee))$$

Then, we are left only with two properties to be passed to the model checker:

Informal description	Formal specification
searchLineNumber and searchEmployee responds to doLogin	$\Box(doLogin \rightarrow \Diamond(searchLineNumber \wedge searchEmployee))$
searchEmployee responds to searchLineNumber	$\Box(searchLineNumber \rightarrow \Diamond searchEmployee)$

Table 4.4: Combined properties from 4.3.

4.2.2 Existence property specification

In order to use the existence specification pattern, we must find sequence patterns that are present in the whole set of test cases. In other words, we should perform the test case mining stage with a minimum support of 100%. If any such pattern is found, we use the global scope and define an existence property, meaning that for every path taken, we will eventually find that pattern. Considering that a list of events was found as patterns with support of 100%, the following pseudocode can be used:

```
//Method to write the specification of formal existence properties
//It receives as argument a list of events that were found during mining
with support of 100%

Set propertySet = new Set();

Set extractExistenceProperties(List commonEvents) {

    for (i = 0; i < pattern.length; i++) {
        Event P = pattern.getEvent(i);

        Property existenceProperty =  $\Diamond$ (P.getName());

        if (!propertySet.contains(existenceProperty))
            propertySet.add(existenceProperty);
    }

    return propertySet;
}
```

Still considering test cases in 4.1 to illustrate the process, the only event returned by the mining phase with 100% of support is *doLogin*. Thus, we are able to specify one existence property that indicates that the event *doLogin* must occur, no matter the execution path taken:

Informal description	Formal specification
doLogin must occur	$\Diamond(doLogin)$

Table 4.5: *Existence property extracted from 4.1.*

In case more than one event is present in all test cases, then we can combine them to create a single existence property to be verified by the model checker. Suppose events *a*, *b* and *c* are present in all test cases of a certain statechart model. Then, we could combine their existence properties in the following single property:

$$\Diamond(a \wedge b \wedge c)$$

4.3 Generation of properties for specific events

Due to the fact the only the patterns returned by the sequential mining are considered to derive the property specifications, only the events that appear more often in the test cases are going to be selected to create properties. Rare events are discarded when the minimum support is too high. An immediate solution would be to set the minimum support as a low

doLogin -1 searchLineNumber -1 searchEmployee -1 setRegularUser -1 -2
doLogin -1 searchLineNumber -1 searchEmployee -1 setRegularUser -1 searchContract -1 -2
doLogin -1 searchLineNumber -1 searchEmployee -1 setRegularUser -1 searchContract -1 setCommittedUser -1 -2
doLogin -1 searchLineNumber -1 searchEmployee -1 setRegularUser -1 searchContract -1 setCommittedUser -1 emptyCart -1 -2
doLogin -1 searchLineNumber -1 searchEmployee -1 setRegularUser -1 searchContract -1 setNotCommittedUser -1 -2
doLogin -1 searchLineNumber -1 searchEmployee -1 setRegularUser -1 searchContract -1 setNotCommittedUser -1 proceedCheckout -1 -2

Table 4.6: *Specific database for event setRegularUser.*

setRegularUser
setRegularUser, searchContract
setRegularUser, searchContract, setNotCommittedUser
setRegularUser, searchContract, setNotCommittedUser, proceedCheckout
setRegularUser, searchContract, emptyCart
doLogin, searchEmployee, setRegularUser
doLogin, searchEmployee, setRegularUser, searchContract
doLogin, searchEmployee, setRegularUser, searchContract, setNotCommittedUser
doLogin, searchEmployee, setRegularUser, searchContract, setNotCommittedUser, proceedCheckout

Table 4.7: *Some sequence combinations generated by PrefixSpan for setRegularUser.*

value, but that would cause too many patterns to be selected and many irrelevant properties may be specified making the model checking process more costly in time and resources.

Taking these aspects into consideration, we propose a solution that gives the users the option to input specific events to which they want properties to be specified. This approach allows rare events to be handled properly and, since the user is able to check which properties we were automatically generated with mining, becomes a general solution to formally specify event-based requirements.

For the specific event l passed by the user, we initially select the test cases that contain l to construct our sequence database. Secondly, we run the *PrefixSpan* provided by *SPMF* with minimum support of 0, which will output all possible sequence combination present in the database that contain the event l . Then, we are able to construct response property specifications for l .

As an example, consider the *setRegularUser* event, which is not present in the properties in table 4.3 generated with minimum support of 70%. Suppose a user wishes to obtain response properties with this event and inputs it in our implementation.

We, then, construct a new sequence database that has only sequences that contain the event *setRegularUser*. The next step, is to run *PrefixSpan* with minimum support of 0. With that input value, the algorithm will generate all possible combinations that can be derived from the specific database. To illustrate this, we provide the specific database in table 4.6 and some of the possible combinations in table 4.7.

Finally, for each sequence combination returned, we can specify the related response property. The pseudocode is similar to the one shown in 4.2.1 and goes as follows:

```
//Method to write the specification of formal response properties
//It receives as argument a sequence combination and the specific event

Set propertySet = new Set();

Set extractResponseProperties(Sequence combination, Event specific) {

    for (i = 0; i < pattern.length - 1; i++) {
        Event P = pattern.getEvent(i);
        Event S = pattern.getEvent(i+1);
        if (P == specific or S == specific) {
            Property responseProperty =  $\Box(P.getName() \rightarrow \Diamond S.getName())$ ;

            if (!propertySet.contains(responseProperty))
                propertySet.add(responseProperty);
        }
    }
    return propertySet;
}
```

Considering the whole set of combinations for *setRegularUser*, the response properties below are automatically specified:

Informal description	Formal specification
setRegularUser responds to searchEmployee	$\Box(searchEmployee \rightarrow \Diamond setRegularUser)$
searchContract responds to setRegularUser	$\Box(setRegularUser \rightarrow \Diamond searchContract)$
setRegularUser responds to searchLineNumber	$\Box(searchLineNumber \rightarrow \Diamond setRegularUser)$
setCommittedUser responds to setRegularUser	$\Box(setRegularUser \rightarrow \Diamond setCommittedUser)$
setNotCommittedUser responds to setRegularUser	$\Box(setRegularUser \rightarrow \Diamond setNotCommittedUser)$
setRegularUser responds to doLogin	$\Box(doLogin \rightarrow \Diamond setRegularUser)$
emptyCart responds to setRegularUser	$\Box(setRegularUser \rightarrow \Diamond emptyCart)$
proceedCheckout responds to setRegularUser	$\Box(setRegularUser \rightarrow \Diamond proceedCheckout)$

Table 4.8: Response properties for event *setRegularUser*.

Moreover, we can summarize the extracted properties by combining them in a similar process as presented in section 4.2.1. There are properties in which some event responds to *setRegularUser* and in others *setRegularUser* responds to some event. Hence, we are left with the following two concise properties to use in the model checking verification:

- *Informal description:* emptyCart, proceedCheckout, setCommittedUser, setNotCommittedUser and searchContract respond to setRegularUser

Formal specification: $\Box(setRegularUser \rightarrow \Diamond(emptyCart \wedge proceedCheckout \wedge setCommittedUser \wedge setNotCommittedUser \wedge searchContract))$

- *Informal description:* `setRegularUser` responds to `doLogin`, `searchLineNumber` and `searchEmployee`

Formal specification: $\Box((doLogin \vee searchLineNumber \vee searchEmployee) \rightarrow \Diamond(setRegularUser))$

Chapter 5

Tools demonstration

To illustrate the whole process of generating test cases and extracting formal properties and how our implemented tools work, we will use as an example the statechart model given in 5.1 created with the Yakindu Statechart Tools [1]. It models the order processing of an ecommerce portal after the user concluded the purchase. An order is made of entries, each one containing a product and its corresponding quantity. The order information must be converted in a XML file that will be sent to an integration layer. In parallel to the XML preparation, a job is executed to send customers an email informing that their order was received and is being processed.

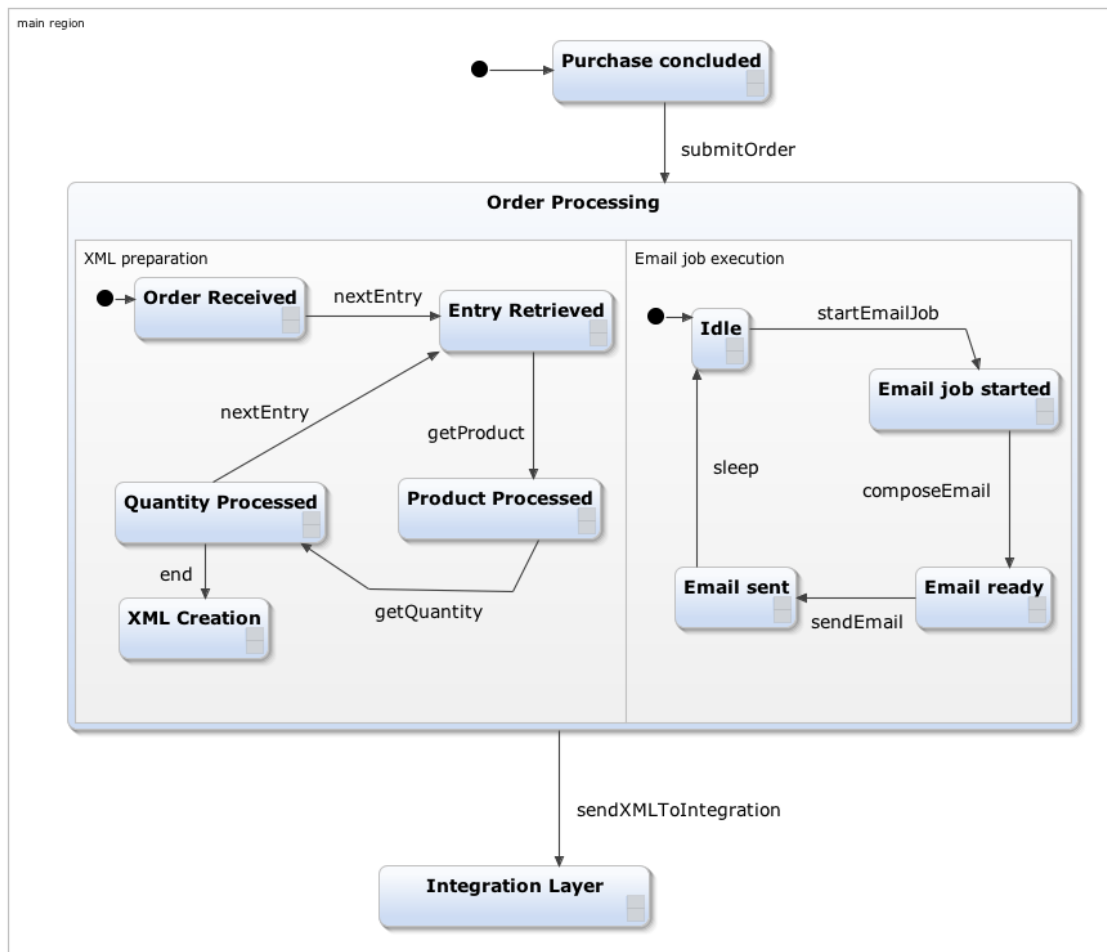


Figure 5.1: Statechart model for order processing

5.1 Test case generation tool

The interface of test generation tool is displayed in figure 5.2.

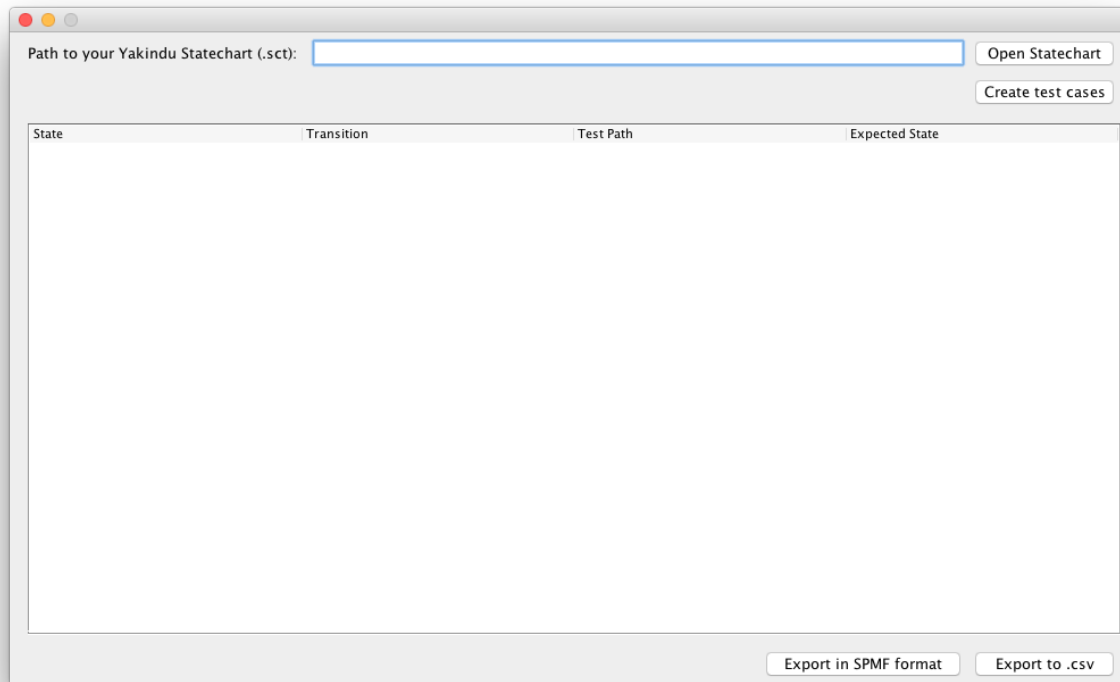


Figure 5.2: *Test case generator interface.*

We start by opening the statechart we created. Then, we click on button *Create test cases* and the created test cases will be outputted in the screen, as shown in figure 5.3. Note that the table is divided in four columns:

- *State*, which tells from which state we are taking a transition
- *Transition*, which informs the transition that is being tested
- *Test path*, which contains the path to execute that transition
- *Expected state*, which is the expected state we should get when that transition is taken

If we click to *export in SPMF format*, we are able to save the test paths in a text file with the format that the framework *SPMF* and our second tool need as input. Test paths are then saved in the way displayed by figure 5.4.

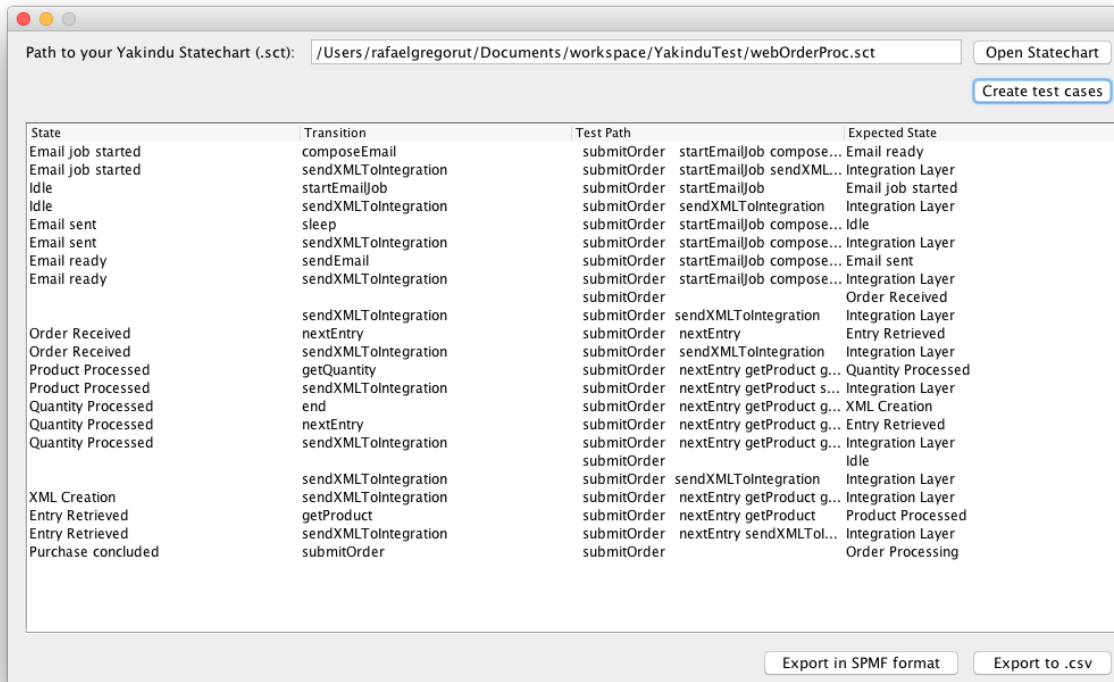
We are also capable to export the information from all columns to a *.csv* file, which can be read by most spreadsheet softwares in the market. Just click on button *Export to .csv*.

The Java code written to develop this tool can be found in our Github repository¹.

5.2 Formal property specification tool

The interface of our formal property specification tool is displayed in figure 5.5. It contains two tabs:

¹<https://github.com/rafaelgregorut/StatechartTests>



State	Transition	Test Path	Expected State
Email job started	composeEmail	submitOrder	startEmailJob compose... Email ready
Email job started	sendXMLToIntegration	submitOrder	startEmailJob sendXML... Integration Layer
Idle	startEmailJob	submitOrder	startEmailJob Email job started
Idle	sendXMLToIntegration	submitOrder	sendXMLToIntegration Integration Layer
Email sent	sleep	submitOrder	startEmailJob compose... Idle
Email sent	sendXMLToIntegration	submitOrder	startEmailJob compose... Integration Layer
Email ready	sendEmail	submitOrder	startEmailJob compose... Email sent
Email ready	sendXMLToIntegration	submitOrder	startEmailJob compose... Integration Layer
		submitOrder	Order Received
Order Received	sendXMLToIntegration	submitOrder	sendXMLToIntegration Integration Layer
Order Received	nextEntry	submitOrder	nextEntry Entry Retrieved
Product Processed	sendXMLToIntegration	submitOrder	sendXMLToIntegration Integration Layer
Product Processed	getQuantity	submitOrder	nextEntry getProduct g... Quantity Processed
Quantity Processed	sendXMLToIntegration	submitOrder	nextEntry getProduct s... Integration Layer
Quantity Processed	end	submitOrder	nextEntry getProduct g... XML Creation
Quantity Processed	nextEntry	submitOrder	nextEntry getProduct g... Entry Retrieved
Quantity Processed	sendXMLToIntegration	submitOrder	nextEntry getProduct g... Integration Layer
		submitOrder	Idle
XML Creation	sendXMLToIntegration	submitOrder	sendXMLToIntegration Integration Layer
Entry Retrieved	sendXMLToIntegration	submitOrder	nextEntry getProduct g... Integration Layer
Entry Retrieved	getProduct	submitOrder	nextEntry getProduct Product Processed
Entry Retrieved	sendXMLToIntegration	submitOrder	nextEntry sendXMLTo... Integration Layer
Purchase concluded	submitOrder	submitOrder	Order Processing

Figure 5.3: Test cases created for statechart 5.1.

- *Properties based on test case mining*, where properties will be specified based on the results returned by sequential mining
- *Properties for specific events*, where the user is able to specify an event and properties related to it will be displayed.

Initially, we open the file created by our test case generator in *SPMF* input format, pass a minimum support of 0.3 (30%) and click on *Specify properties* to generate the properties, as displayed in figure 5.6

Response properties are displayed in the first panel, note that combined properties are also displayed. Existence properties are shown in the second panel, notice that their combination are displayed too. In both panels there are two columns:


- *Informal description*, that contains a description of the property in natural language
- *Formal property*, where the formal specification in LTL is displayed.

Notice that, in case a sequence is ignored in the mining process, its ID will be displayed in the bottom area *Not used sequences*. The ID of a sequence corresponds to the line number where the sequence is located.

If we change to tab *Properties for specific events*, we can obtain response properties for a specific event. In our example, we passed as input the event *sendEmail* and, once *Specify properties* is clicked, the response properties are displayed as shown in figure 5.7. Note that combined properties are also displayed.

The Java code written to develop this tool can be found in our Github repository².

²<https://github.com/rafaelgregorut/ExtractingStateSequences>



```

submitOrder -1 -2
submitOrder -1 nextEntry -1 -2
submitOrder -1 nextEntry -1 getProduct -1 -2
submitOrder -1 nextEntry -1 getProduct -1 getQuantity -1 -2
submitOrder -1 nextEntry -1 getProduct -1 getQuantity -1 end -1 -2
submitOrder -1 nextEntry -1 getProduct -1 getQuantity -1 end -1 sendXMLToIntegration -1 -2
submitOrder -1 nextEntry -1 getProduct -1 getQuantity -1 nextEntry -1 -2
submitOrder -1 nextEntry -1 getProduct -1 getQuantity -1 sendXMLToIntegration -1 -2
submitOrder -1 nextEntry -1 getProduct -1 sendXMLToIntegration -1 -2
submitOrder -1 nextEntry -1 sendXMLToIntegration -1 -2
submitOrder -1 startEmailJob -1 -2
submitOrder -1 startEmailJob -1 composeEmail -1 -2
submitOrder -1 startEmailJob -1 composeEmail -1 sendEmail -1 -2
submitOrder -1 startEmailJob -1 composeEmail -1 sendEmail -1 sendXMLToIntegration -1 -2
submitOrder -1 startEmailJob -1 composeEmail -1 sendEmail -1 sleep -1 -2
submitOrder -1 startEmailJob -1 composeEmail -1 sendXMLToIntegration -1 -2
submitOrder -1 startEmailJob -1 sendXMLToIntegration -1 -2

```

Figure 5.4: Test paths from 5.1 in the SPMF input format.

The image shows a software window titled "Property generator tool interface". At the top, there is a text field labeled "Test cases sequences (SPMF) :" followed by an empty input box and an "Open" button. Below this, there are two tabs: "Properties based on test case mining" (which is selected and highlighted in blue) and "Properties for specific events". Under the selected tab, there is a "Minimum support (e.g. 0.5)" label followed by an empty input box and a "Specify properties" button. The main area of the window is divided into two sections: "Responsive Properties" and "Existence Properties". Each section contains a table with two columns: "Informal Description" and "Formal property". Both tables are currently empty. At the bottom of the window, there is a label "Not used sequences:" followed by an empty input box.

Test cases sequences (SPMF) :

☒ Properties based on test case mining ☐ Properties for specific events

Minimum support (e.g. 0.5)

Responsive Properties

Informal Description	Formal property
----------------------	-----------------

Existence Properties

Informal Description	Formal property
----------------------	-----------------

Not used sequences:

Figure 5.5: *Property generator tool interface.*

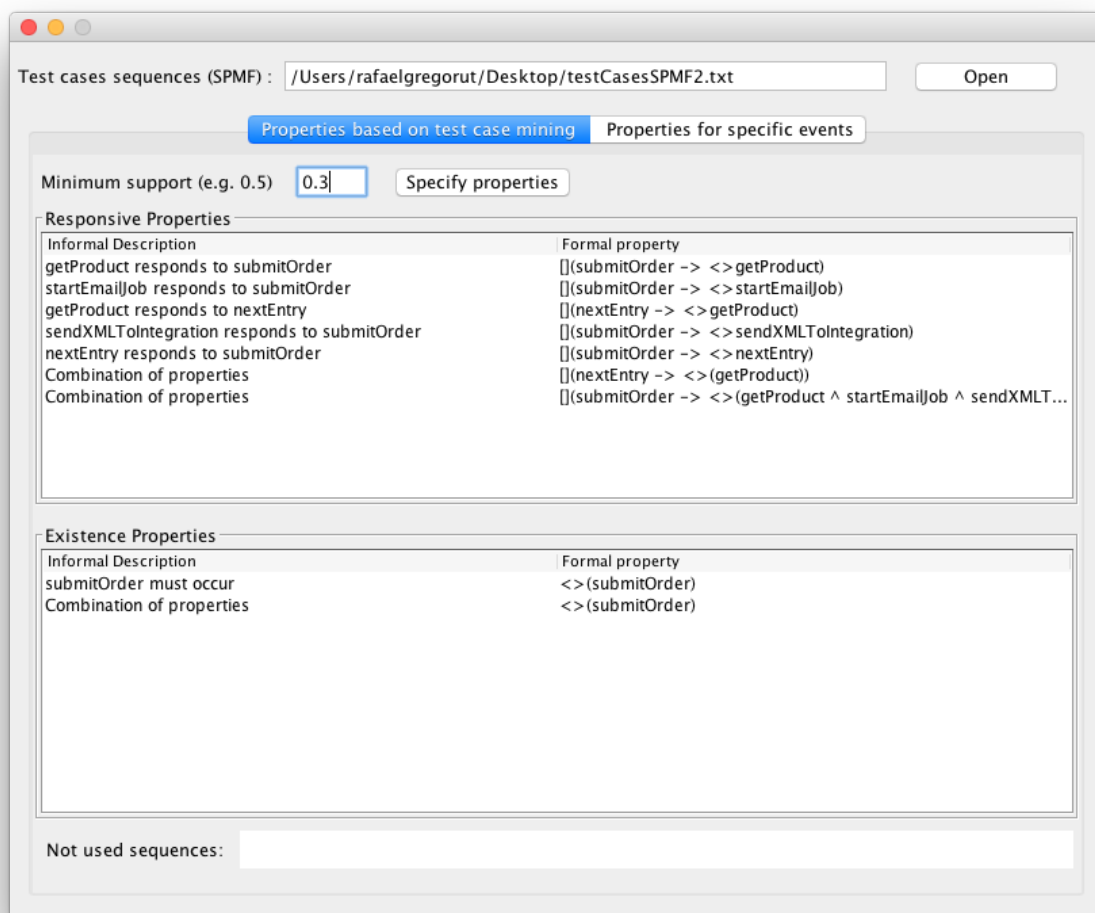


Figure 5.6: *Properties automatically specified for 5.1 based on the mining of 5.4.*

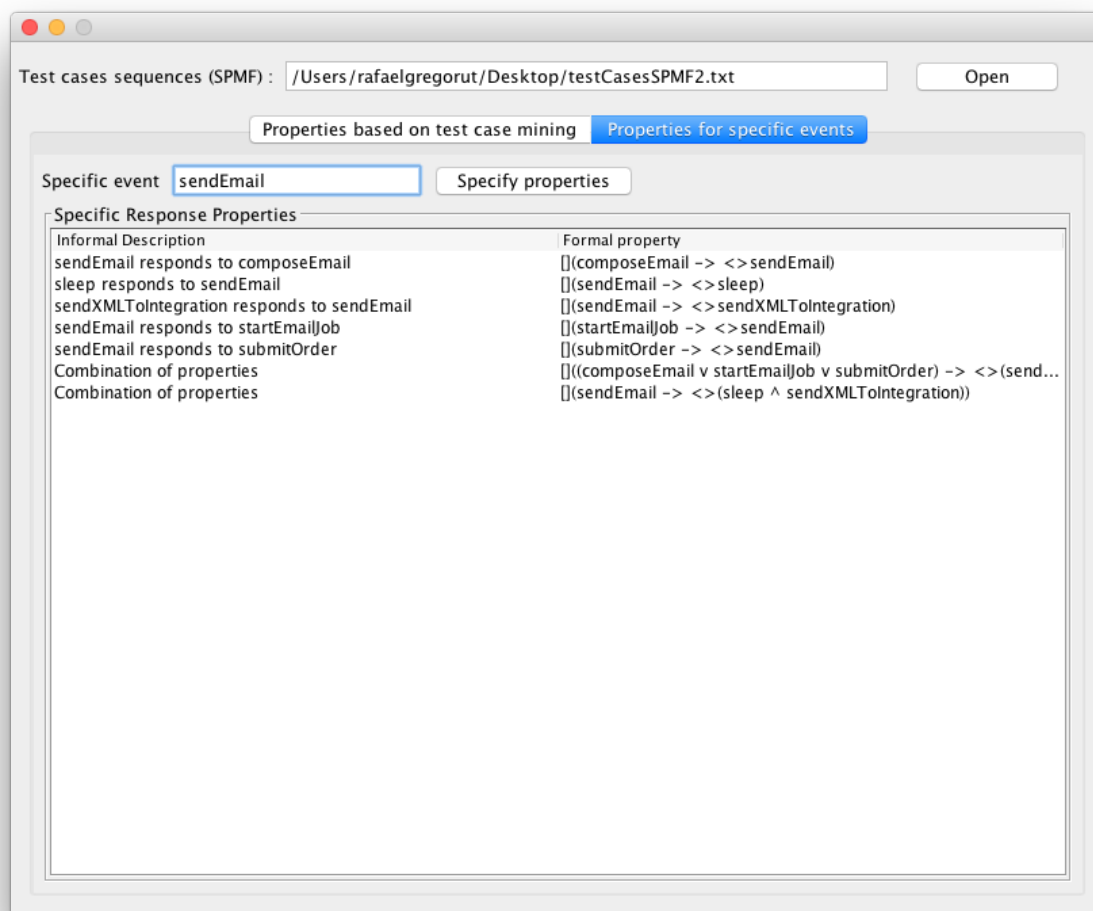


Figure 5.7: *Properties specified for sendEmail event.*

Chapter 6

Conclusion

The specification is a reference to be consulted throughout the entire software development cycle. For the code implementation, the specification's relevance is due to the fact that it contains client's requisites and project decisions to guide programming. As far as testing is concerned, the requirements specified should also be used in test cases to validate the observed behaviour and the output of the system comparing to what was expected. However, the main issue a specification can suffer from is the lack of precision and completeness, which will cause gaps in understanding during the whole process and consequently damage development and testing.

Therefore, manually created test cases based on informal specifications tend to lack accuracy and the imprecise information they contain may lead to incorrect conclusions regarding the quality of the system under consideration. An implementation that passes all test cases successfully is not guaranteed to have no errors, since the testing may not have covered all usage scenarios and it certainly did not test all possible inputs.

Formal methods can bring formalism and contribute to the software development process. Already used in critical systems, they provide techniques with rigorous mechanisms to assure the quality and safety of the product being delivered. Even though the complexity of general systems is huge, formal approaches can be applied in different parts of different phases of the development.

Consider statecharts for example, a type of formal specification based on finite state machines. They provide ways to specify flows and scenarios with formalism and precision, even for concurrent systems. Besides, they have the visual appeal that facilitates their comprehension. Tools, such as Yakindu, to create and even simulate their execution are available and collaborate to their spread in the software engineering community.

Formal verification can also be used in specific components of the system, such as security protocols. It does not depend on any specific input and, hence, can discover incorrect behaviours that tests would not be able to find. Besides, formal verification can prove the absence of errors, an achievement that is not reachable by testing. Nonetheless, this approach faces practical obstacles, such as the difficulty to convert informal requisites into formal properties and the specification language that must be used.

With these aspects in mind, we believe that formal techniques should be used along with testing to increase the quality of a system. The technique to generate test cases from formal statechart specifications can be used to create test cases with more precision and guide the test execution. In addition, the automatic synthesis of formal properties provides more automation to the formal verification process and assist developers to specify relevant properties of the system.

Chapter 7

Subjective chapter

In this chapter the author is free to express his own impressions and opinions about the knowledge and experience acquired during this project.

7.1 Learning

During this project, I had the opportunity to learn in greater depth some topics in computer science that were briefly discussed or even not presented in regular courses, such as testing, formal specification and formal verification.

In relation to formal specifications, it was interesting to realize that statecharts can be visually appealing and simultaneously provide accuracy to model scenarios. Although they need to be more spread in the community and industry, statecharts can be applied to real projects.

The concepts learned about testing complemented the practical experience from the internship. In addition, I believe that, due to the contact with testing theory, the execution of my work became more consistent and the maturity level of my professional activities increased. Besides, the practical experience also helped writing this monograph, since I could see many concepts being applied out of the academic environment.

Finally, it was fun to connect distinct areas (testing, formal specifications, formal verification, sequential mining, logic) to automate processes in the software development cycle.

7.2 Challenges

One challenge was to find a free open-source tool to create Statecharts. Initially, we were using a tool that already had test criteria for statecharts, but there were many bugs so we had to abandon it. Yakindu was a great finding and contributed a lot to the progress of the project.

Since we could not use the initial tool anymore, the test case generation for statecharts had to be implemented. The orthogonality feature needed more attention, specially to understand the semantics behind the diagram modeling concurrency.

7.3 Courses

I believe that the Computer Science curriculum offered by the Institute of Mathematics and Statistics at University of São Paulo gives students a broad knowledge of many fields in computer science and a strong mathematical background. From my perspective, though,

some topics should be explored in greater details in the program. For instance, it is not offered a course regarding software quality, which is an important set of concepts that are required from students as soon as they start their professional career. The only course that stressed the importance of testing, but not as a main topic, was *Extreme Programming Lab*, which is not even a compulsory course. Moreover, I believe that more techniques and tools related to formal methods, such as formal specifications, could be presented. Among the many courses taken, I consider the ones below more directly relevant for the conclusion of this project:

- **MAC0332 - Software Engineering**

Comprehension of the activities that take place in the process of software development and their purposes.

- **MAC0414 - Formal Languages and Automata Theory**

Studied formalisms and algorithms regarding state machines.

- **MAC0239 - Formal Methods in Programming**

First contact with formal methods and linear time logic.

- **MAC0342 - Extreme Programming Lab**

The only course in which students were explicitly required to test their implementations. Not only unit tests were written, but also acceptance tests with real clients were performed.

- **MAC0242 - Programming Lab 2**

More knowledge about Java and object oriented programming were acquired.

Appendix A

Linear Temporal Logic (LTL)

In the next sections we present the syntax and semantics of linear temporal logic based on [16] and [29].

A.1 Syntax

LTL has the following syntax given in the Backus Naur form:

$$\phi ::= \top \mid \perp \mid p \mid \neg\phi \mid \phi \wedge \phi \mid \phi \vee \phi \mid \phi \rightarrow \phi \mid \Diamond\phi \mid \Box\phi \mid X\phi \mid \phi U \phi \mid \phi W \phi \mid \phi R \phi$$

where $p \in A$, the set of propositional atoms.

A.2 Semantics

- **Definition:** A transition system $M = (S, \rightarrow, L)$ is a set of states S endowed with a transition relation \rightarrow (binary relation on S), such that every $s \in S$ has some $s' \in S$, and a labeling function $L : S \rightarrow 2^A$.
- **Definition:** A path π in model $M = (S, \rightarrow, L)$ is an infinite sequence s_1, s_2, \dots in S such that, for each $i \geq 1$, $s_i \rightarrow s_{i+1}$. We write π^i for the suffix starting at s_i .

Let $M = (S, \rightarrow, L)$ be a model and $\pi = s_1 \rightarrow \dots$ be a path in M . Whether π satisfies an LTL formula is defined by the satisfaction relation \models as follows:

- 1 $\pi \models \top$
- 2 $\pi \not\models \perp$
- 3 $\pi \models p \Leftrightarrow p \in L(s_1)$
- 4 $\pi \models \neg\phi \Leftrightarrow \pi \not\models \phi$
- 5 $\pi \models \phi_1 \wedge \phi_2 \Leftrightarrow \pi \models \phi_1 \text{ and } \pi \models \phi_2$
- 6 $\pi \models \phi_1 \vee \phi_2 \Leftrightarrow \pi \models \phi_1 \text{ or } \pi \models \phi_2$
- 7 $\pi \models \phi_1 \rightarrow \phi_2 \Leftrightarrow \pi \models \phi_2 \text{ whenever } \pi \models \phi_1$
- 8 $\pi \models \Box\phi \Leftrightarrow, \text{ for all } i \geq 1, \pi^i \models \phi$

9 $\pi \models \Diamond\phi \Leftrightarrow$ there is some $i \geq 1$ such that $\pi^i \models \phi$

10 $\pi \models X\phi \Leftrightarrow \pi^2 \models \phi$

11 $\pi \models \phi U \psi \Leftrightarrow$ there is some $i \geq 1$ such that $\pi^i \models \psi$ and for all $j = 1, \dots, i - 1$ we have $\pi^j \models \phi$

12 $\pi \models \phi W \psi \Leftrightarrow$ either there is some $i \geq 1$ such that $\pi^i \models \psi$ and for all $j = 1, \dots, i - 1$ we have $\pi^j \models \phi$; or for all $n \geq 1$ we have $\pi^n \models \phi$

13 $\pi \models \phi R \psi \Leftrightarrow$ either there is some $i \geq 1$ such that $\pi^i \models \phi$ and for all $j = 1, \dots, i - 1$ we have $\pi^j \models \psi$; or for all $n \geq 1$ we have $\pi^n \models \psi$

Bibliography

- [1] Yakindu statechart tools. <http://www.statecharts.org/>. Last accessed in 11/09/2015. 21, 41
- [2] Paul Ammann and Jeff Offutt. *Introduction to Software Testing*. Cambridge University Press, 2008. 1, 7, 8, 10
- [3] Christel Baier and Joost-Pieter Katoen. *Principles of Model Checking*. The MIT Press, 2008. 15, 16
- [4] Boris Beizer. *Software Testing Techniques*. 2 edition, 1990. 6
- [5] Kirill Bogdanov. *Automated testing of Harel's statecharts*. PhD thesis, Department of Computer Science, University of Sheffield, January 2000. 21, 25, 30
- [6] Jonathan P. Bowen, Kirill Bogdanov, John A. Clark, Robert M. Hierons, and Paul Krause. Fortest: Formal methods and testing. 1
- [7] Edsger W. Dijkstra. The humble programmer. ACM Turing Lecture 1972. 1
- [8] Matthew B. Dwyer, George S. Avrunin, and James C. Corbett. Property specification patterns for finite-state verification. In *2nd Workshop on Formal Methods in Software Practice*, May 1998. 17, 34
- [9] Matthew B. Dwyer, George S. Avrunin, and James C. Corbett. Patterns in property specifications for finite-state verification. In *Proceedings of the 21st International Conference on Software Engineering*, May 1999. 17
- [10] L.-H. Eriksson and K. Johansson. Using formal methods for quality assurance of interlocking systems. 15
- [11] Formal Methods Europe. Formal methods. http://www.fmeurope.org/?page_id=2. Last accessed in 11/11/2015. 1
- [12] P. Fournier-Viger, A. Gomariz, T. Gueniche, A. Soltani, C. Wu., and V. S. Tseng. SPMF: a Java Open-Source Pattern Mining Library. *Journal of Machine Learning Research (JMLR)*, 15:3389–3393, 2014. 34
- [13] Irbis Gallegos, Omar Ochoa, Ann Gates, Steve Roach, Salamah Salamah, and Corina Vela. A property specification tool for generating formal specifications: Prospec 2.0. 2, 16, 17
- [14] Patrice Godefroid. Combining model checking and testing. 16

- [15] David Harel, Amir Pnueli, Jeanette Schmidt, and Rivi Sherman. On the formal semantics of statecharts. In *Proceedings of Symposium on Logic in Computer Science*, pages 55–64, 1987. 1, 3, 4, 6
- [16] Michael Hauth and Mark Ryan. *Logic in computer science. Modelling and reasoning about systems*. Cambridge University Press, 2 edition, 2004. 1, 53
- [17] Harry R. Lewis and Christos H. Papadimitriou. *Elements of the Theory of Computation*. Prentice Hall, Inc, 2 edition, 1998. 3, 4
- [18] Lu Luo. Software testing techniques - technology maturation and research strategy. Technical report, Institute for Software Research International, Carnegie Mellon University, Pittsburgh,USA. 1, 6
- [19] Nizar R. Mabroukeh and C. I. Ezeife. A taxonomy of sequential pattern mining algorithms. *ACM Computing Surveys*, 43, 2010. 18
- [20] José Carlos Maldonado, Ellen Franciane Barbosa, Auri M. R. Vincenzi, Márcio Eduardo Delamaro, Simone do Rocio Senger de Souza, and Mario Jino. *Introdução ao teste de software*, 2004. 9
- [21] José Carlos Maldonado, Márcio Eduardo Delamaro, and Mario Jino. *Introdução ao Teste de Software*. Elsevier, 2007. 13
- [22] Stephan Merz. Model checking: A tutorial overview. 16
- [23] NASA Langley Formal Methods. What is formal methods? <http://shemesh.larc.nasa.gov/fm/fm-what.html>. Last accessed in 11/11/2015. 1
- [24] Glenford J. Myers, Tom Badgett, and Corey Sandler. *The art of software testing*. John Wiley & Sons, Inc, 3rd edition, 2012. 7
- [25] NASA. Testing vs. model checking. http://babelfish.arc.nasa.gov/trac/jpf/wiki/intro/testing_vs_model_checking#no1. Last accessed in 11/07/2015. 16
- [26] Jian Pei, Behzad Mortazavi-Asl, and Umeshwar Dayal. Mining sequential patterns by pattern-growth: The prefixspan approach. *IEEE Transactions on Knowledge and Data Engineering*, 16, 2004. 18, 19, 34
- [27] Ramakrishnan Srikant and Rakesh Agrawal. Mining sequential patterns: Generalizations and performance improvements. 18, 19
- [28] Jeff Tian. *Software Quality Engineering*. John Wiley & Sons, 2005. 15
- [29] Wikipedia. Linear temporal logic. https://en.wikipedia.org/wiki/Linear_temporal_logic. Last accessed in 11/11/2015. 1, 53
- [30] Érica Ferreira de Souza. Geração de casos de teste para sistemas da área espacial usando critérios de teste para máquinas de estados finitos. Master's thesis, Instituto Nacional de Pesquisas Espaciais - INPE, São José dos Campos, Brazil, February 2010. 13, 14, 15