

CL3 - Diagramas

1. Diagrama en Bloques

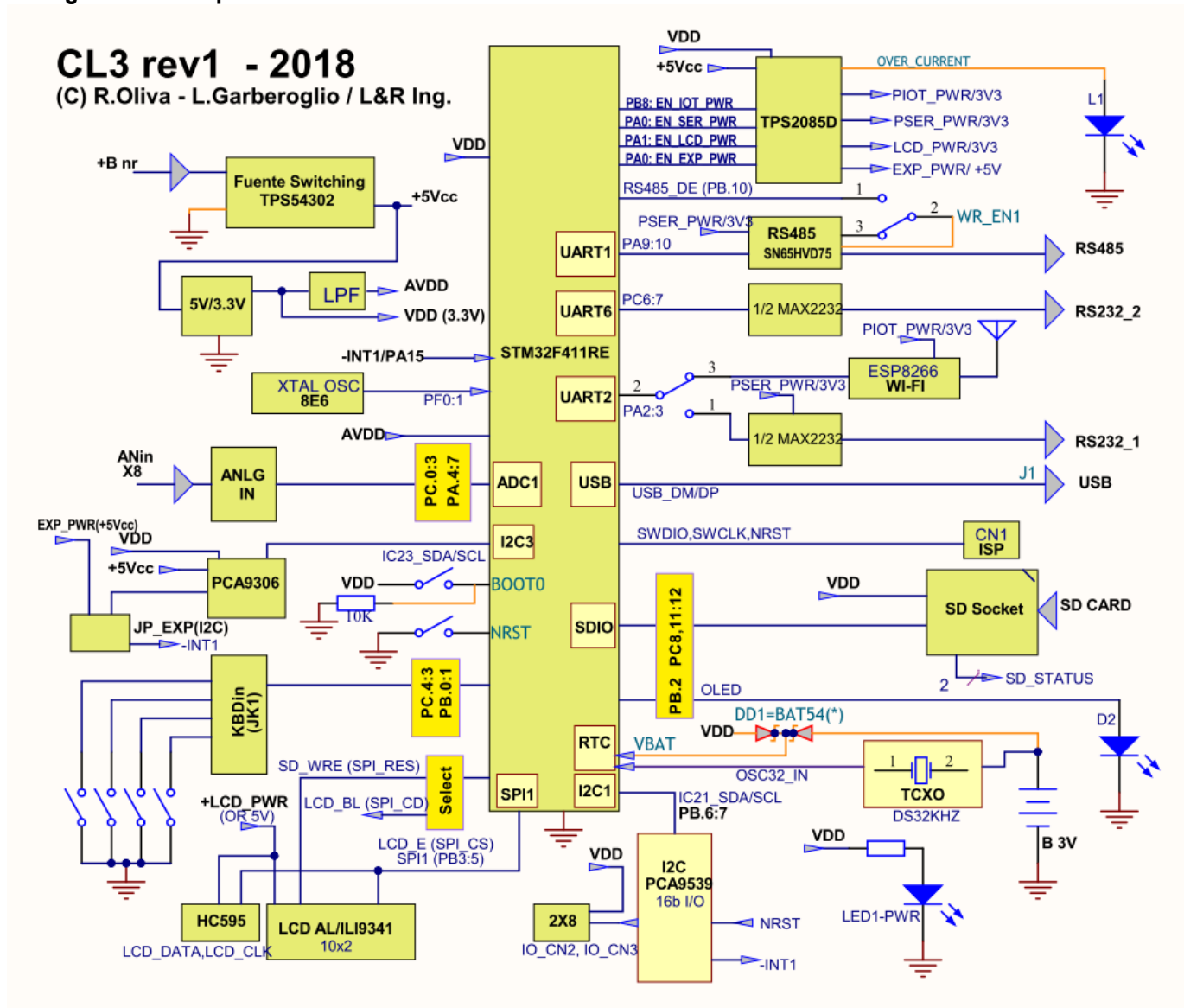


Figura 1: Diagrama en Bloques Placa CL3 – STM32F411RE

2. Dimensiones mecánicas

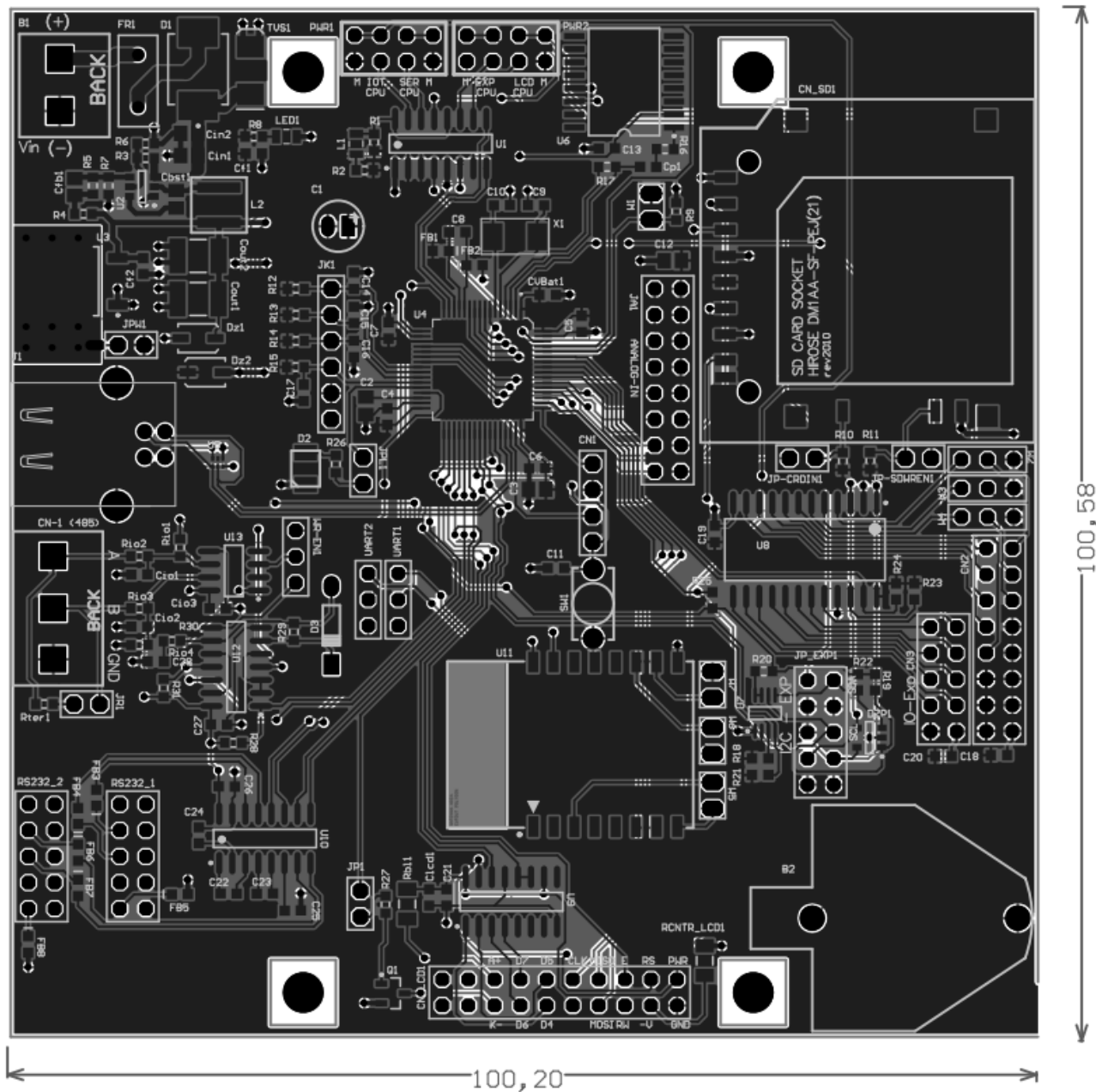
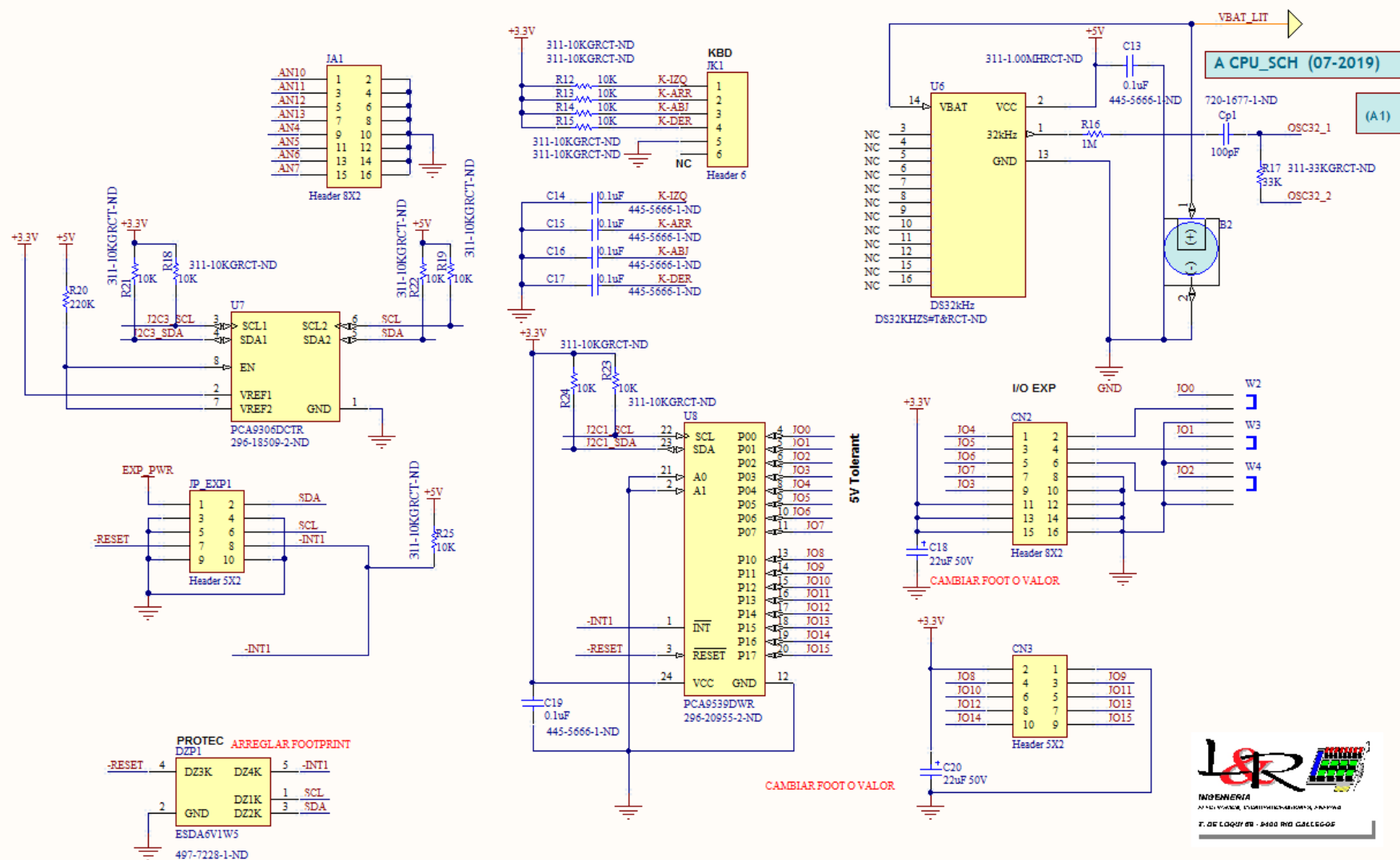


Figura 2: Dimensiones de Placa CL3 - STM32F411RE

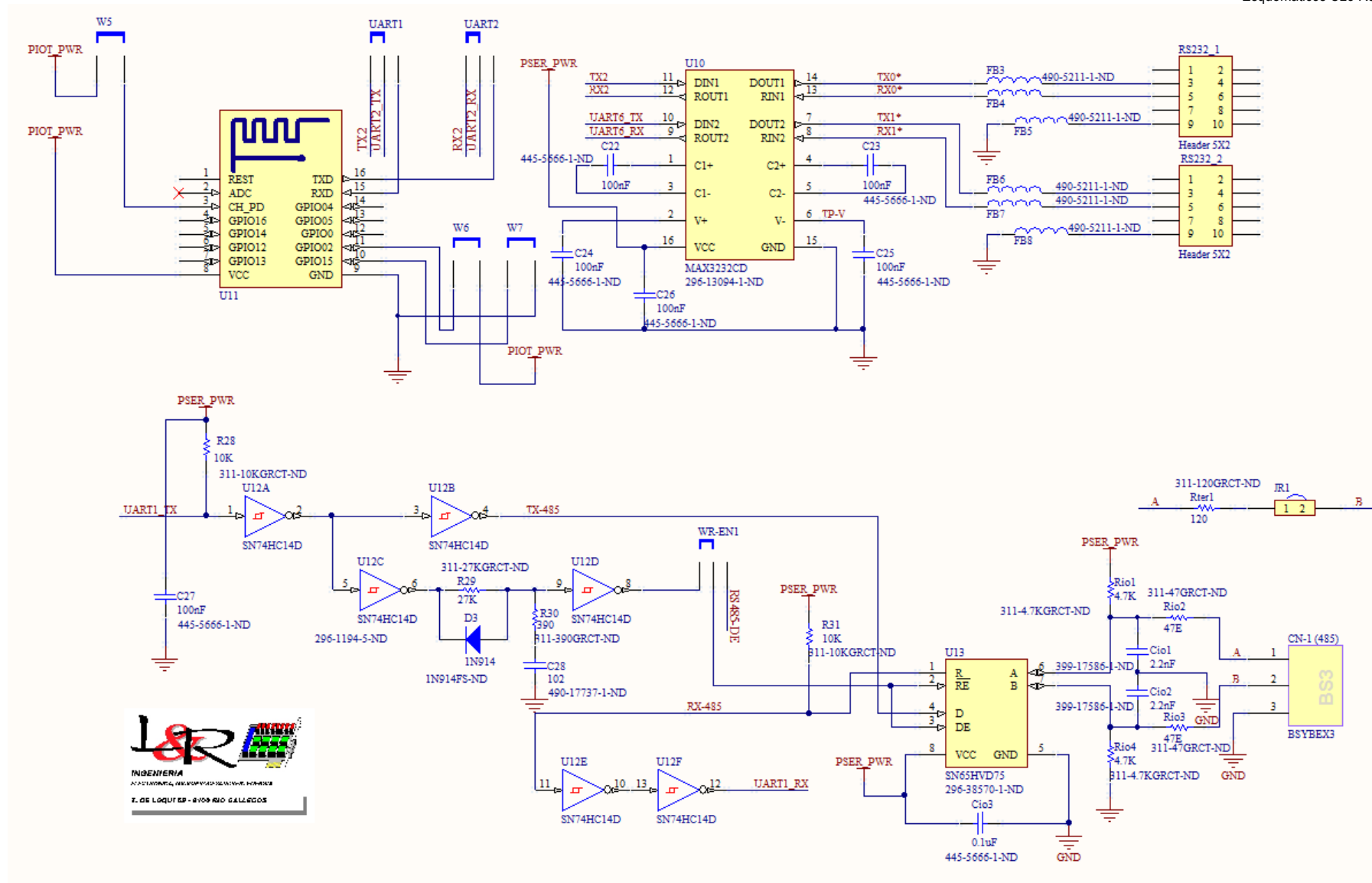
3. Esquemáticos y Modificaciones - Prototipo (06-2019):

- (A, A1) Diodo DD1 para mantener alimentación via Batería Litio en VBAT1 - RTC interno de CPU
- (B, B1 - C,C1) Errores en notación Esquemático - PWR distrib.
- (D1,2,3,3a,3b,4) - Reasignación pines para ILI9341 / SPI Display

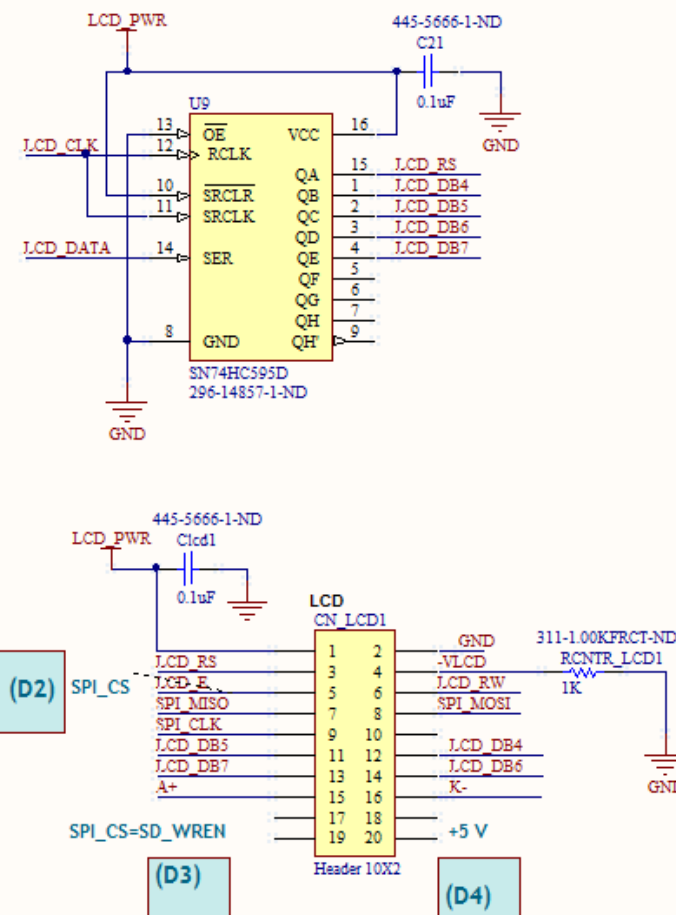
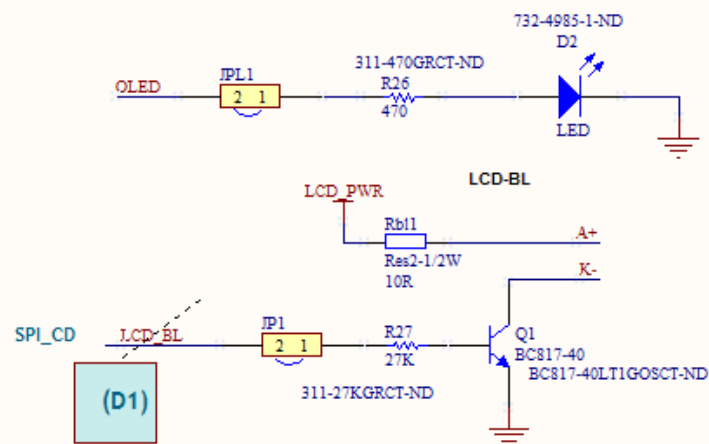
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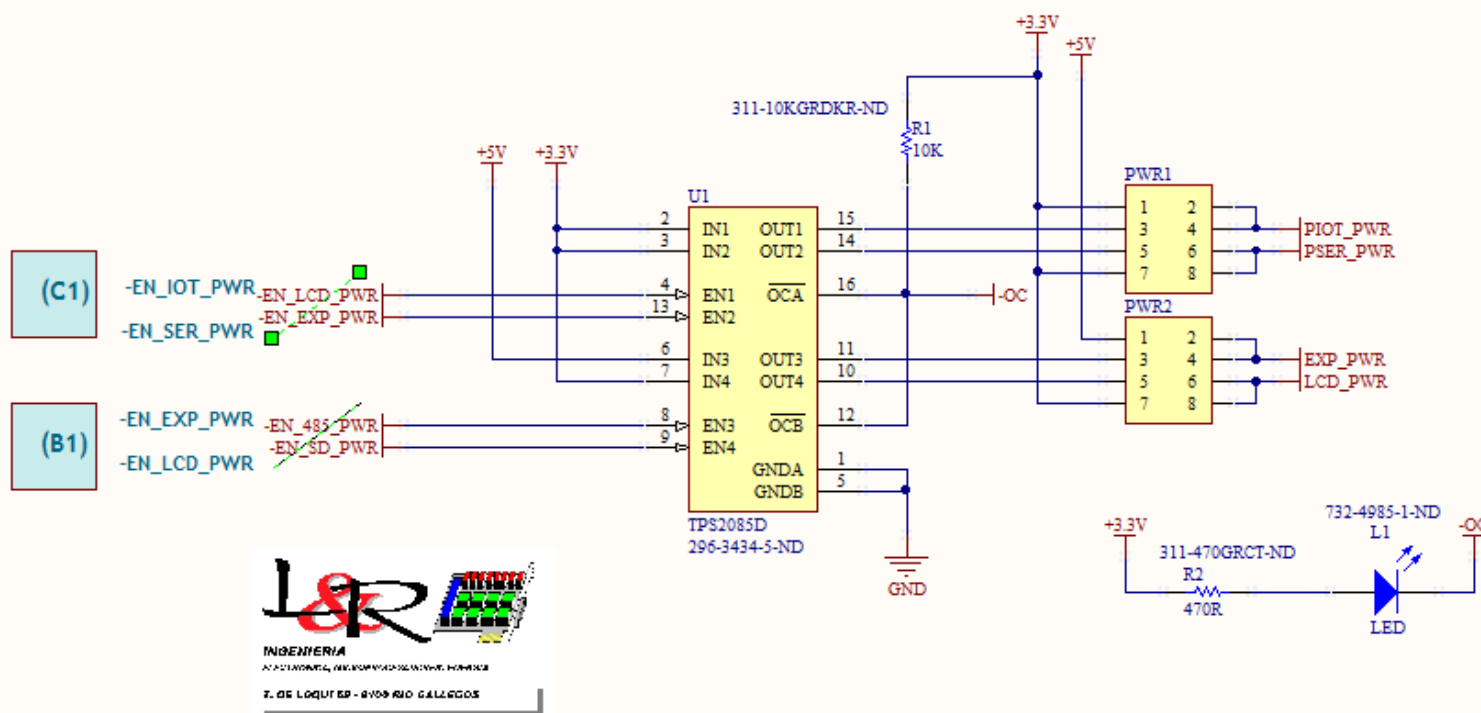
AKI_SCH



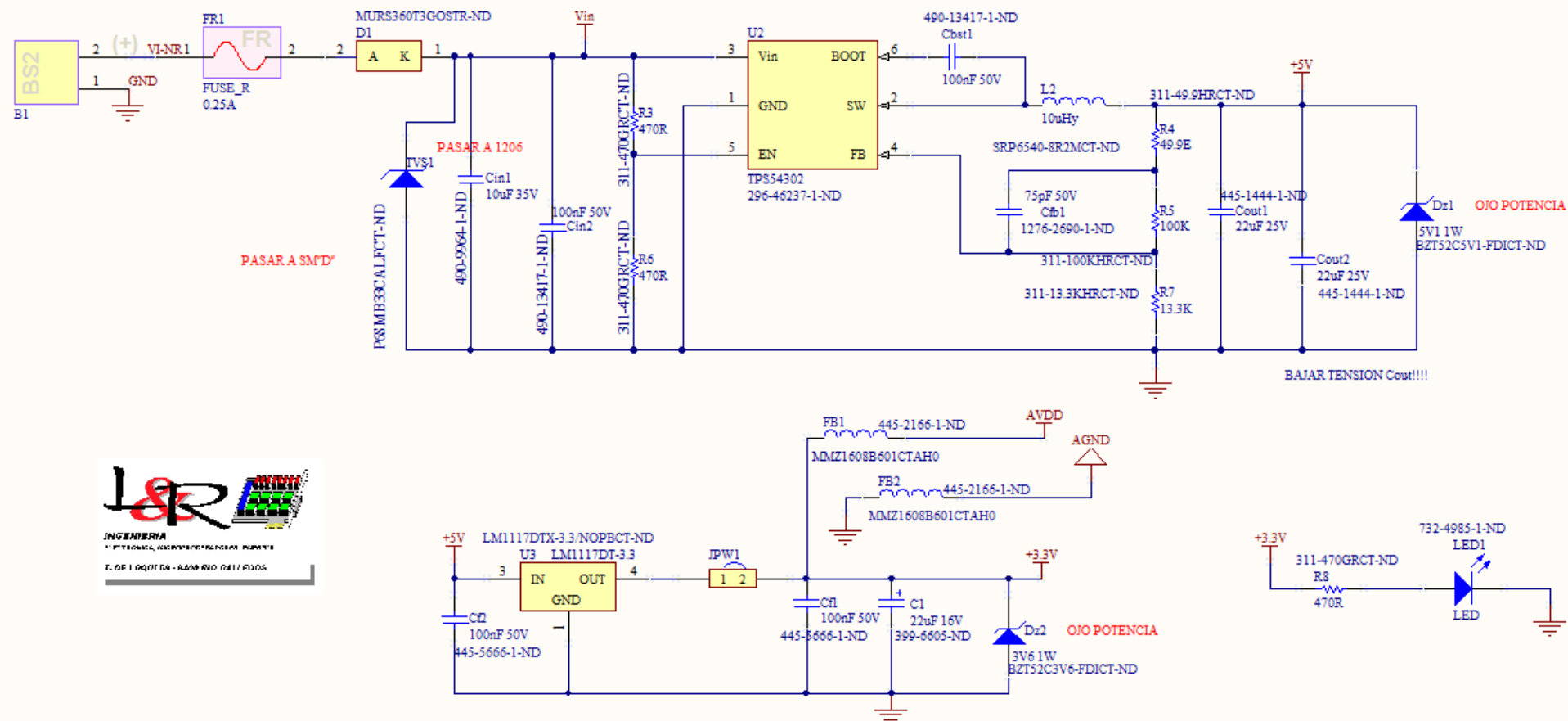
CL3_COMM_SCH



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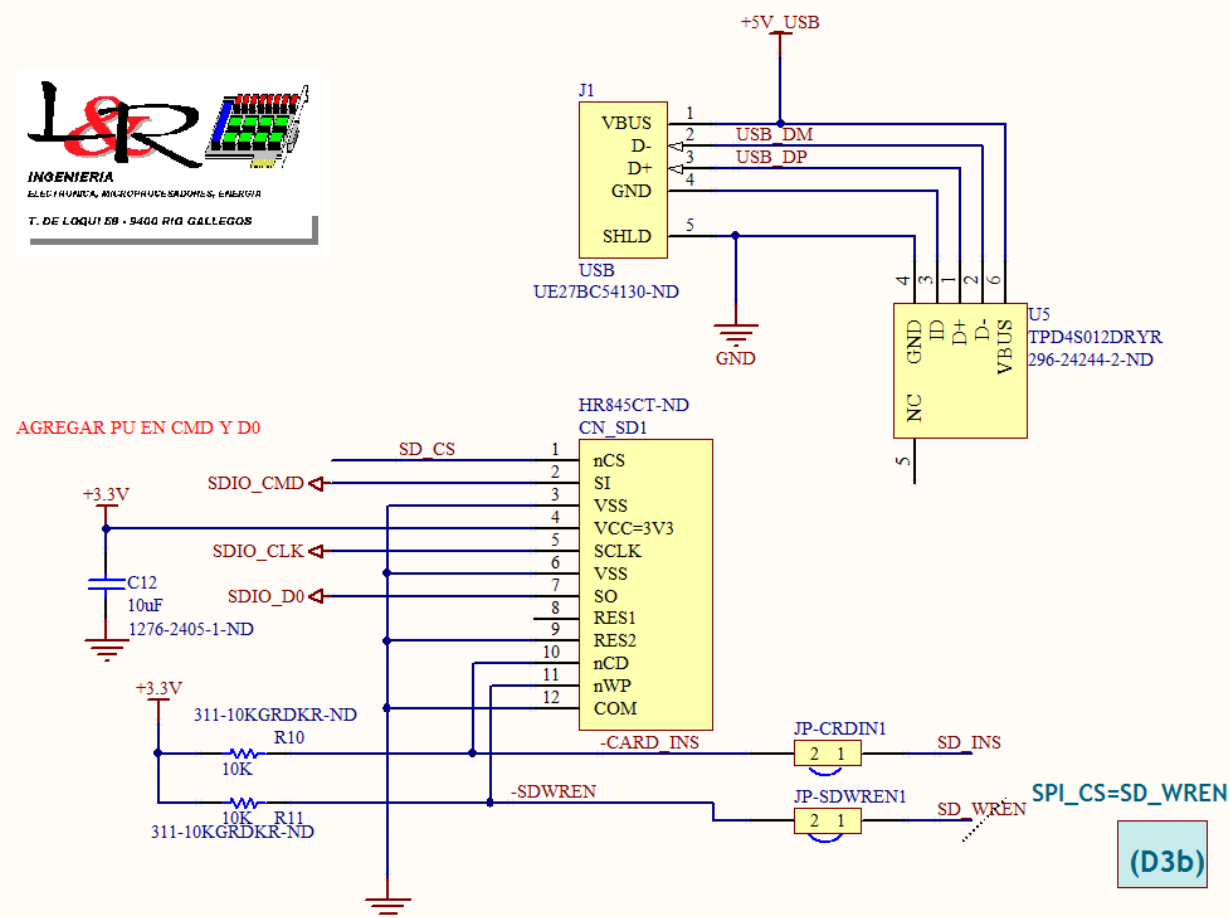


CL3_PWRDISTR_SCH



CL3_PWRSUP_SCH





CL3_SD_USB_SCH