SPI Slave with Single Port RAM

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&

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Team Name:

Place & Route

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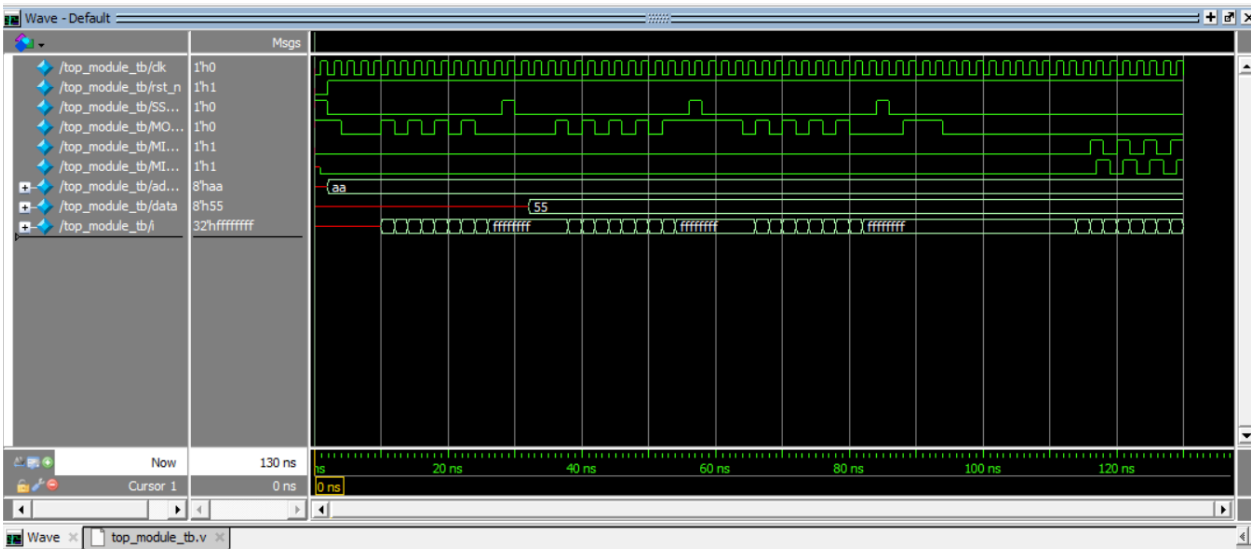
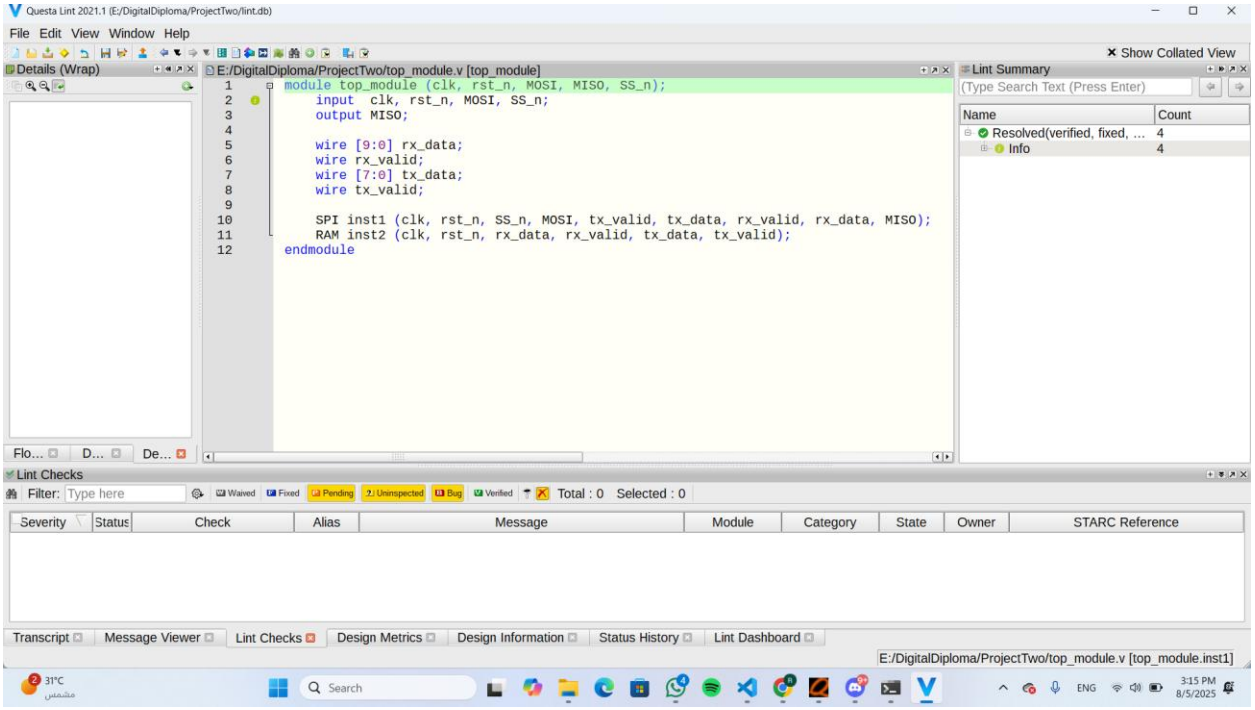
Waveform Snippets

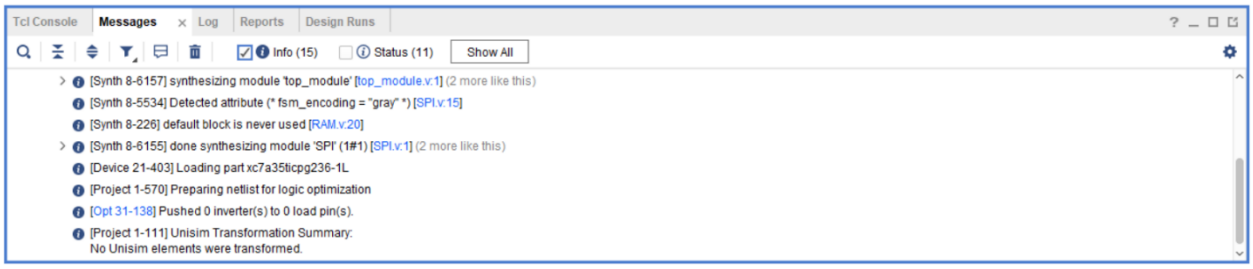


Linting Snippets

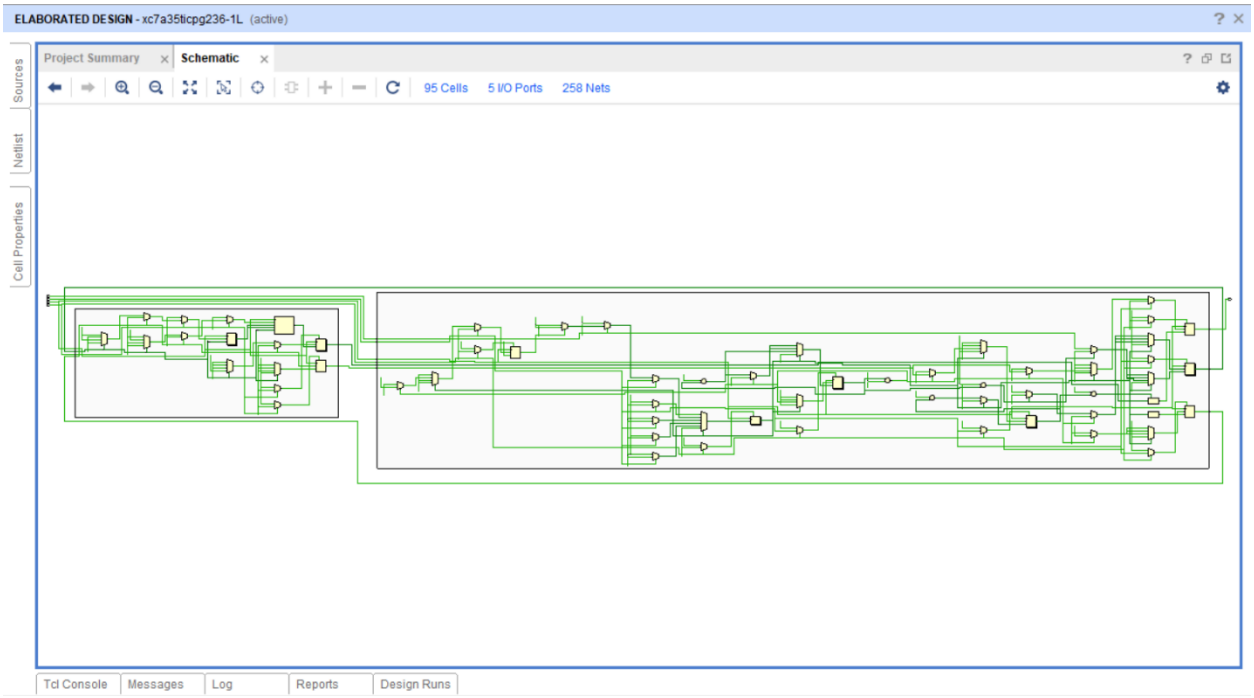


Elaboration

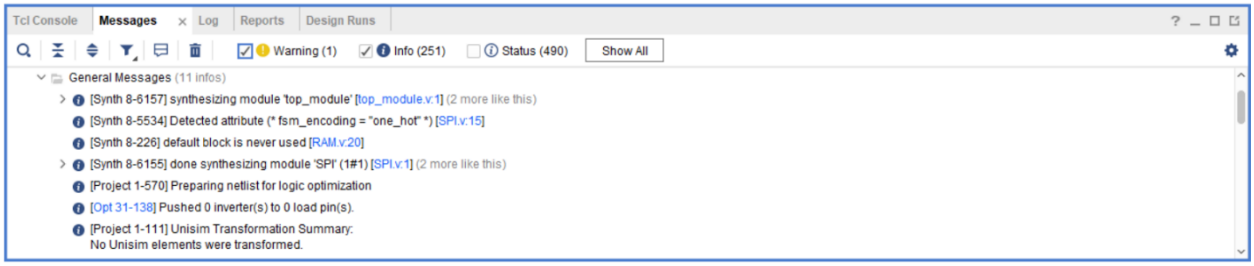
Messages for Gray Encoding



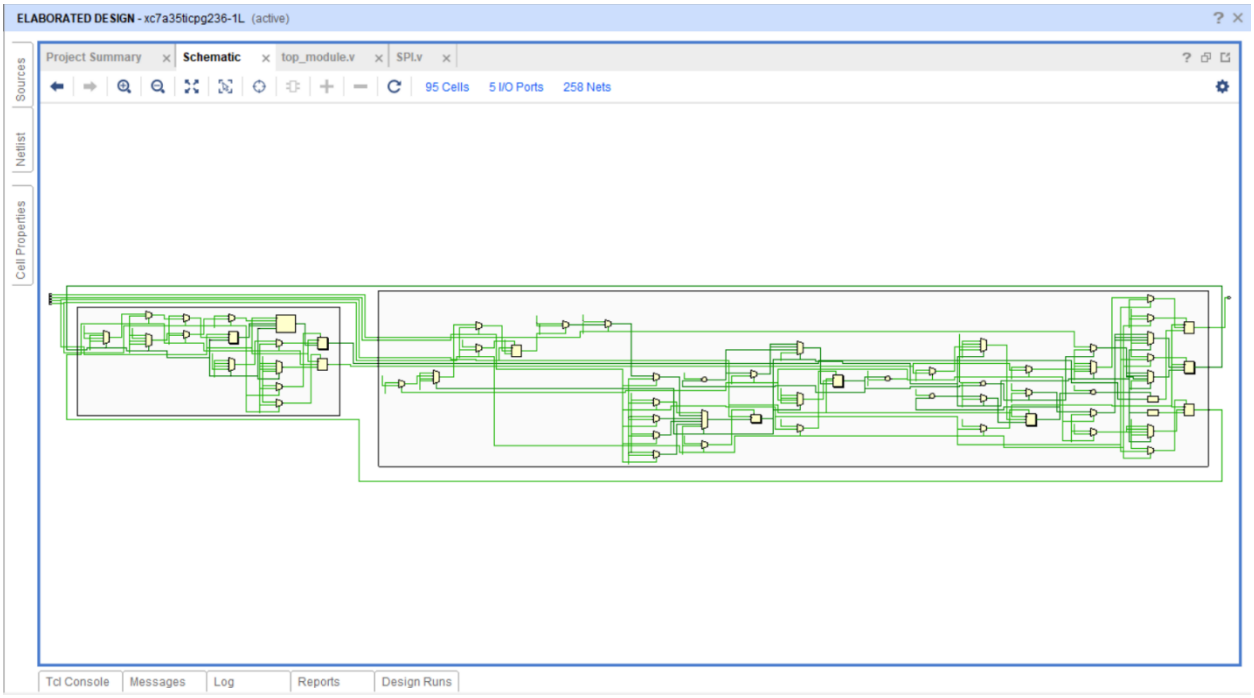
Schematic for Gray Encoding



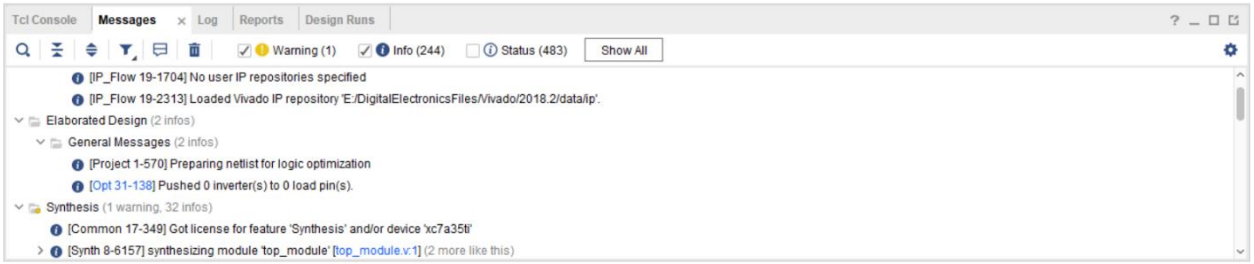
Messages for One Hot Encoding



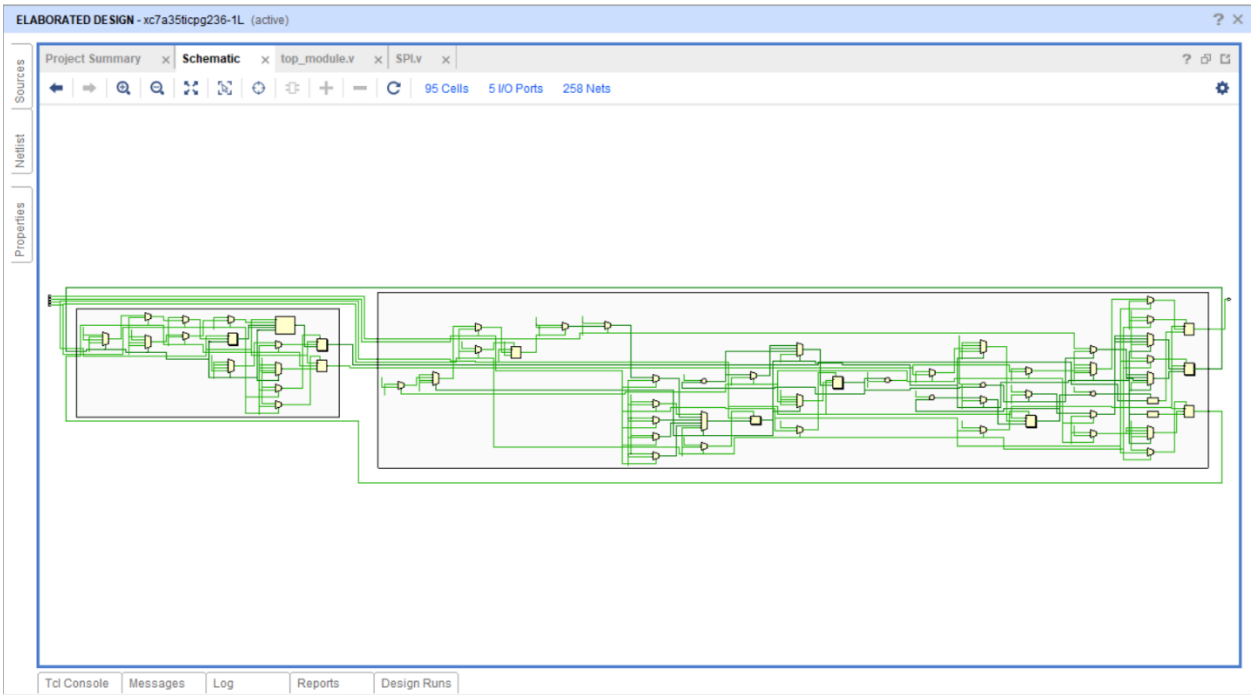
Schematic for One Hot Encoding



Messages for Sequential Encoding

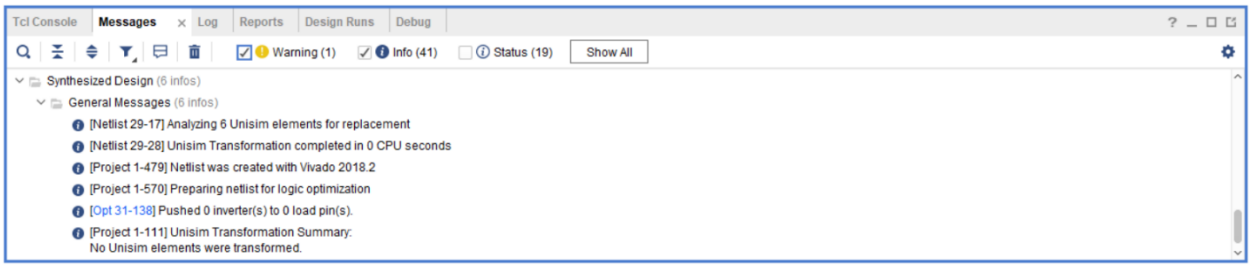


Schematic for Sequential Encoding

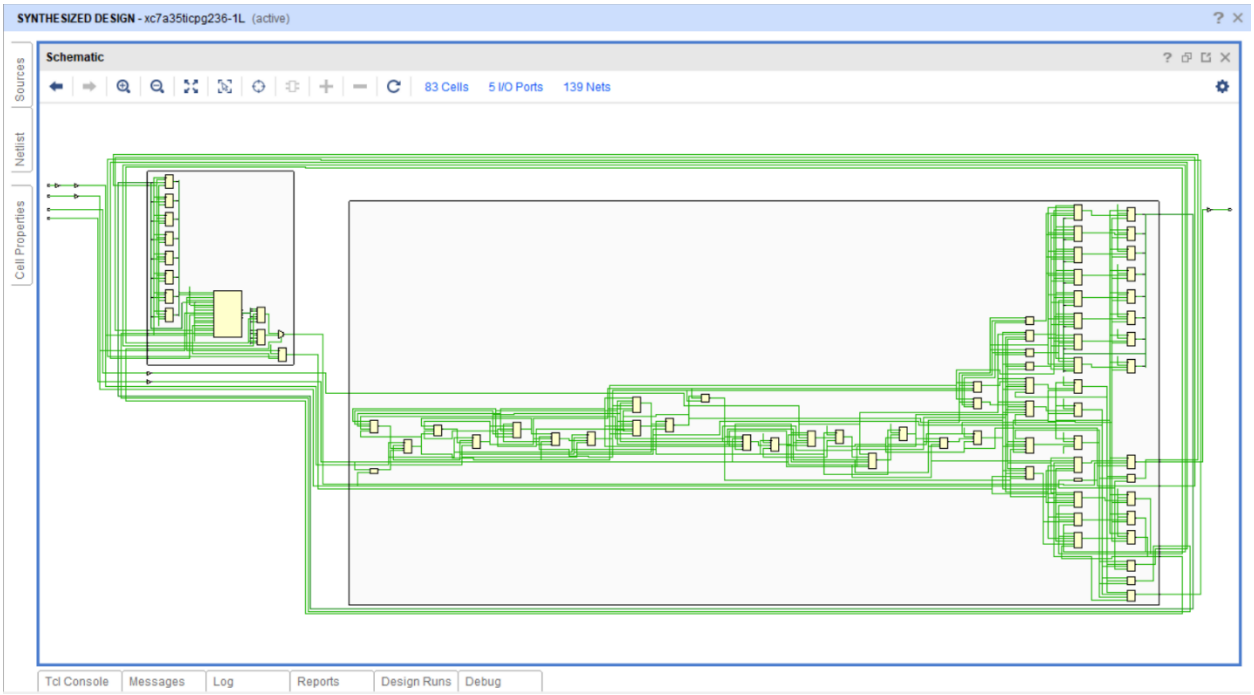


Synthesis

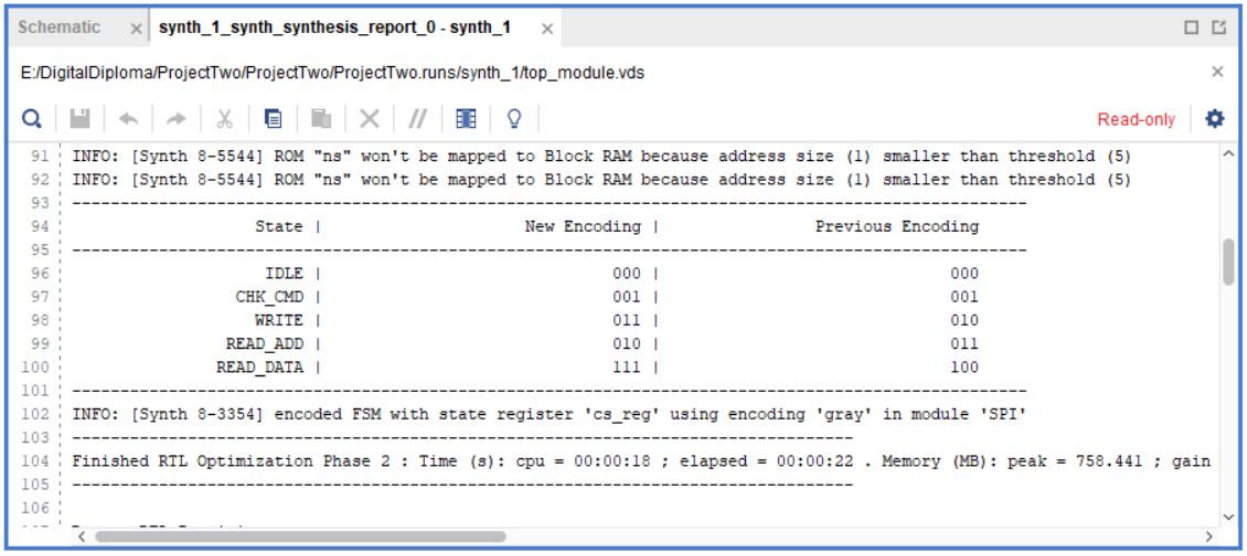
Messages for Gray Encoding



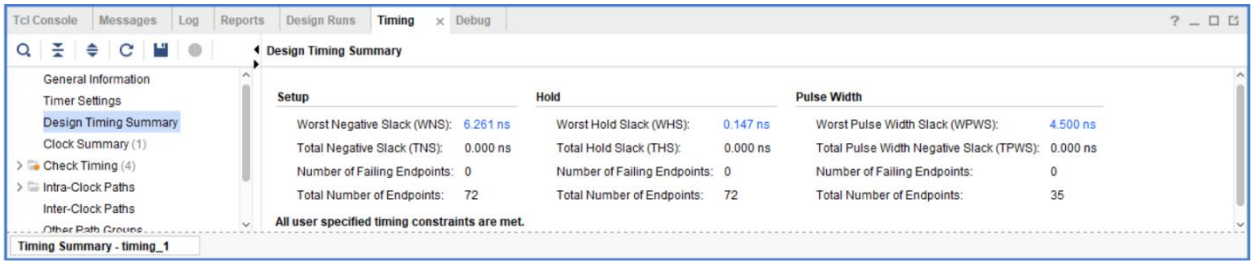
Schematic for Gray Encoding



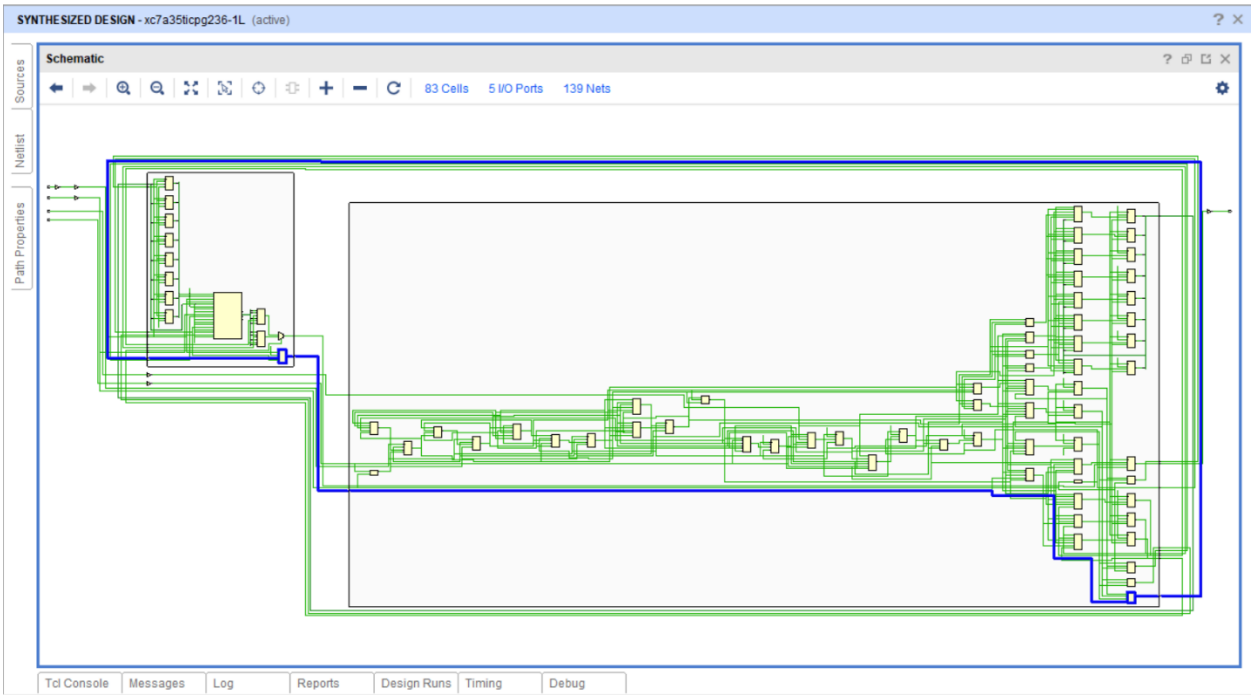
Report Showing Gray Encoding



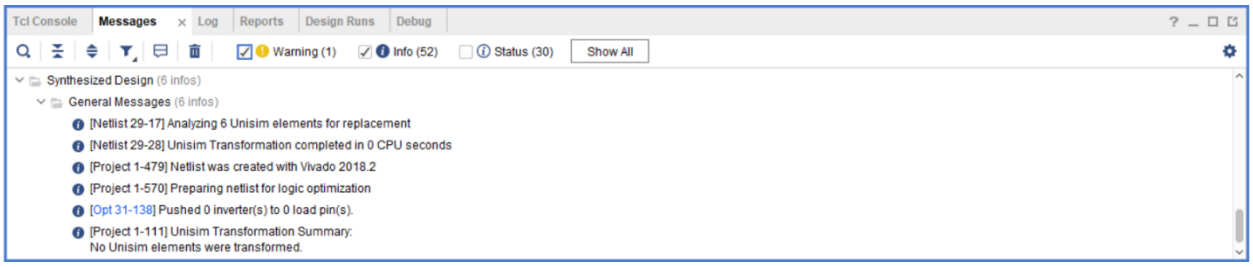
Timing Report for Gray Encoding



Critical Path for Gray Encoding



Messages for One Hot Encoding



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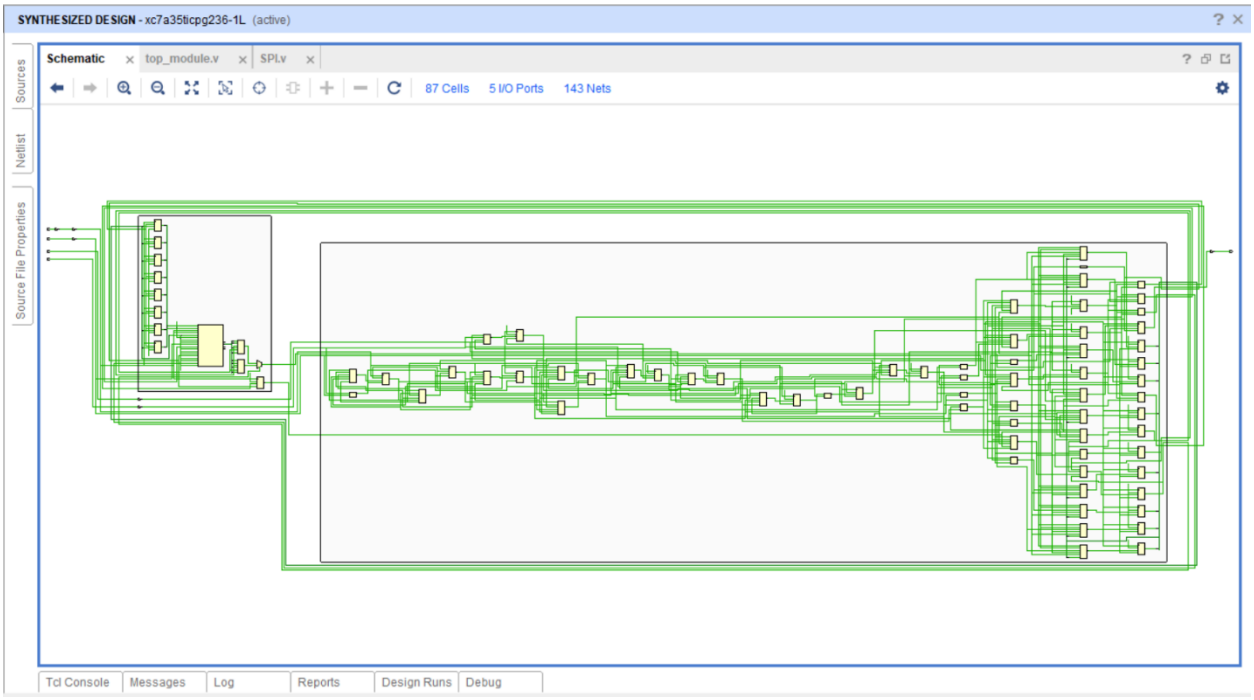
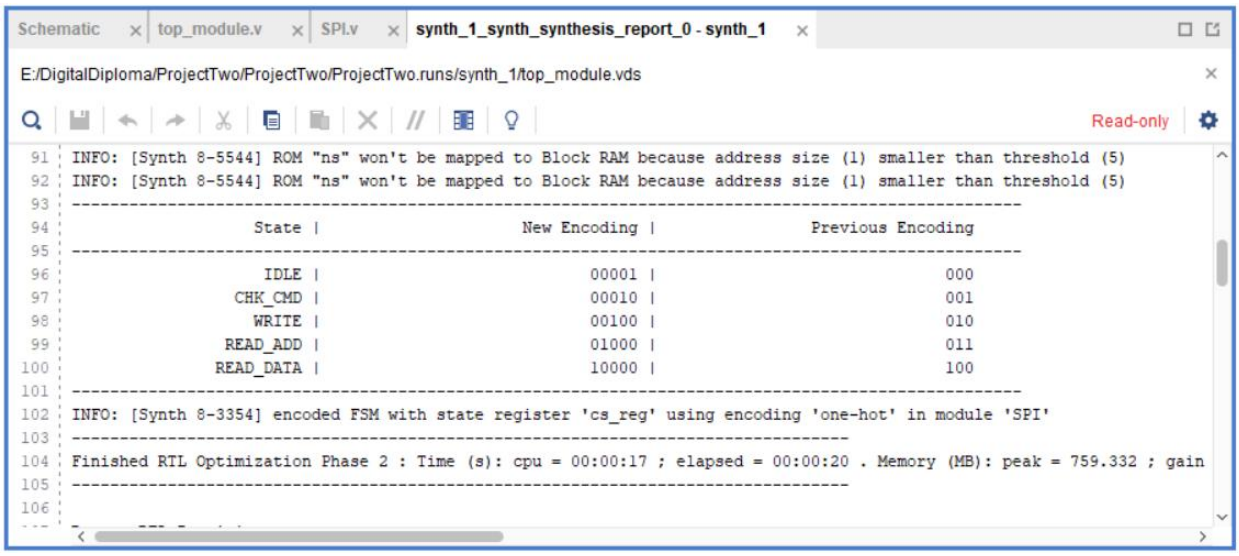
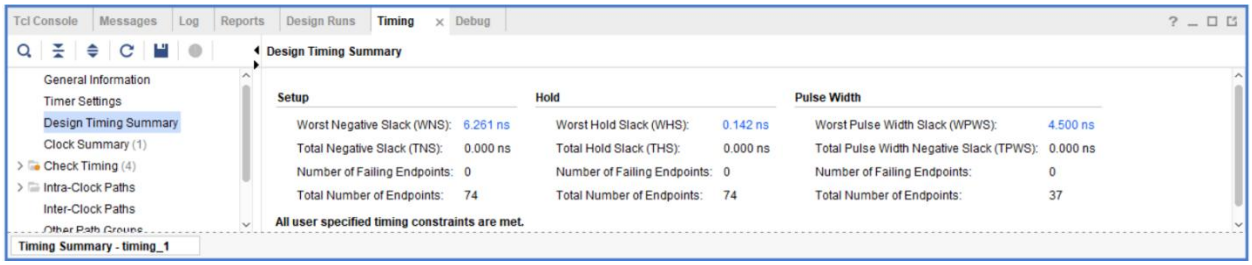


Report Showing One Hot Encoding



Timing Report for One Hot Encoding



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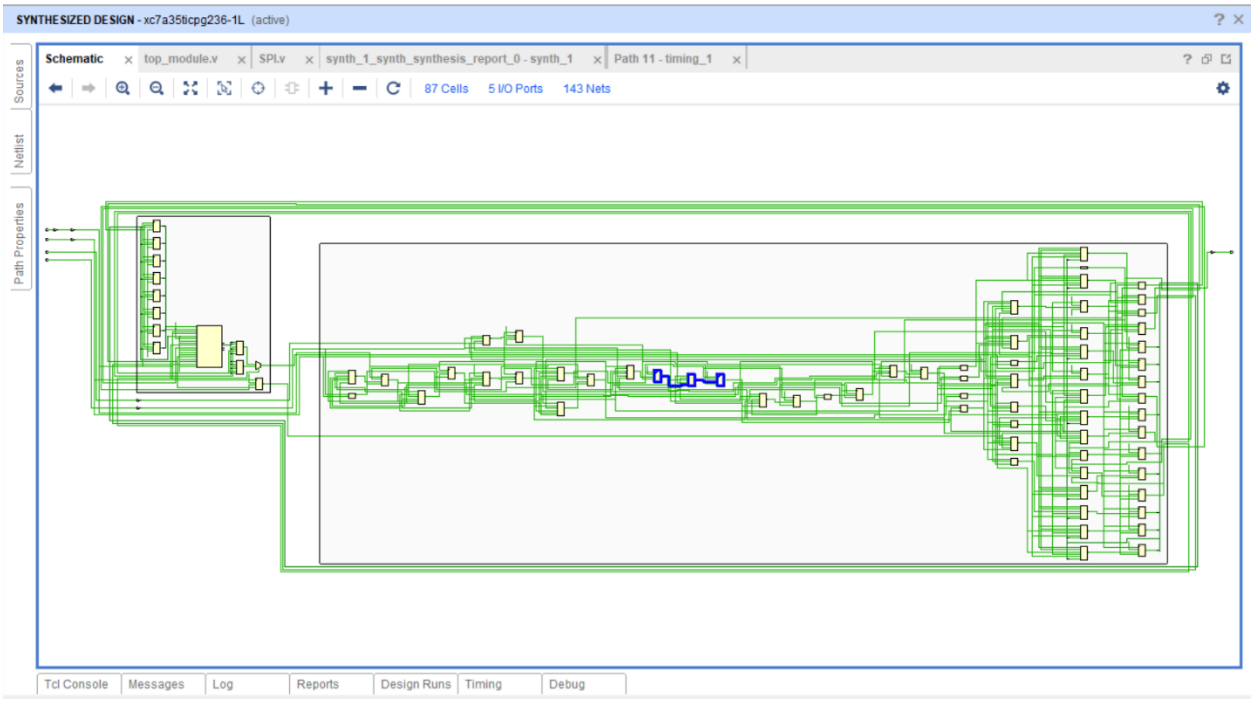
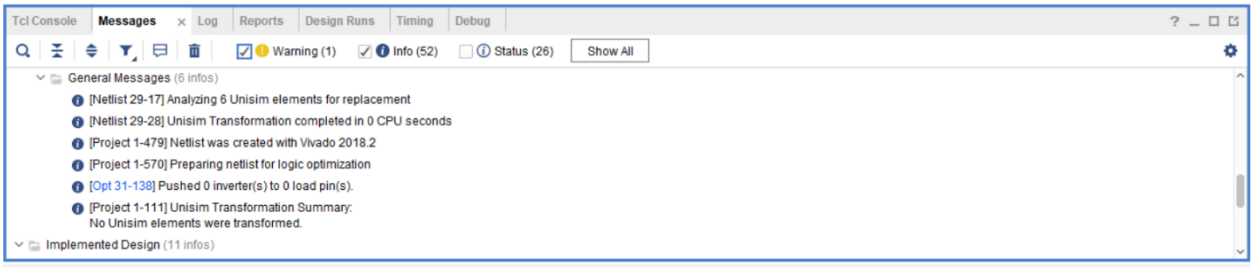
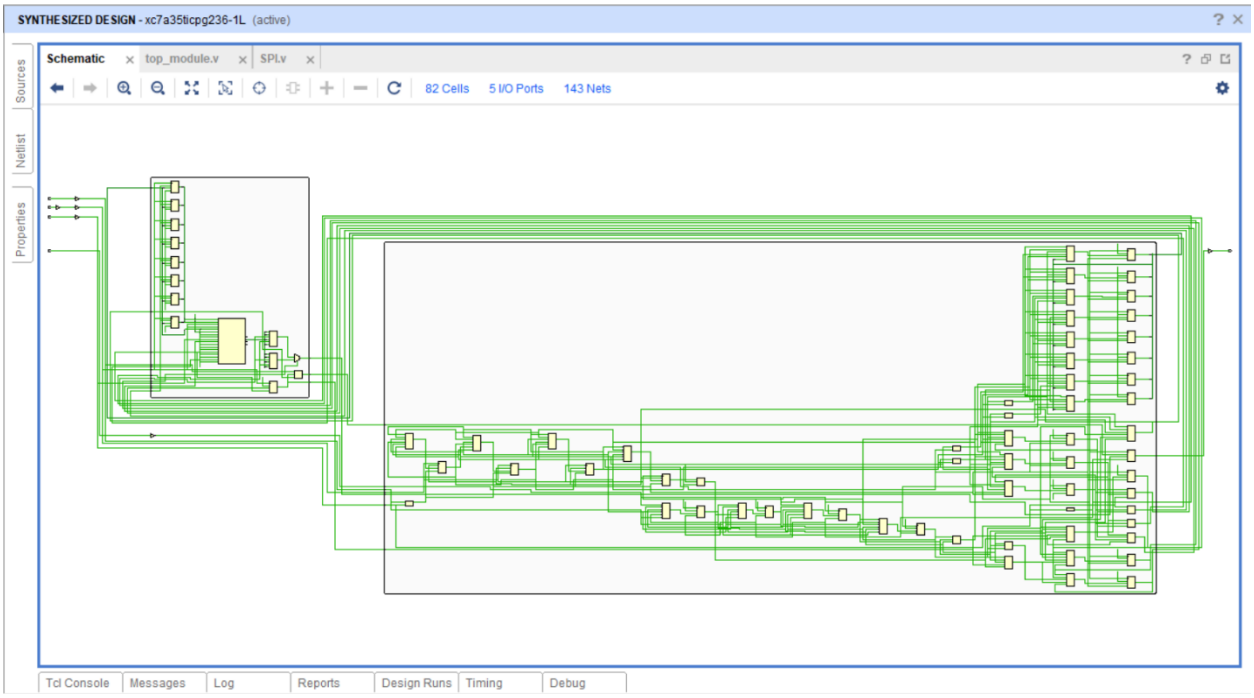


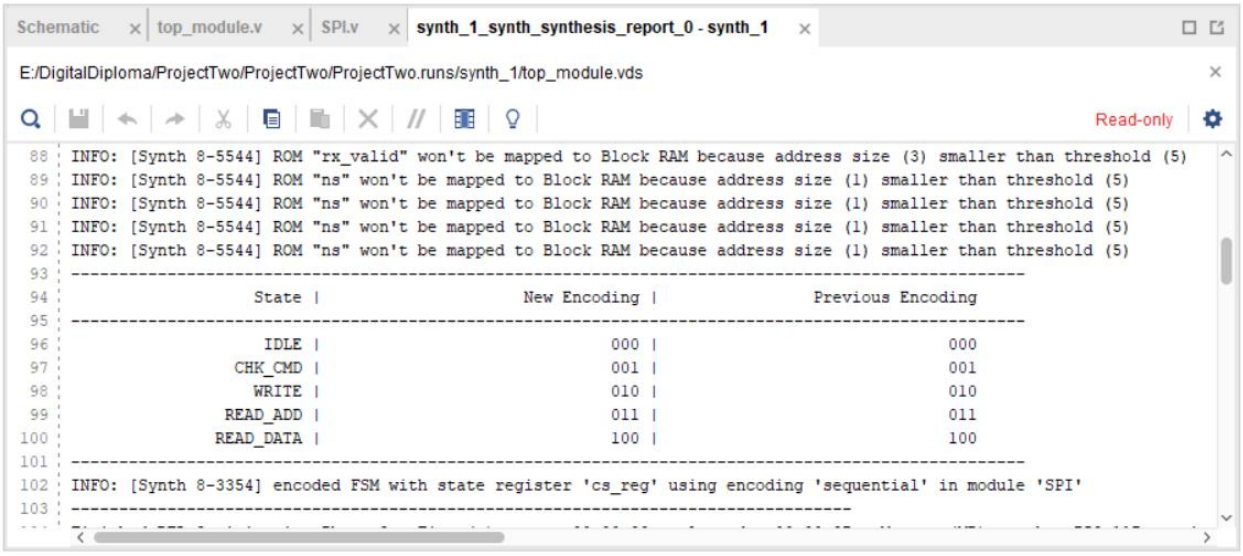
Messages for Sequential Encoding



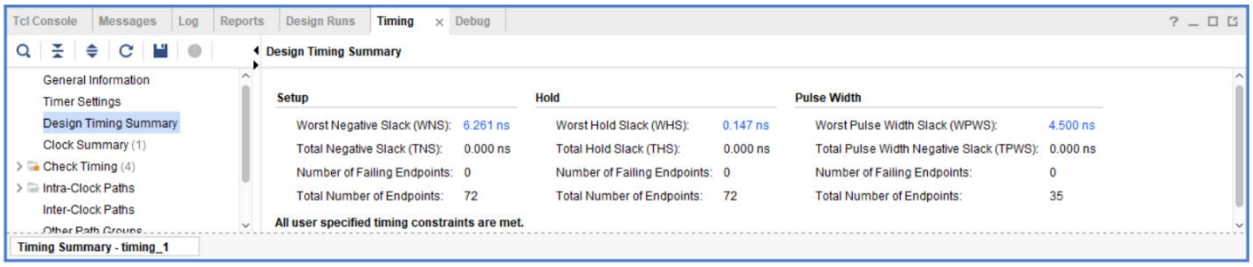
Schematic for Sequential Encoding



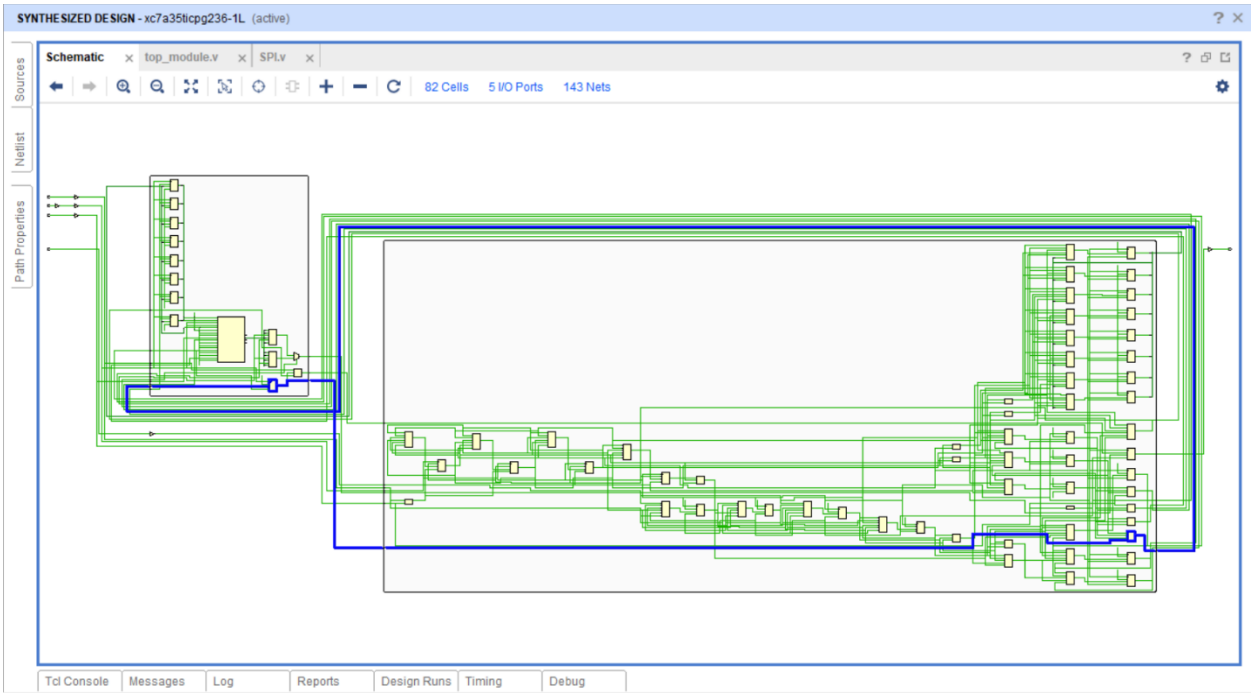
Report Showing Sequential Encoding



Timing Report for Sequential Encoding

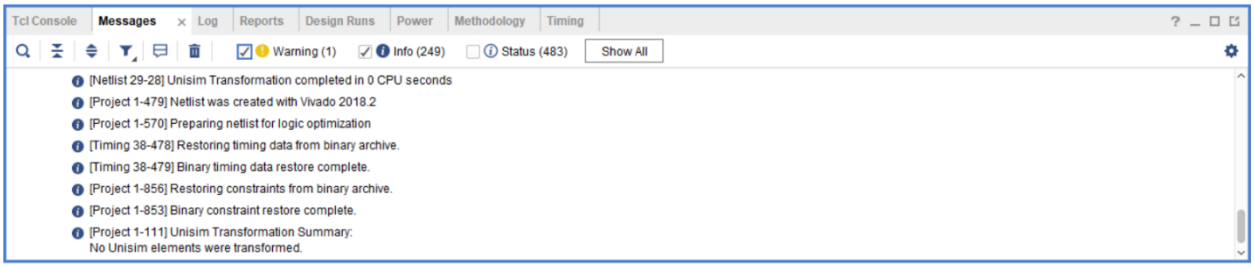


Critical Path for Sequential Encoding

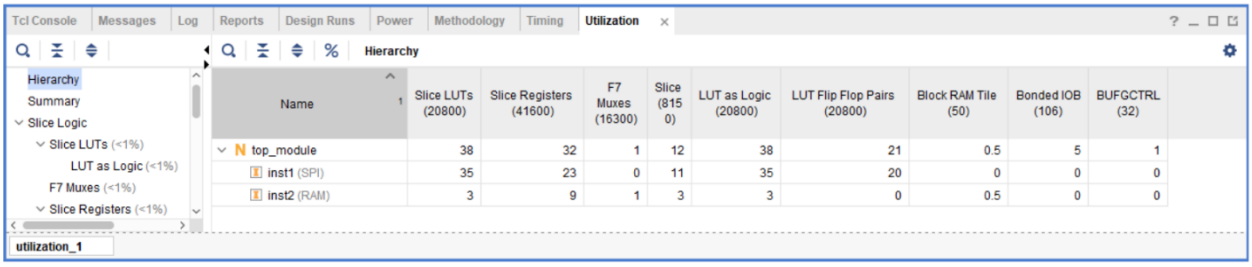


Implementation

Messages for Gray Encoding

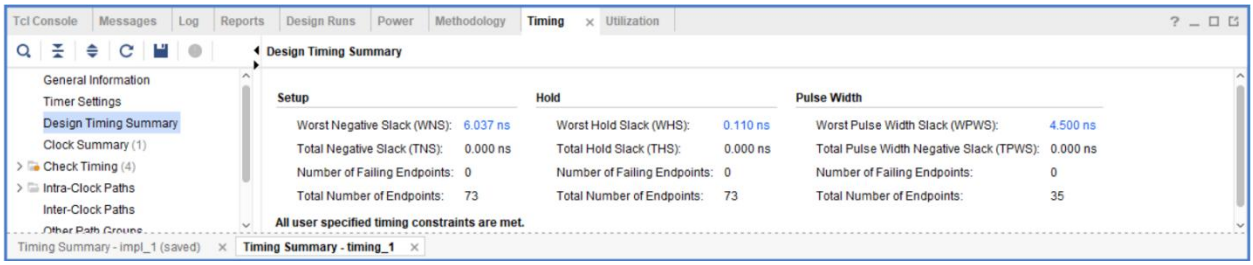


Utilization Report for Gray Encoding

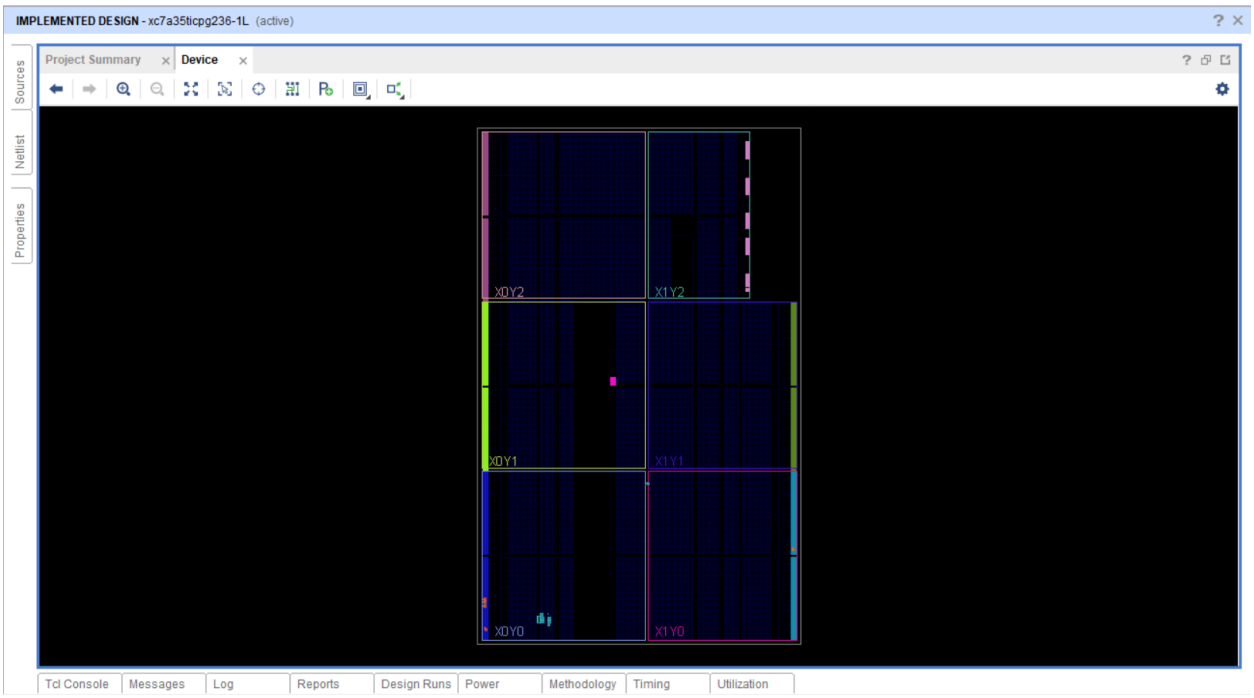


Name	Slice LUTs (20800)	Slice Registers (41600)	F7 Muxes (16300)	Slice (8150)	LUT as Logic (20800)	LUT Flip Flop Pairs (20800)	Block RAM Tile (50)	Bonded IOB (106)	BUFGCTRL (32)
top_module	38	32	1	12	38	21	0.5	5	1
inst1 (SPI)	35	23	0	11	35	20	0	0	0
inst2 (RAM)	3	9	1	3	3	0	0.5	0	0

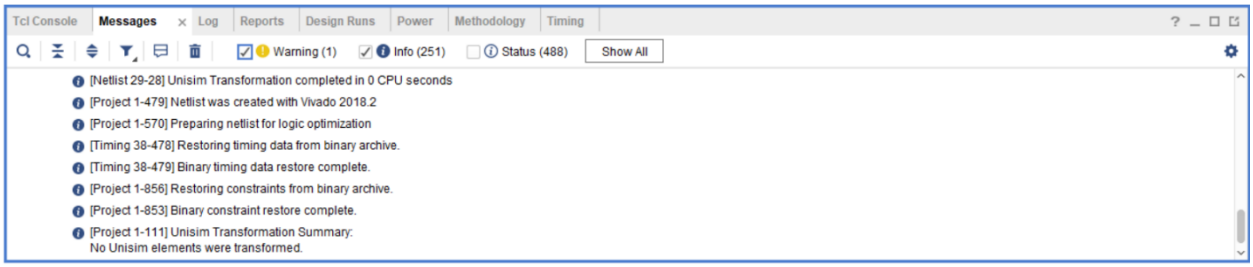
Timing Report for Gray Encoding



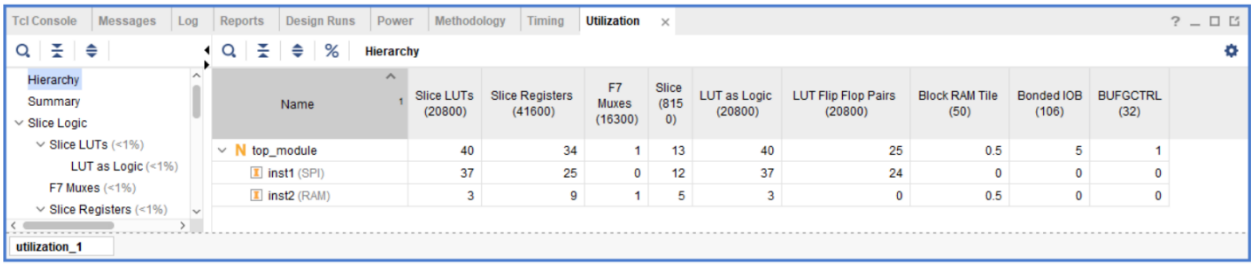
Device Snippet for Gray Encoding



Messages for One Hot Encoding



Utilization Report for One Hot Encoding



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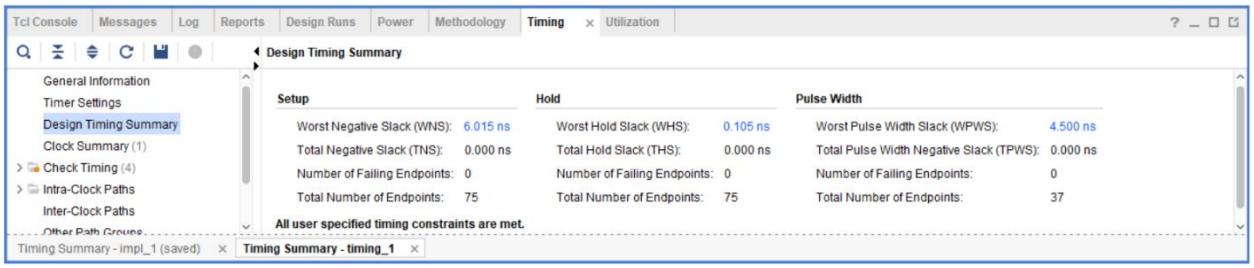
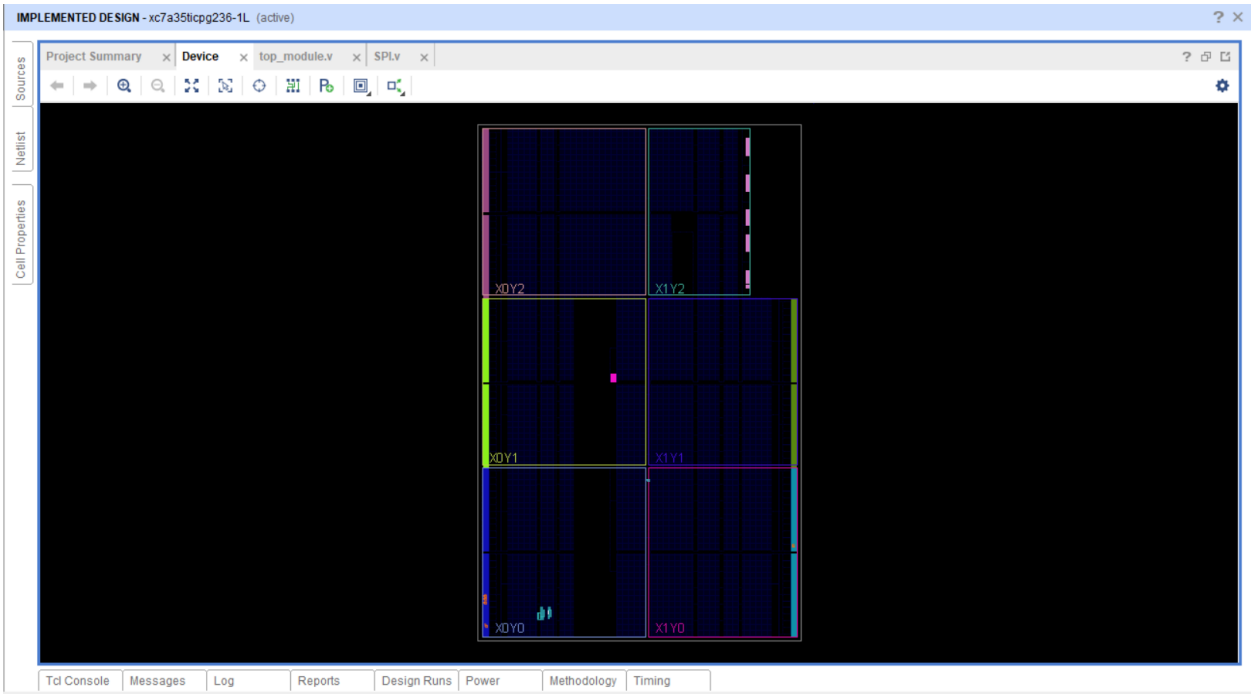
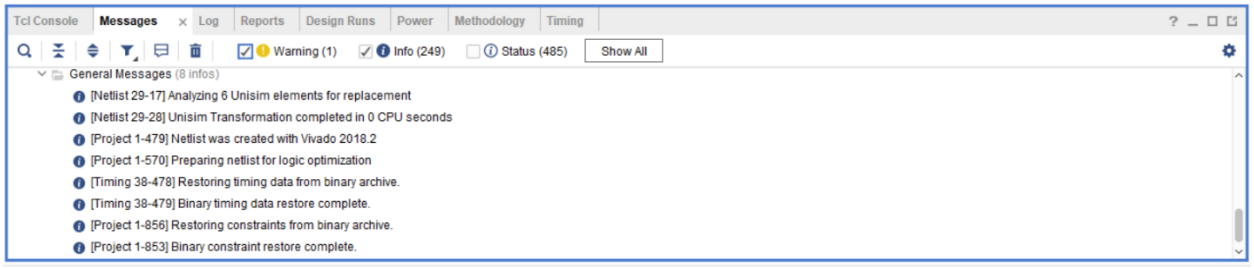


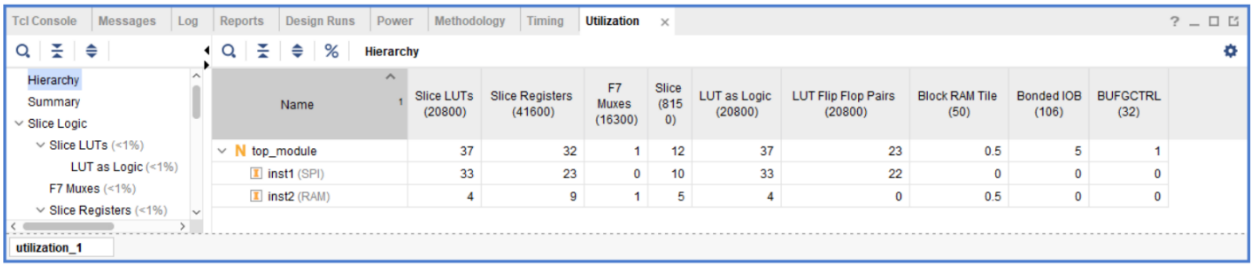
Device Snippet for One Hot Encoding



Messages for Sequential Encoding

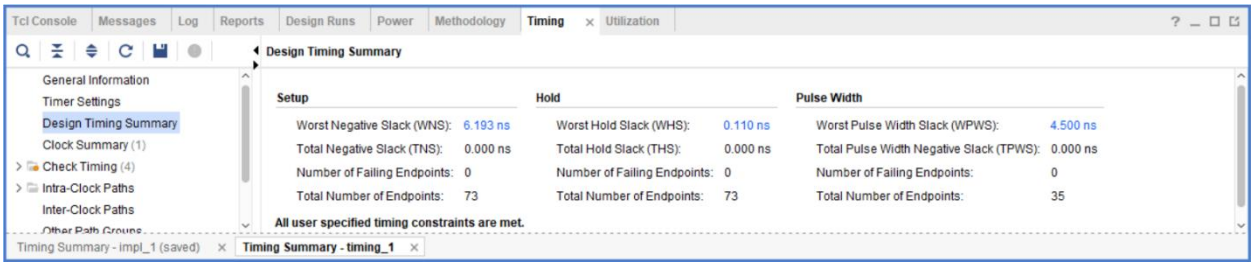


Utilization Report for Sequential Encoding

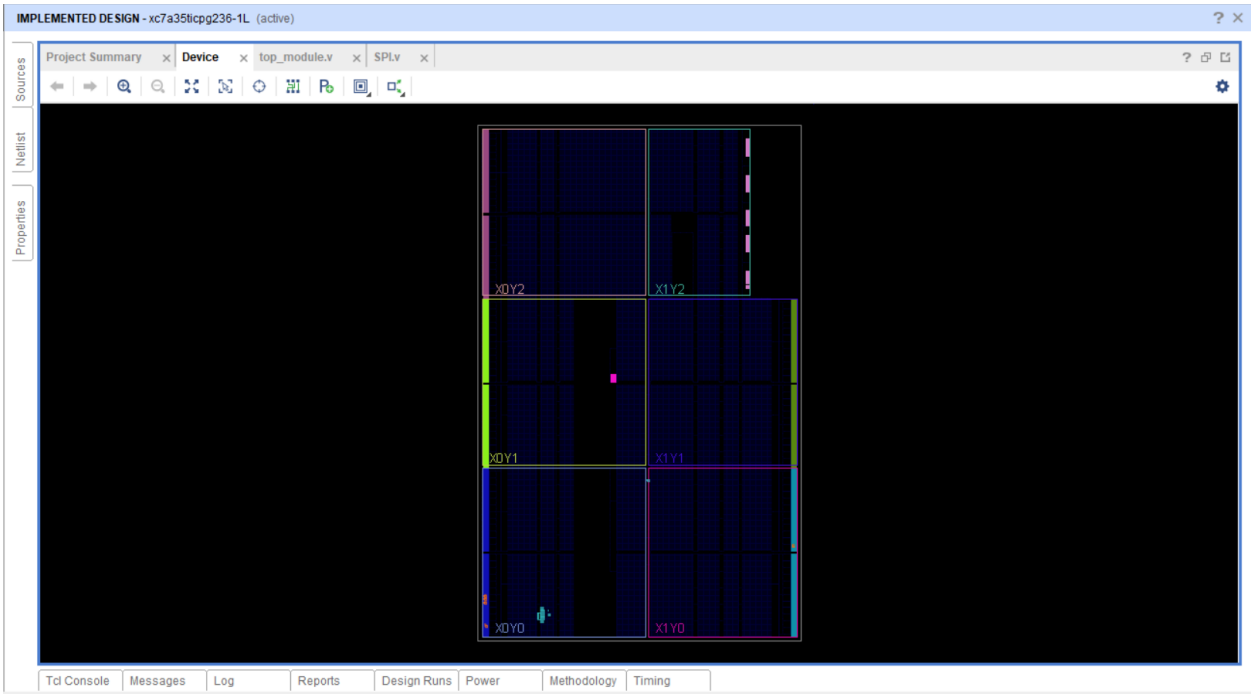


Name	Slice LUTs (20800)	Slice Registers (41600)	F7 Muxes (16300)	Slice (8150)	LUT as Logic (20800)	LUT Flip Flop Pairs (20800)	Block RAM Tile (50)	Bonded IOB (106)	BUFGCTRL (32)
top_module	37	32	1	12	37	23	0.5	5	1
inst1 (SPI)	33	23	0	10	33	22	0	0	0
inst2 (RAM)	4	9	1	5	4	0	0.5	0	0

Timing Report for Sequential Encoding



Device Snippet for Sequential Encoding



Successful Bitstream Generation (for Gray Encoding)