

Rafeul Alam

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SUMMARY OF QUALIFICATIONS

- Constructed a collection of research and documentation of electrical devices for system integration and development for high data volume applications at Air Force Research Laboratory (AFRL)
- Performed a critical evaluation on supplier compliance for a competitive multi-million dollar proposal for Northrop Grumman Aerospace Systems
- Persuaded at least 100 potential students daily to join The Art Institute as a National Admissions Representative for Education Management Corporation
- Developed two responsive web pages (<http://rafeexgit.github.io> & <http://project5w1h.us>) using HTML5, CSS and JavaScript from designs inspired by html5up.net

PROFESSIONAL EXPERIENCE

College Technical Intern (B-2 Avionics Electronics & Payload)

June-Aug. 2017

Northrop Grumman Corporation

Palmdale, California

- Analyzed and grouped system requirements to cross reference with supplier compliance in order to conduct a technical evaluation to award a multi-million dollar contract from a competitive proposal bid
- Developed a fully formulated Excel tracking system so that the responsible engineer and subcontract administrator can maintain deadlines, easily view approaching incomplete proposals and remain accountable in communication throughout a proposal process

Phillips Scholars Program (Internship)

May-Aug. 2016

Air Force Research Laboratory

Kirtland AFB | Albuquerque, NM

- Operated as local Electrical Engineer subject-matter expert for overall project in the Space Vehicles branch
- Presented a poster of the compiled research on FPGA implementations and characterizations among fellow scholars and AFRL research scientists/engineers
- Joined weekly lectures by AFRL scientists/engineers, team project meetings and teleconferences with Space Dynamics Laboratory (Utah)

ENGINEERING PROJECTS

Rear-End Collision System (Senior Design Project)

Aug. 2016-May 2017

- Designed a motorcycle accessory device in order to warn riders of a potential collision/giving awareness in their blind spot
- Generated market research, technology research and wrote proposals for initial phase of project
- Modeled the 3D printed encasing using Google SketchUp and Adobe Fusion 360 software for electrical components of device

Internet-of-Things (IoT) based Pet Care System

Aug.-Dec. 2016

- Developed an embedded IoT based system that will function as an automatic pet feeder to promote proper pet health care and pet owner engagement
- Modeled the system incorporating Samsung's ARTIK 10 developer board and additional hardware components

FPGA Development for High-Data Volume Applications

May-Aug. 2016

- Developed theoretical background research for a project concerning high-data volumes with FPGA implementations
- Composed research and data into an internal report on FPGA implementations for future experimental work

EDUCATION

Arizona State University (ASU), Tempe, AZ

Graduated: May 2017

BSE in Electrical Engineering

GPA: 3.55

Relevant Course:

- Entrepreneurship and Value Creation
- Hardware Language/Programmable Logic
- Communication Networks

Academic Awards:

- Cum Laude honors award
- Dean's List for seven semesters
- Provost Scholarship

TECHNICAL SKILLS

- Programming Languages: HTML5/CSS, Python, Terminal/Linux, VHDL, MIPS Assembly, C++, Java, Visual Basic (VBA)
- Software Experience: Cadence6 & Cadence5, Logic Works 5, PSpice, Matlab, Xilinx Design Suites, Excel (Data Analysis), BIOS
- Electrical Equipment: IC chips, Oscilloscopes, FPGA boards, ARTIK 10, Arduino, Raspberry Pi Model 3, ANYS HFSS

EXTRACURRICULAR ACTIVITIES

ECEE Student Mentor

Aug. 2015-May 2017

- Volunteer to help promote the School of Electrical, Computer, and Energy Engineering & mentor freshman/new students to a greater success