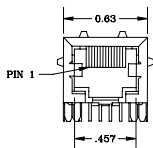


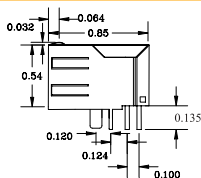
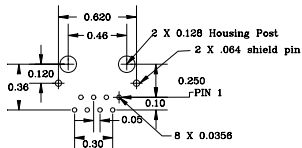
- Unique layout conserves precious circuit board space
- Designed for HUB applications
- IEEE 802.3u
- Compliant with a wide assortment of ICs
- Primary center taps available for additional EMI reduction



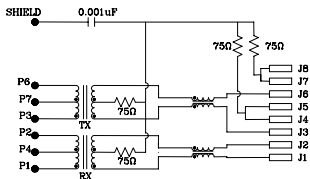
MECHANICAL AND SCHEMATIC (All dimensions in inches)



PCB LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATION @ 25°C

Part Number	Inductance µH min 8mA DC 0 - 70°C	Return Loss dB min			CMRR (TX/RX)		Crosstalk dB min		Voltage Isolation 10 mA, 1 min. (Vrms)	Turns Ratio	
		(J1-J2) & (J3-J6)	1 - 30 MHz	30 - 60 MHz	60 - 80 MHz	1 - 60 MHz	60 - 100 MHz	1 - 60 MHz	60 - 100 MHz		
8305	350	16	16 - 20 log (FS) MHz	10	45 / 40	40 / 35	40	38	1500	1CT:1CT	1CT:1CT
8306	350	16	16 - 20 log (FS) MHz	10	45 / 40	40 / 35	40	38	1500	1.414CT:1CT	1CT:1CT
8307	350	16	16 - 20 log (FS) MHz	10	45 / 40	40 / 35	40	38	1500	2CT:1CT	1CT:1CT
8308	350	16	16 - 20 log (FS) MHz	10	45 / 40	40 / 35	40	38	1500	1.25CT:1CT	1CT:1CT
8309	350	16	16 - 20 log (FS) MHz	10	45 / 40	40 / 35	40	38	1500	1CT:1.414CT	1CT:1CT
8310	350	16	16 - 20 log (FS) MHz	10	45 / 40	40 / 35	40	38	1500	1CT:1.25CT	1CT:1CT
8311	350	16	16 - 20 log (FS) MHz	10	45 / 40	40 / 35	40	38	1500	1CT:2CT	1CT:1CT