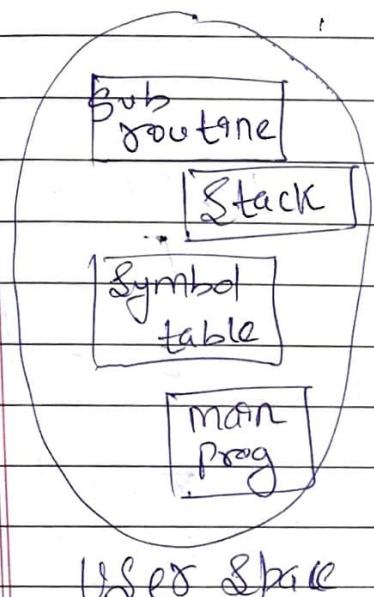


## # Segmentation —

- A Program is a collection of Segments
- A Segment is a logical unit such as
  - main program
  - procedure
  - function
  - local var., global var



⇒ Specifies each address by 2 qualities

- (a) Segment name
- (b) Segment offset

logical address ⇒ <Segment #, offset>

## # Segmentation Architecture —

- Logical add. contain 2 tuples. <Seg no., offset>
- Segment table — maps 2 dimensional Physical address —
  - base% — Contains the starting physical add.
  - limit% — Specifies the length of Seg.

Segment table

Base	limit
addr. of seg.	length of seg.

## \* Segment table base register (STBR) —

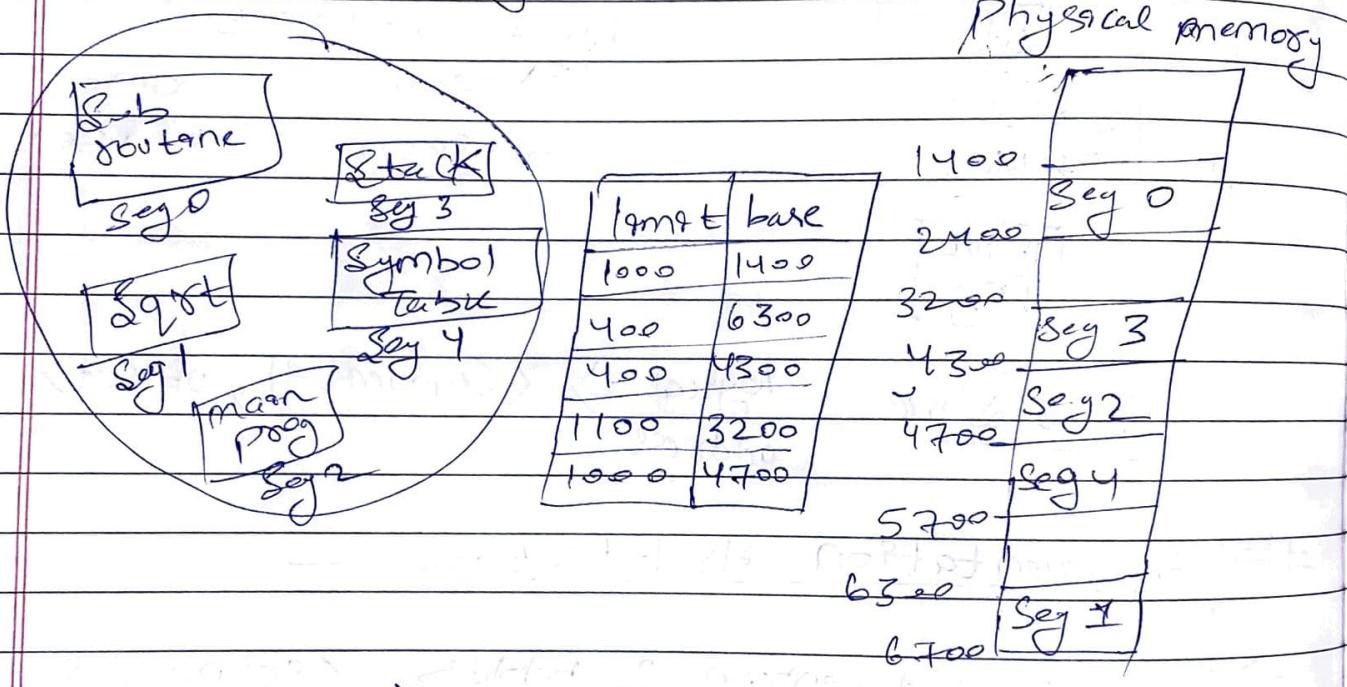
Points to the Seg. tables location in memory

## \* Segment table length registers (STLR) —

Indicates no. of segments used by Prog.

- Seg no. & is legal if  $S \leq STLR$ .

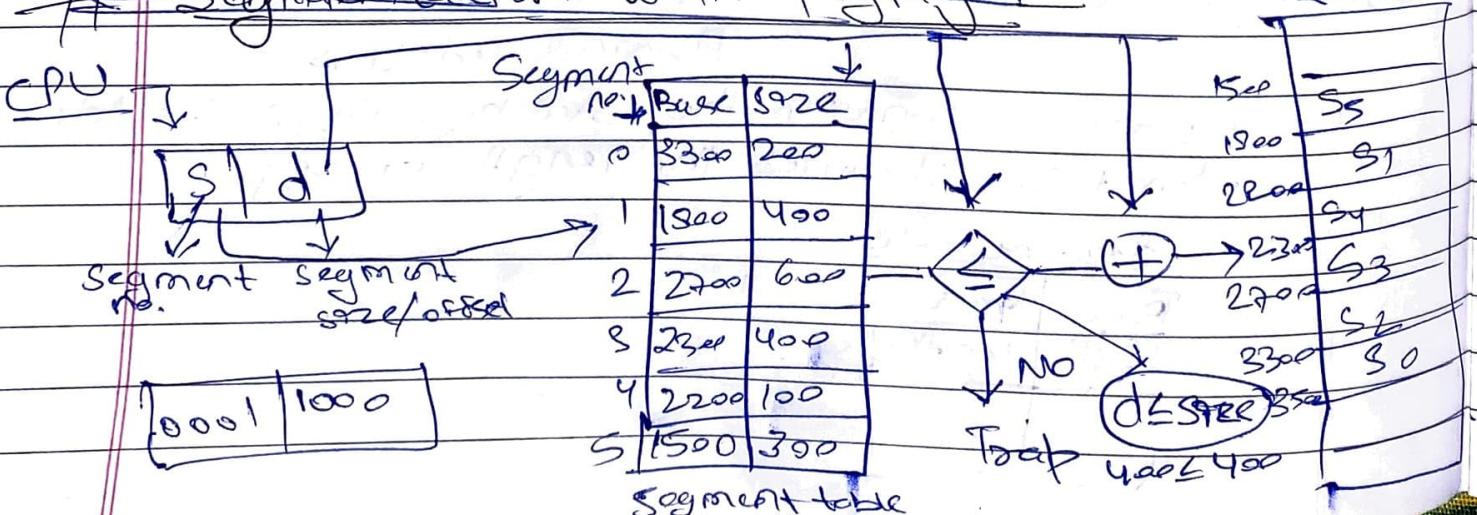
## Example of Segmentation —



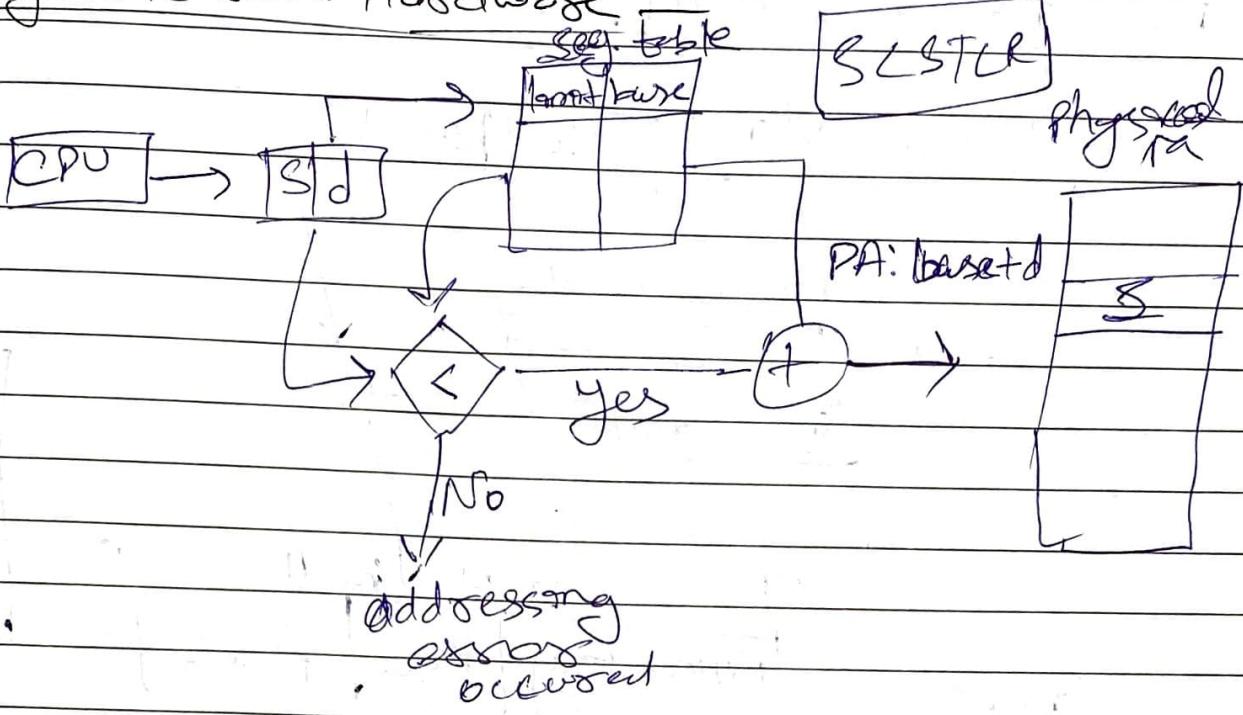
(late binding)

## Segmentation with paging

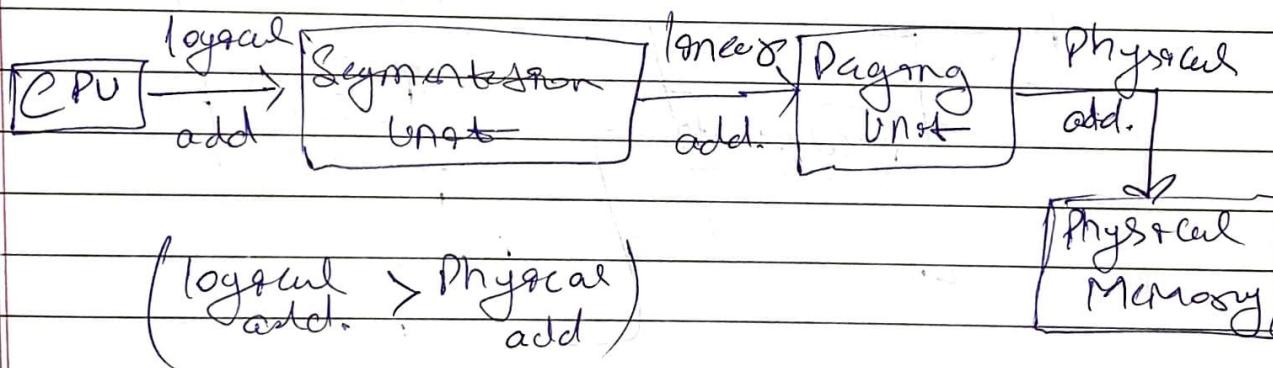
M.M.



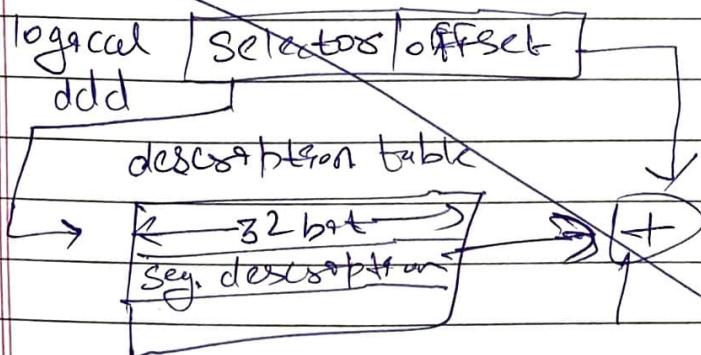
## \* Segmentation hardware



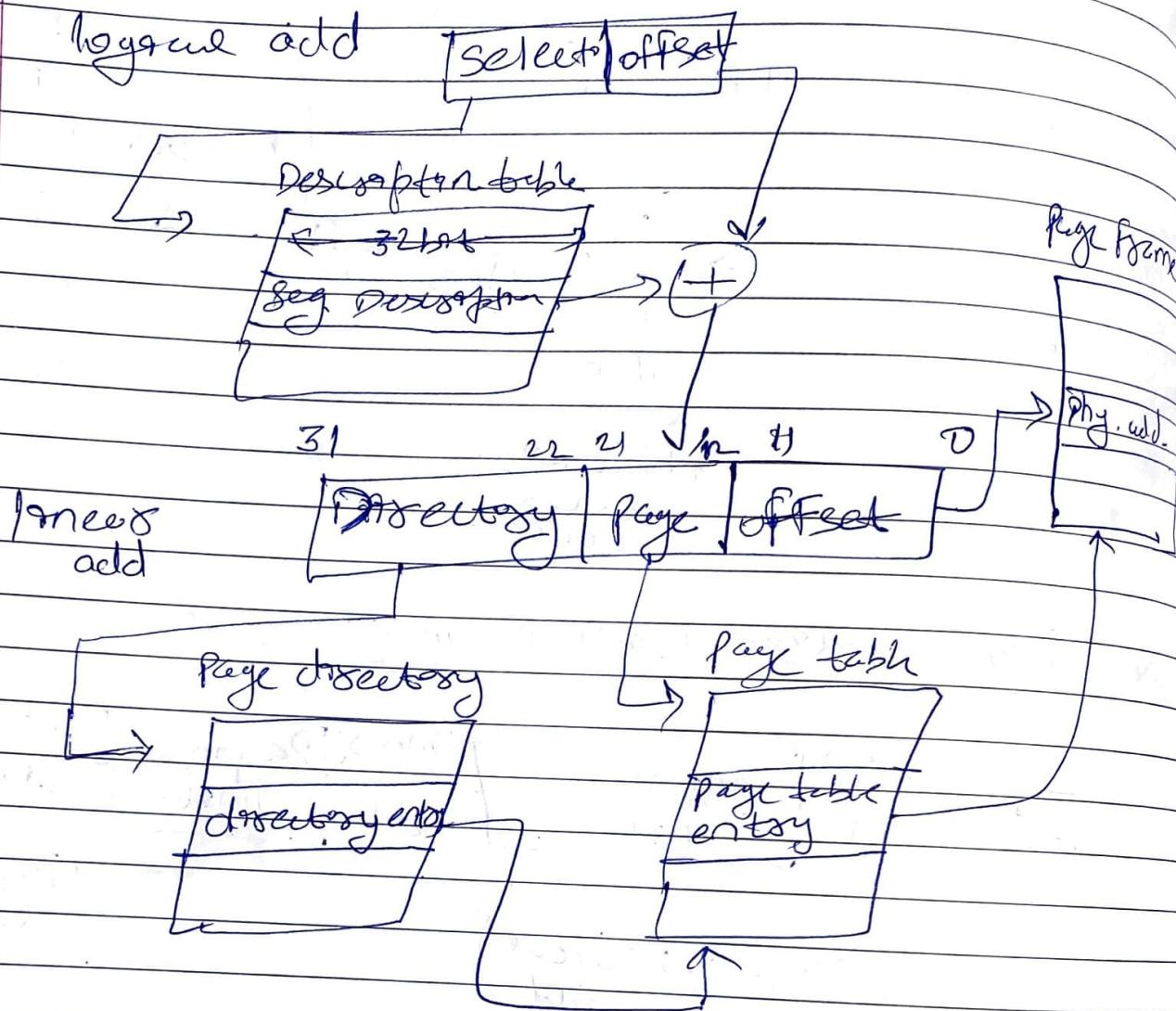
## \* Segmentation with Paging



~~Ex - Intel Pentium,~~

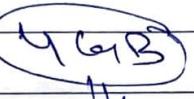
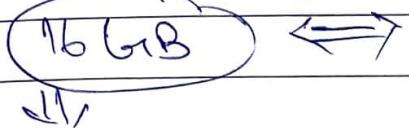


Ex intel pentium



## # Virtual Memory ←

~~Sign which appears~~



real memory  
of computer

- A programmer can write a prog. which requires more memory than the capacity of M.M. Such a prog. is executed by V.M. Technique.
- logical add. space can therefore be much larger than physical add. space.
- Allows add. spaces to be shared by several processes.
- more prog. running concurrently
- less I/O needed to load or swap processes.

physical memory

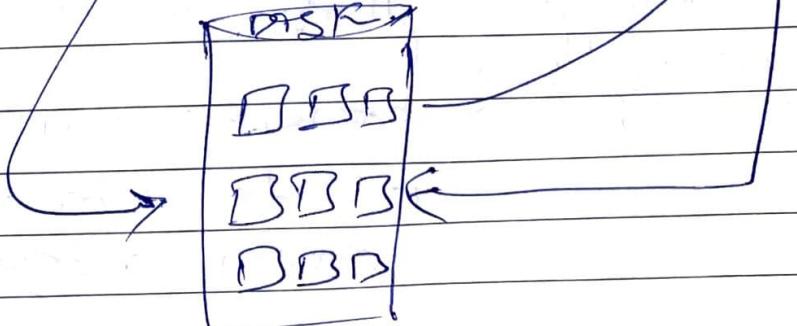
V.M



Memory map



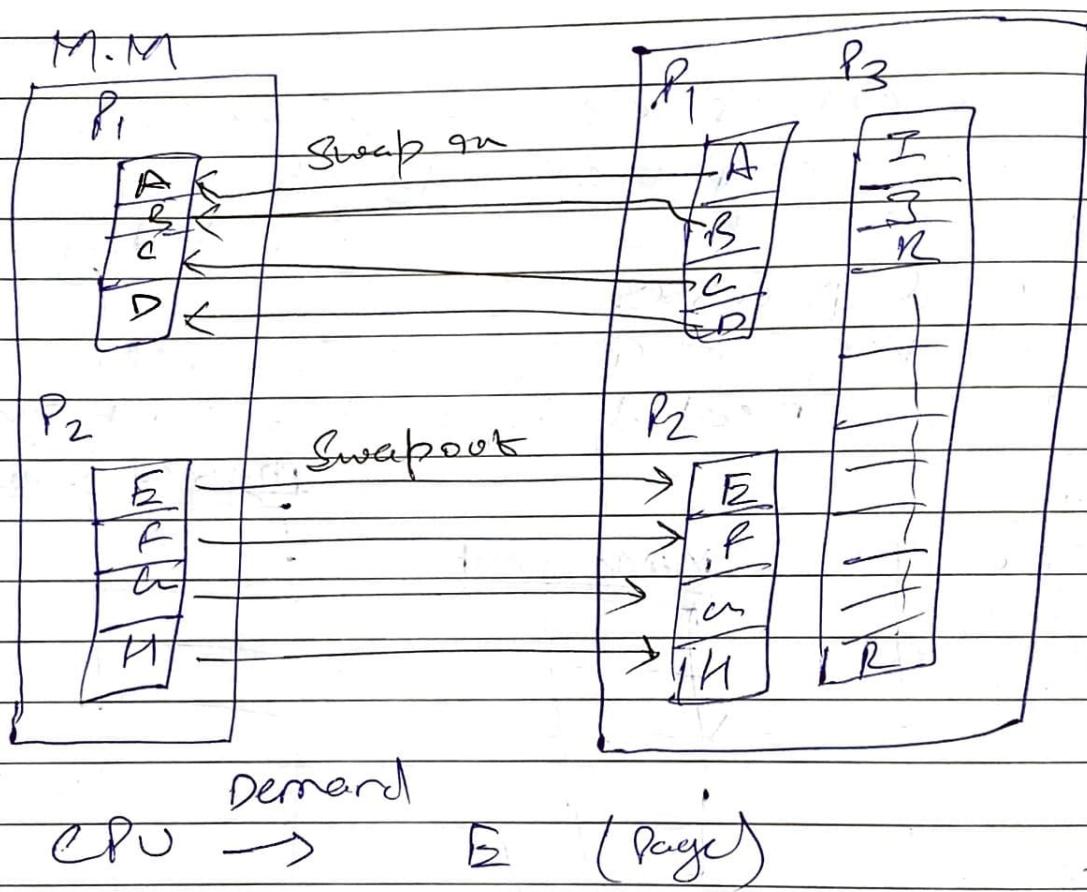
VM cont



- V.M. can be implemented via —
  - Demand Paging
  - Demand Segmentation

### \* Demand Paging —

The processes reside in Secondary memory  
 & Pages are loaded only on demand not in advance

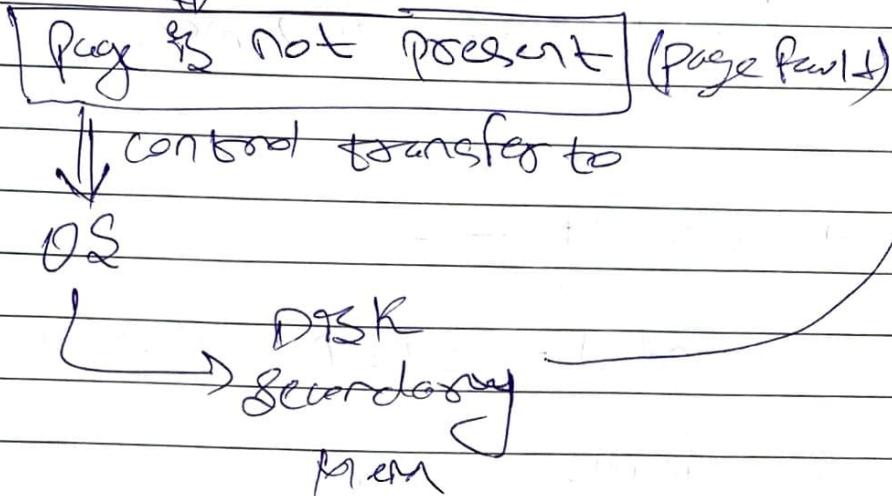


If the page is not present on M.M.  
 then we call it as Page fault.

~~How to handle it?~~

~~CPU → transfers control from Page to OS to demand the page back into memory.~~

CPU → request for page in M.RA ←



## # Performance of Demand Paging —

- ⇒ Page fault rate  $0 \leq P \leq 1.0$
- if  $P = 0$ , no page fault
- if  $P = 1$ , every reference is a fault

## ⇒ Effective Access Time (EAT)

$$\begin{aligned}
 EAT = & (1 - P) \times \text{Memory Access} \\
 & + P \left( \begin{array}{l} \text{Page fault overhead} \\ + \\ \text{Swap page out} \\ + \\ \text{Swap page in} \\ + \\ \text{restart overhead} \end{array} \right)
 \end{aligned}$$

Ex Memory access time = 200 nano sec  
 Avg. page fault Service time = 8 mil sec

$$\begin{aligned}
 EAT = & (1 - P) \times 200 + P(8 \text{ mil sec}) \\
 = & 200 - 200P + 8000000P \\
 = & 200 + 7999800P
 \end{aligned}$$

If one access out of 1000 causes a page fault,

$$BAT = 8.2 \text{ msec}$$

## Page fault -

Demanding page is not present in M-M.

Page fault can be handled by OS:

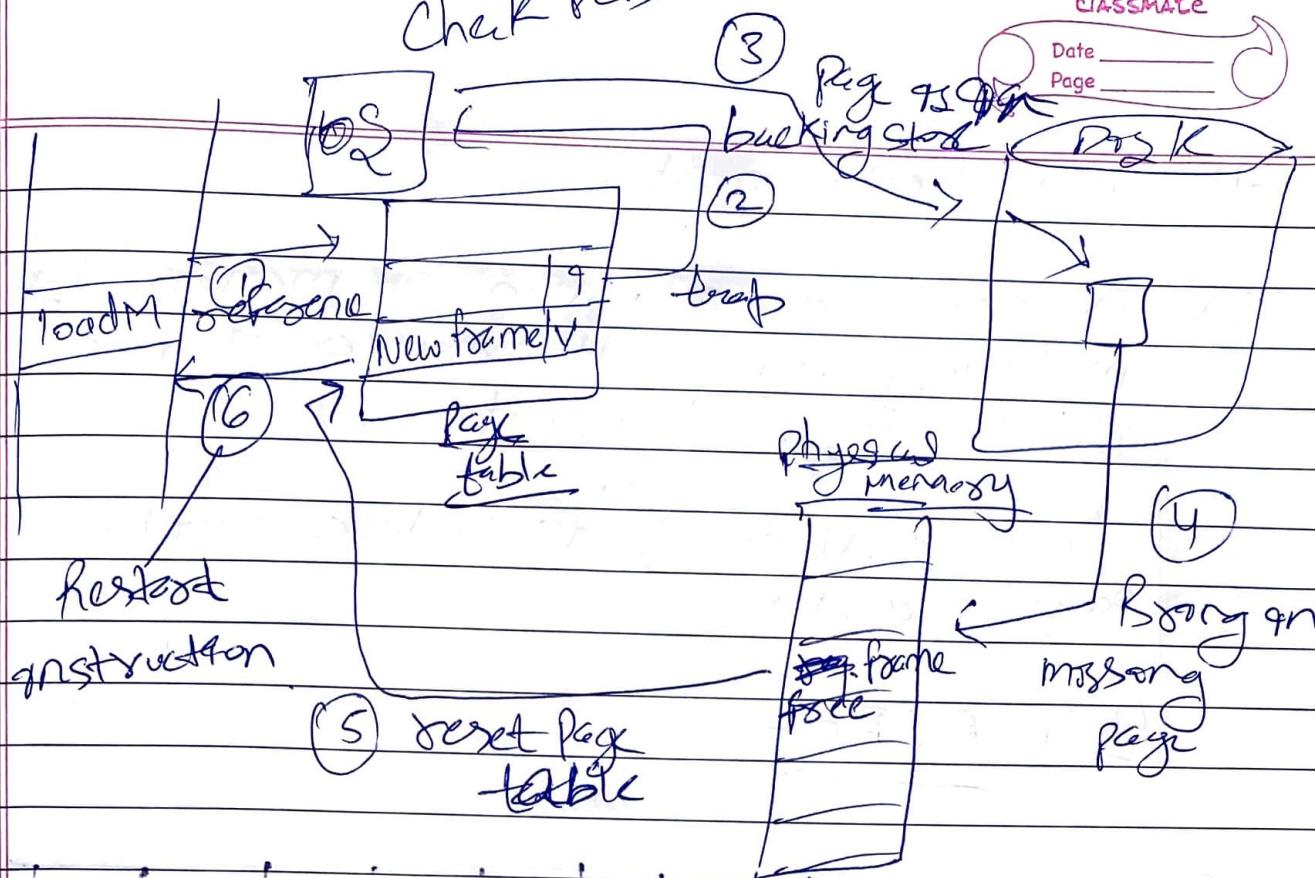
1. OS looks at PCB to decide:
  - invalid reference  $\Rightarrow$  abort
  - Just not in memory  $\Rightarrow$  (load the page)  
(loads it loads the page)
2. Get empty frame
3. Swap page into frame via disk operation
4. Reset page table to indicate page.

Now in memory,

Set utilization  $BAT = V$

5. Restart the instruction that caused the page fault.

# Check PCB



What happens if there is no free frame?

(1) Page replacement :

find some page in memory, but not really in use, swap it out.

(2) Some page may be brought into memory several times.

## # Page replacement -

- Prevent over-allocation of memory by modifying page-fault Services routine to include page replacement.
- Use modify(dirty)<sup>bit</sup> to reduce overhead of page transfer.

### (I) FIFO -

$f_3$		1	1	1	X	0	0	Ø	3	3	3	3	1	2	2
$f_2$	0	0	0	Ø	3	3	3	3	2	2	2	2	1	1	1
$f_1$	7	7	7	2	2	2	2	4	4	4	4	4	0	0	0
*	*	*	*	HIT	*	*	*	*	*	*	*	*	HIT	*	*

Reference 7, 0, 1, 2, 0, 3, 0, 4, 2, 3, 0, 3, 1, 2, 0

String

$$\text{Page HIT} = 3$$

$$\text{Page fault} = 12$$

Page miss

$$\text{HIT ratio} = \frac{\text{No. of hits}}{\text{No. of Reference}}$$

$$= \frac{3}{15} \times 100 \Rightarrow 20\%$$

## \* Belady's Anomaly in FIFO

$f_1$	1	3	1	3	2	2	2	2	2	4	4	1	1	1
$f_2$	2	2	2	1	1	1	1	1	3	3	3	1	1	1
$f_3$	1	1	1	4	4	5	5	5	5	5	5	1	1	1
$f_4$	*	*	*	*	*	*	*	*	HIT	*	*	HIT	HIT	(3)
													Page fault	(9)

Sequence  $\rightarrow 1, 2 \rightarrow 3, 4, 1, 2, 5, 1, 2, 3, 4, 5$

$f_1$	1	1	1	1	1	1	1	1	1	1	1	1	1	1
$f_2$														
$f_3$														
$f_4$														

$\hookrightarrow HIT \Rightarrow 2$   
 $\hookrightarrow \text{page faults} \Rightarrow 10$

This is Belady's anomaly

No. of frames increases so  
 page fault also increases and  
 HIT decreases

## # Optimal Page replacement —

Replace the page which is not used in longest dimension of time in future.

$S_4$	1	2	2	2	2	2	2	2	2	2	2	2	2	2	2
$S_3$	1	1	1	1	1	4	4	4	4	4	1	1	1	1	1
$S_2$	0	0	0	0	0	0	0	0	0	0	0	9	0	0	0
$S_1$	7	7	7	7	7	3	3	3	3	3	3	3	3	3	7
* * * * HIT * HIT * HIT															

Ref.  $\rightarrow$  7, 0, 1, 2, 0, 3, 0, 4, 2, 3, 0, 3, 2, 1, 2, 0, 1, 7, 0, 1  
 Strong  $\uparrow \longrightarrow$

page hit = 12

page fault = 8

$$\text{Hit ratio} \Rightarrow \frac{12}{20} \times 100$$

## # Least Recently Used — (LRU)

Replace the least recently used page in past.

$S_4$	1	2	2	2	2	2	2	2	2	2	2	2	2	2	2
$S_3$	1	1	1	1	1	4	4	4	4	4	1	1	1	1	1
$S_2$	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
$S_1$	7	7	7	7	7	3	3	3	3	3	3	3	3	3	7
* * * * HIT * HIT * Hot Not															

Ref.  $\rightarrow$  7, 0, 1, 2, 0, 3, 0, 4, 2, 3, 0, 3, 2, 1, 2, 0, 1, 7, 0, 2  
 Strong  $\leftarrow \uparrow \leftarrow \leftarrow \uparrow \leftarrow \leftarrow$

$$\begin{array}{l} \text{Page fault} = 8 \\ \text{Page hits} = 12 \end{array}$$

# Most recently used —

Replace the most recently used page in list.

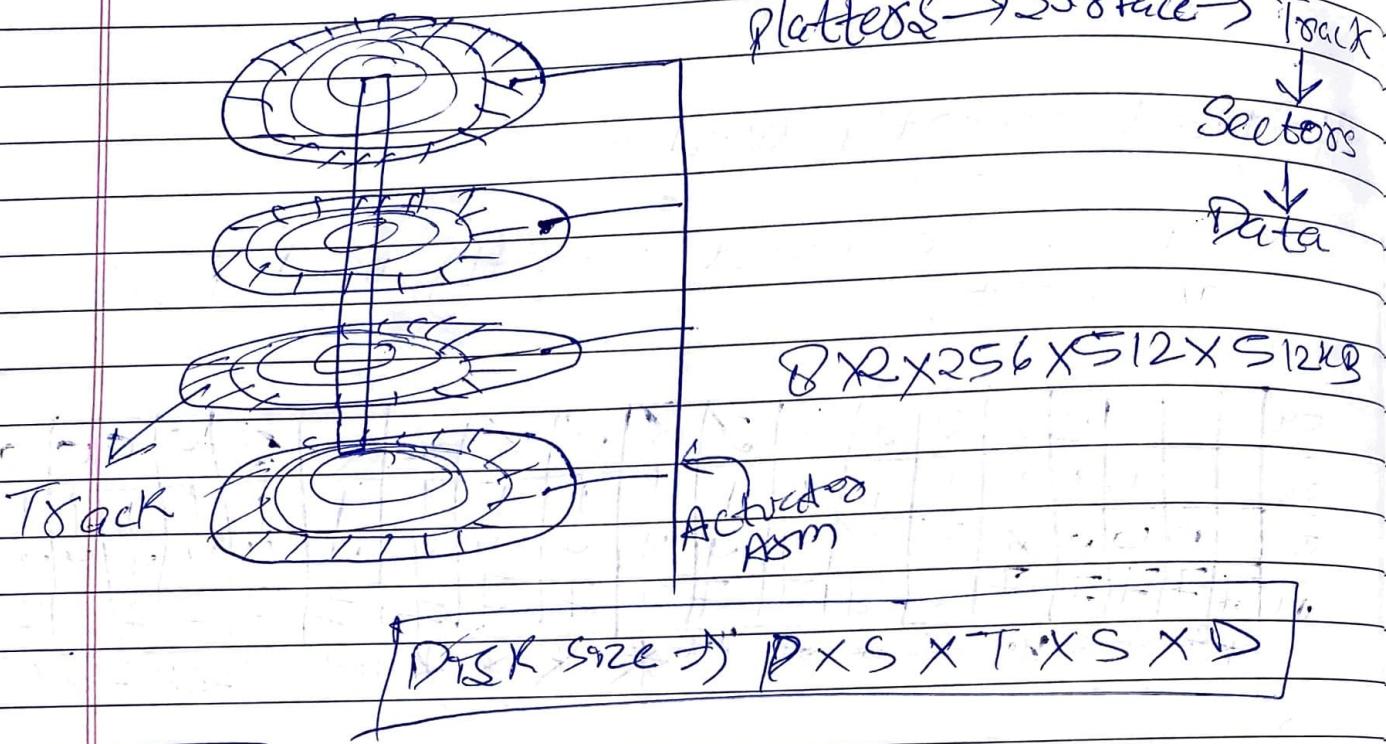
f4	1	2	2	2	2	2	3	8	2	2	2	0	0	0	0
f3	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
f2	0	0	0	6	3	8	4	4	4	4	4	4	4	4	4
s1	7	7	7	7	7	7	7	7	7	7	7	7	7	7	7
	*	*	*	not	*	*	not	*	*	*	not	not	not	not	not

Ref. → 7, 0, 1, 2, 0, 3, 0, 4, 2, 3, 0, 3, 2, 1, 2, 0, 1,  
String ← ↑ ↑ ↑ ,  
← 7, 0, 1

Page fault = 12

Dye Hots = 8

## Disk Architecture



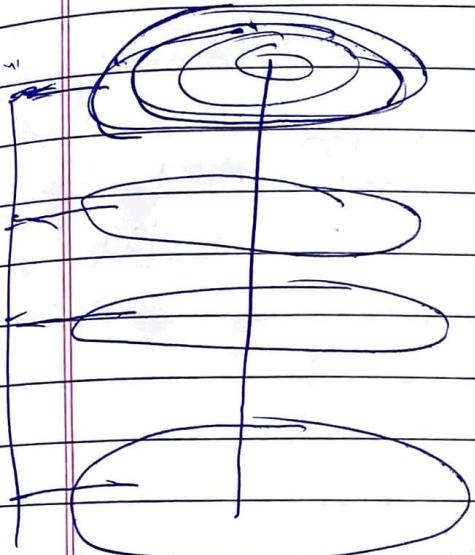
$$\begin{aligned}
 R &= 2^{10} \\
 A &= 2^{20} \\
 C &= 2^{30} \\
 T &= 2^{40}
 \end{aligned}$$

## Disk Access time

- 1) Seek time: Time taken by R/W head to reach desired track
- 2) Rotation time: Time taken for one full rotation ( $360^\circ$ )
- 3) Rotational latency: Time taken to reach to desired Sector (half full rotation time)

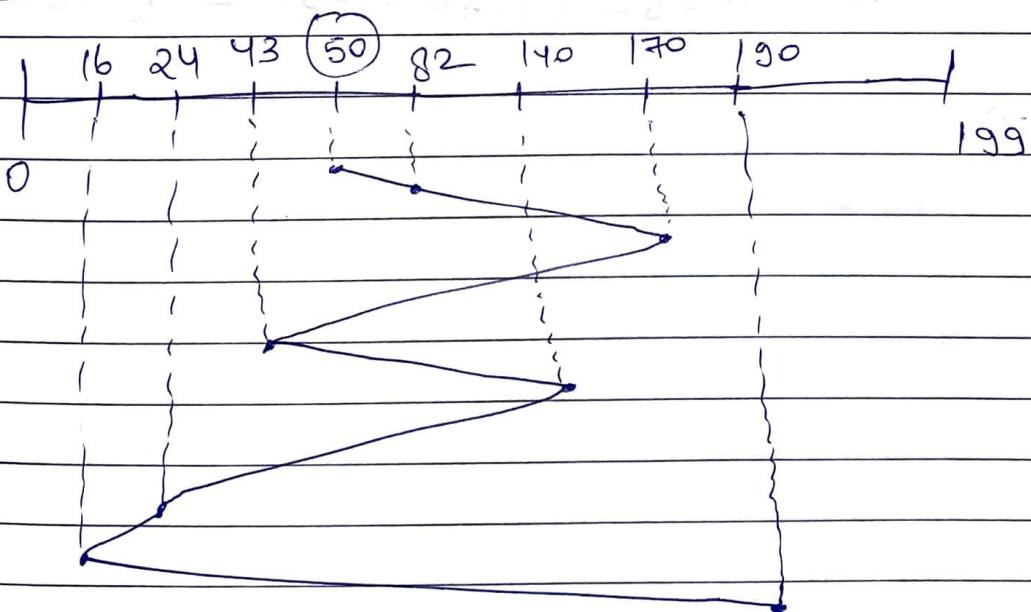
ii) Transfer time: Data to be transferred

Transfer rate:  $\{ \text{No. of Rx capacity of } \times \text{ No. of heads one rotation} \}$   
 in one second



Disk Access  $\Rightarrow$  Start + TT + time CT + AT

# FCFS (First Come First Serve) —



Request track  $\Rightarrow$  82, 170, 43, 140, 24, 16, 190

Current position of R/W head = 50

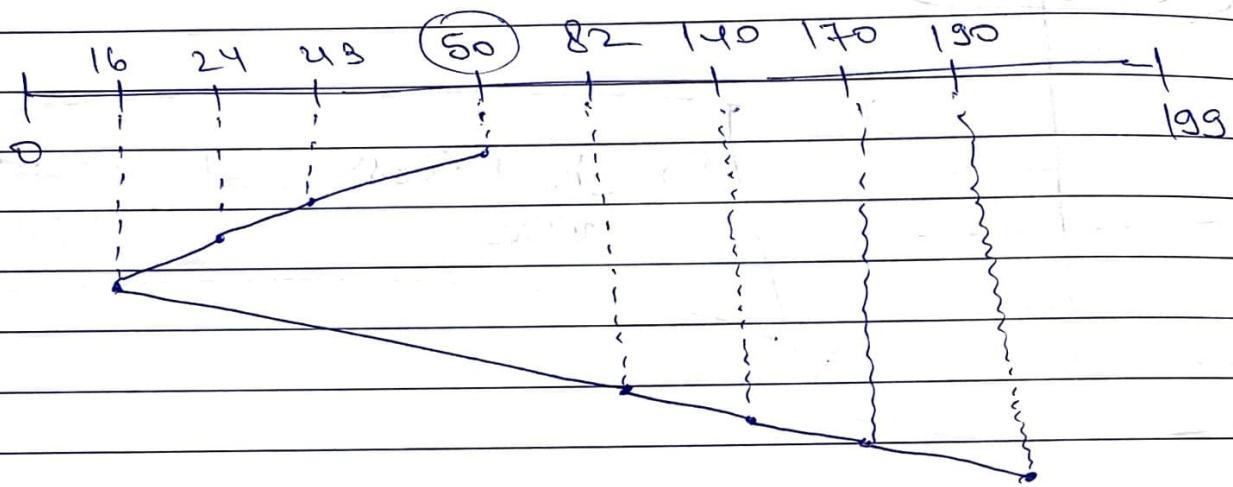
Calculate total no. of tracks movement of head.

$$\Rightarrow (82-50) + (170-82) + (170-43) + (140-170) + (24-140) + (16-24) + (190-16)$$

# SSTF (Shortest Seek time first)

Request  $\rightarrow$  82, 170, 43, 140, 24, 16, 190  
 Queue

curr. pos. of R/W head = 50



$$\Rightarrow (50 - 43) + (43 - 24) + (24 - 16) + (82 - 16) \\ + (140 - 82) + (170 - 140) + (190 - 170)$$

$\Rightarrow$

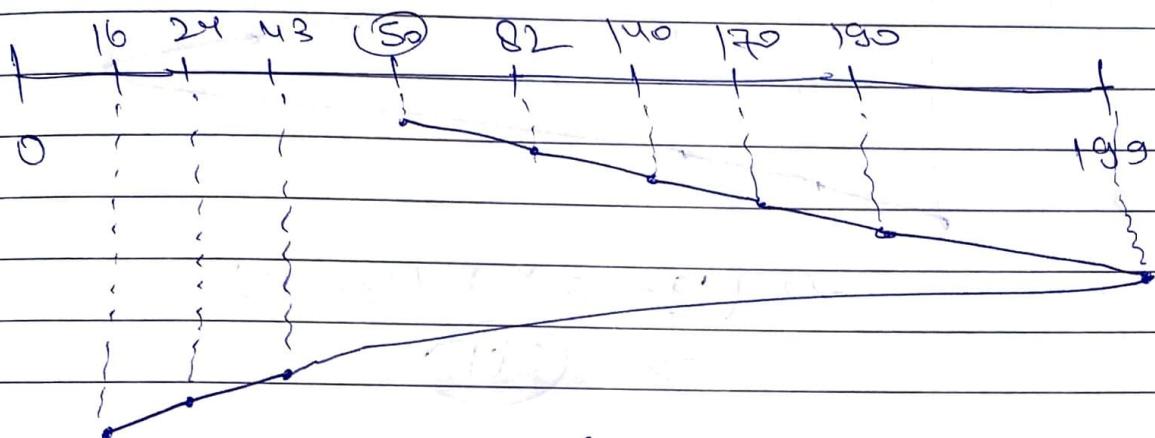
# SCAN Algorithm —

Request  $\rightarrow$  82, 170, 43, 140, 24, 16, 190

Queue

Curr. pos.  $\rightarrow$  50

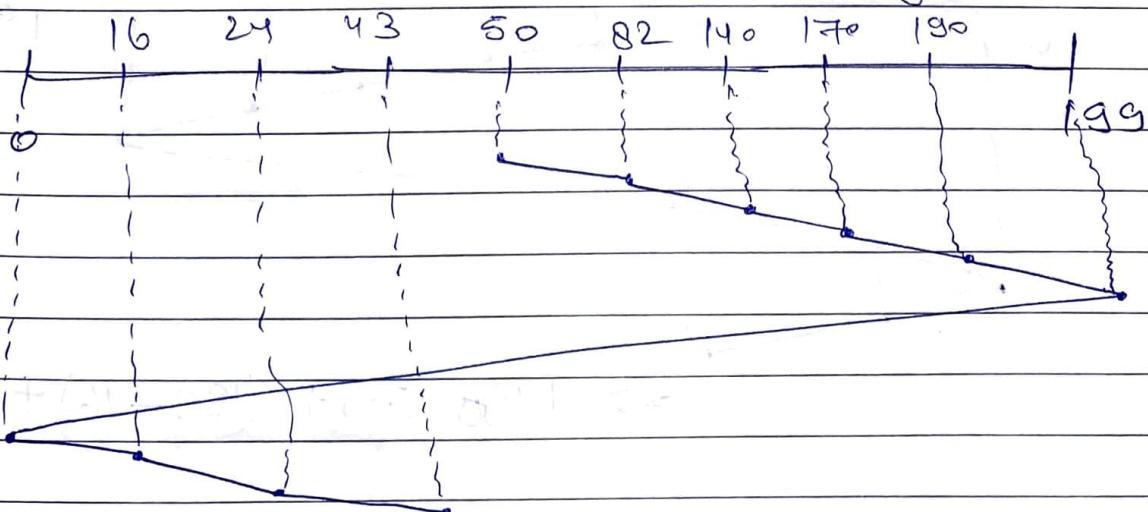
move towards high value.



(it do not go  
at last value)

$$(199-50) + (199-16) \\ = 332$$

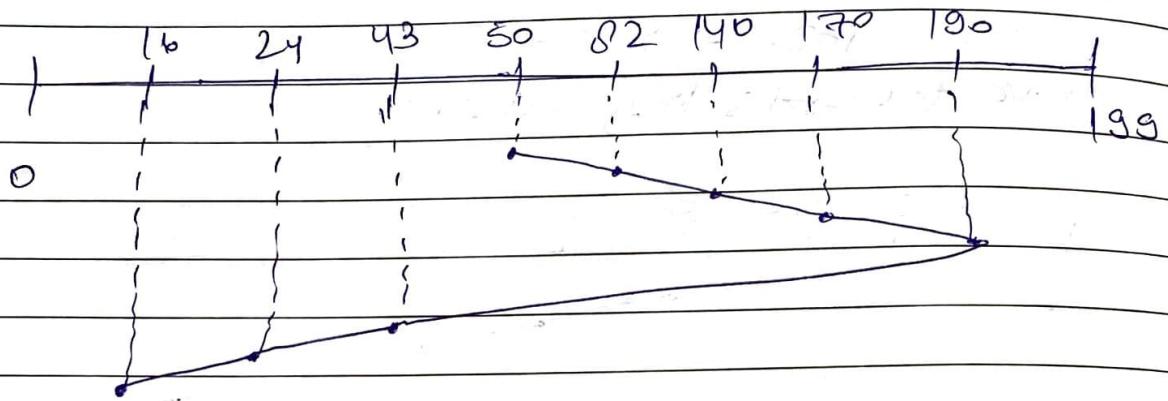
Q. If R/W head takes 1 ns to move from one track to another then total time taken =  $332 \times 1$

# CC-SCAN Algorithm — Direction is towards larger value.

$$(199-50) + (199-0) + (43-0) \\ \Rightarrow 391$$

## # LOOK Algorithm —

Direction is towards larger value.

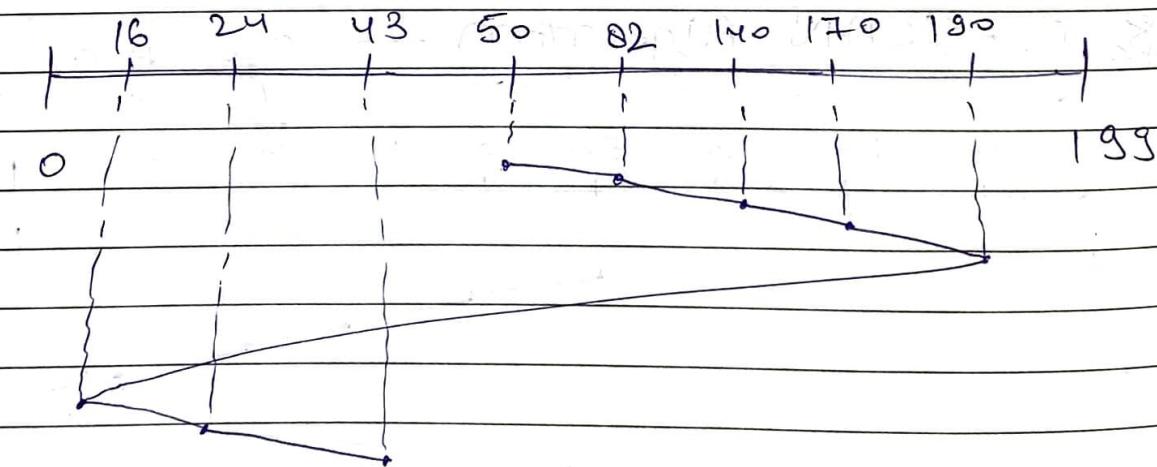


$$\Rightarrow (190 - 50) + (190 - 16)$$

$$\Rightarrow \boxed{314}$$

## # C-LOOK Algorithm —

Direction is towards larger value



$$\Rightarrow (190 - 50) + (190 - 16) + (43 - 16)$$

## RAID (Redundant Array of Independent Disks)

→ IS a way of storing the same data in different places to protect data in case of a drive failure.

### RAID-0

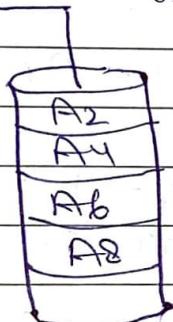
(data striping)

(data ko todte hain)

(performance sabse zyada deti hain)



DISK 0



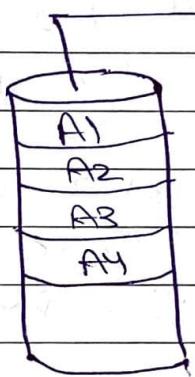
DISK 1

### RAID - 1

(Data mirroring)

(data ka copy)

(Data ko Secure kia)



DISK 0



DISK 1

### RAID 1 + 0

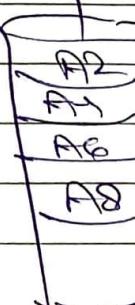
(Data mirroring + Data striping)

RAID 0

RAID 1



RAID 1



RAID-3

(AP = Parity)

Ex-  $A_1 = 1, A_2 = 2, A_3 = 3$ 

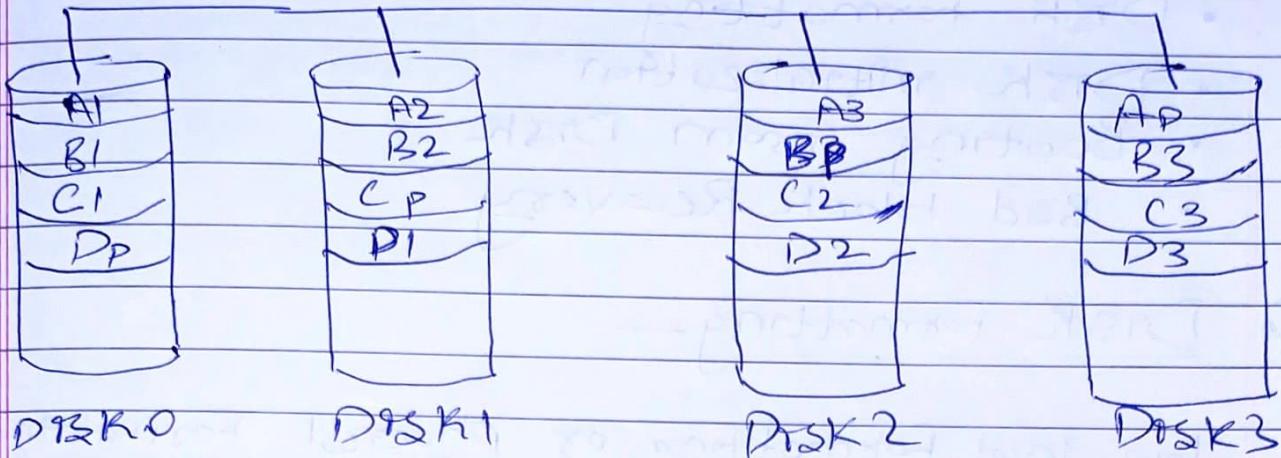
$$\text{So } AP_{(1-3)} = 6 [1+2+3]$$

So if A2 is lost we can calculate it

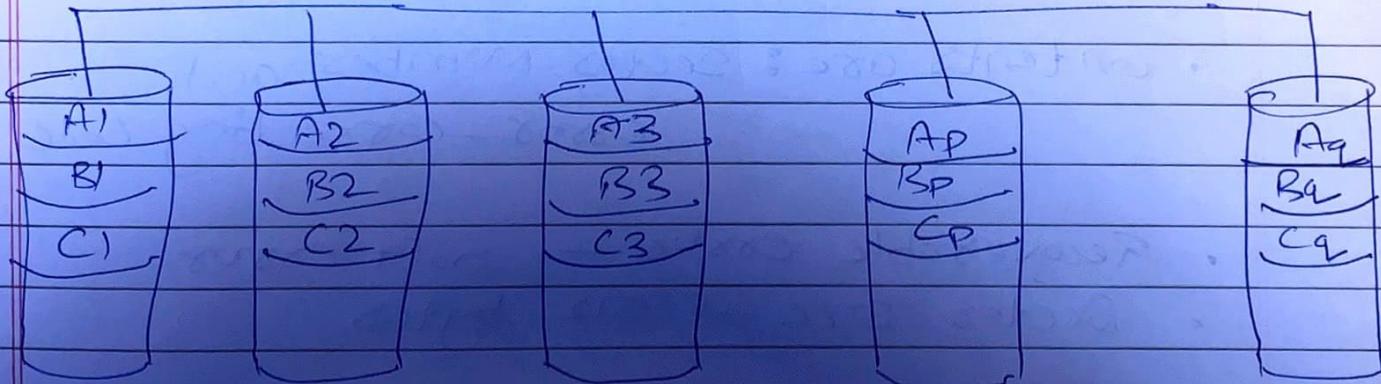
by using parity  $AP_{(1-3)}$ .

→ Here DSKR 3 will be used very much so it will cause a problem so RAID 5 will solve the problem.

RAID-4 (Same as RAID-3)

RAID - 5

So ab kai ek disk over utilize nahi hogi  
ab saari disk equally utilize hogi

RAID - 6

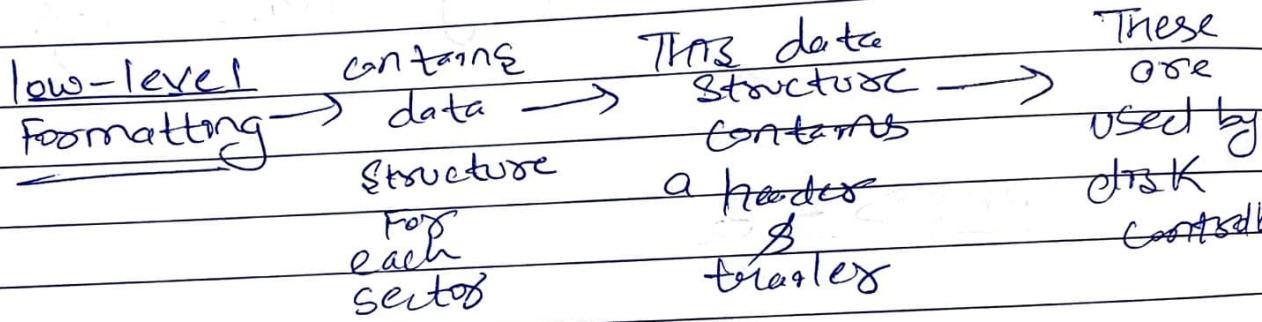
## Disk Management -

- Disk formatting
- Disk initialization
- Booting from Disk
- Bad block Recovery



### Disk Formatting -

Low-level Formatting or Physical Formatting:  
Before a disk can store data it must be divided into sectors.



- contents are : Sector Number and error - correcting code.
- Recoverable error — Soft error
- Sector size of 512 bytes
- Before storing files , OS, two steps
  - partitioning of the Disk
  - logical Formatting (creation of file system FAT)

## (\*) Boot Block —

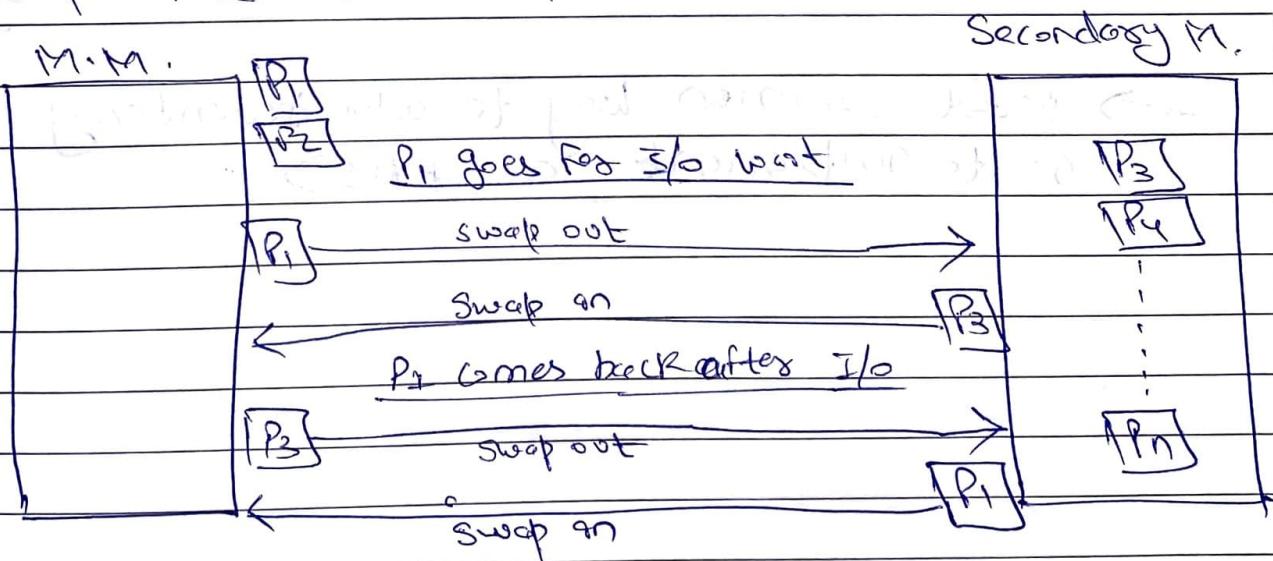
Blocks where the full BootStrap program is stored on the disk.

## (\*) Bad blocks —

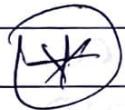
- If blocks go bad during normal operation, then some special programs must be run manually to search for bad blocks.

## (\*) Swap - Space Management —

A process can be swapped temporarily out of M.M. to Secondary memory later again swaps back to M.M.



- Swap-Space management is another low-level task of O.S.
- Goal of Swap-Space implement. is to provide best throughput.
- If we use disk space, it will decrease system performance.



## Disk Reliability -

- Reliability -

ability of disk system to accommodate a single or multiple failure and still remain available to others.

→ Adding redundancy  $\Rightarrow \uparrow$  reliability of disk system.

→ Most common way to add redundancy is to implement a RAID.