PROFILE

- Upcoming graduate seeking position in a junior/entry software engineering role.
- Creative inclination for developing novel solutions for existing and emerging challenges
- Excellent interpersonal communicator with drive to lead diverse groups of individuals

EDUCATION

University of Toronto, St. George Campus

Sept. 2014 – July 2019 (Expected)

B.Sc. (Hons.): Computer Science; Physical & Environmental Geography

Toronto, ON

Current CGPA: 3.46/4.00

Awards: Dean's List, Winter 2016

WORK EXPERIENCE

Developer

May 2018 - Aug. 2018

Rogers Digital Media

Toronto, ON Ottawa Catholic School Board Feb. 2013 - June 2013

Teaching Assistant

Kanata, ON

- Developed internal-use **Node.js** app for managing multiple MySQL databases used by major digital brands such as Sportsnet and Today's Parent.
- Deployed **Angular** frontend to **Azure** instance; for user database management with 3rd party SDK integration.
- Administered behavior management policies for atrisk children in classroom environments
- Assisted in day-to-day organization of multiple classes of students aged 5 to 11

SELECTED PROJECTS

Multiprocessing BMP Filter Server

CSC209, Systems Programming; 3.7/4.0 (Fall 2017)

- Designed and deployed multiprocessing HTTP server developed in C99
- Enabled multiple clients to perform image filtering on server or client-side bitmap files
- Implemented concepts of **structures**, **linked lists**, and memory allocation
- Leveraged client-server communication through POSIX APIs for forks/pipes, sockets & binary R/W

Graphical Image Tagger

CSC207, Software Design; 4.0/4.0 (Fall 2016)

- Developed and deployed **GUI** applet in **Java** to search, find, and organize images
- Enabled users to access and modify **serializable** image objects stored in recursive directories
- Allowed application of image tags from a modifiable, persistent catalog of tags to a set of images

FPGA Simon Says CSC258

CSC258, Computer Organization (Fall 2016)

- Implemented interactive game of Simon Says on physical hardware
- Utilized DE1-SOC/Altera Cyclone V FPGA board
- Developed in Verilog, focusing on HEX displays, VGA output, and switches/toggles
- Simulated and debugged using **ModelSim**.

Arduino Ambient Lighting System

Personal Project, (Summer 2017)

- Modified FastLED library with a custom build of WS2812B addressable LEDs used with an Arduino Nano.
- Successfully debugged various issues with inconsistent power delivery for LEDs.
- Interfaced directly with Windows to provide ambient backlighting for personal monitor.

SELECTED SKILLS

- Iava
- C99/C11
- MySQL/SQLite
- JavaScript/TypeScript

- Node.is
- Angular
- HTML/CSS/jQuery
- Python

- Verilog
- Bash/zsh
- Git
- **IetBrains IDEs**