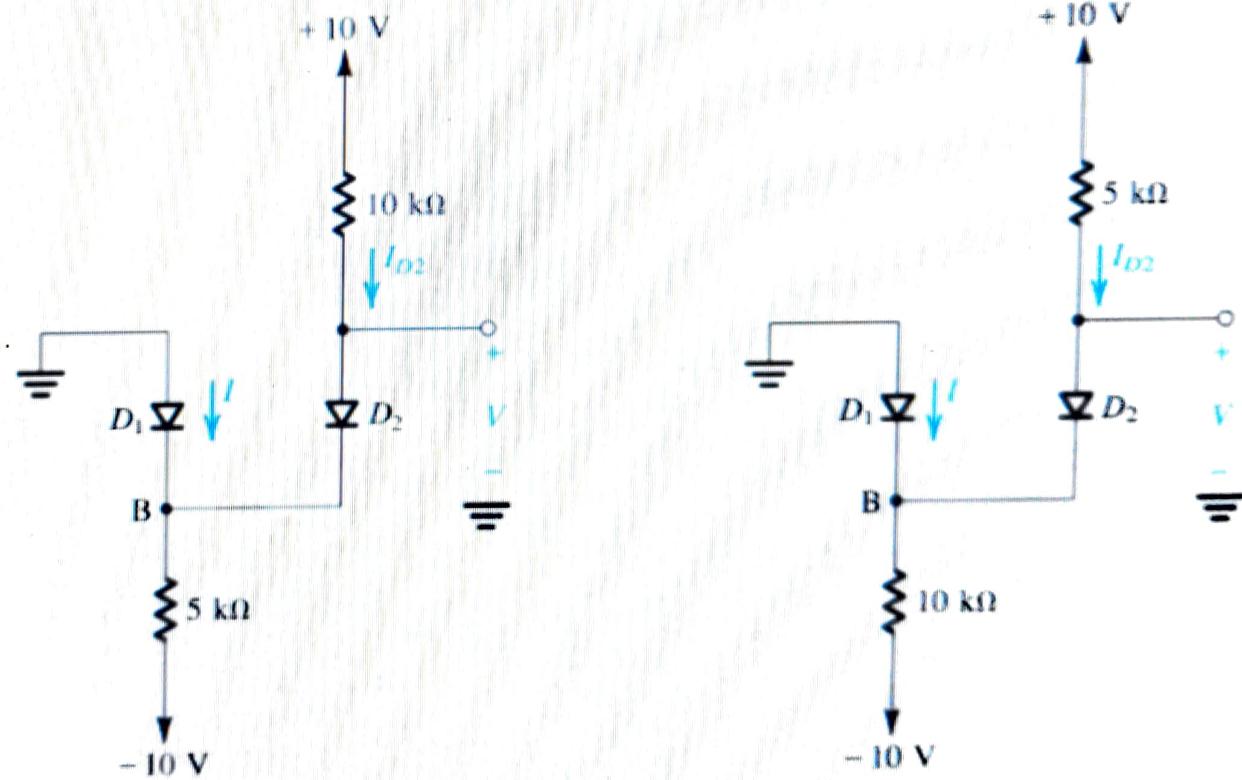


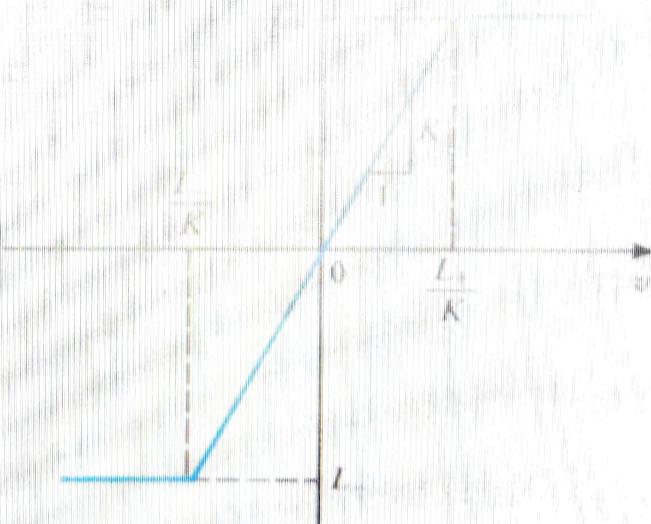
# Problems

- Find  $V$  and  $I$ . Assume diodes to be ideal.



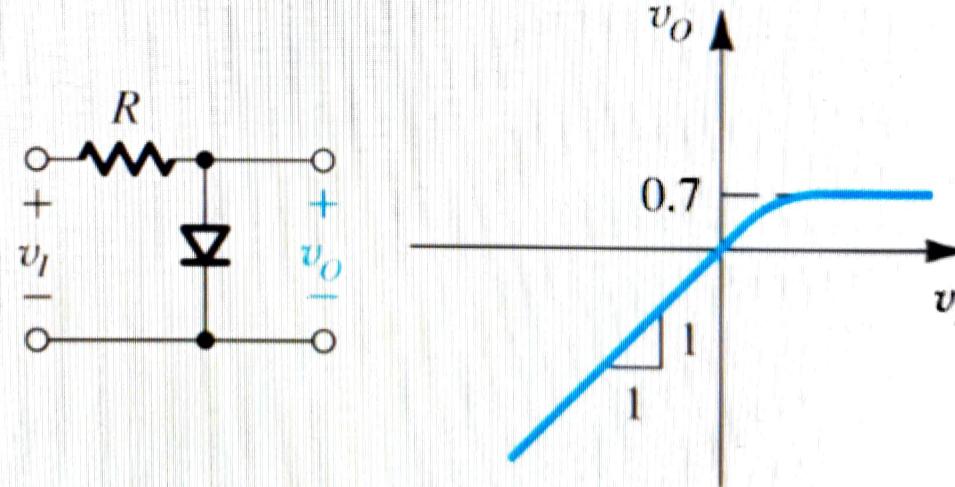
# Diode applications

- Limiter circuits
- Clamped capacitor
- Voltage doubler

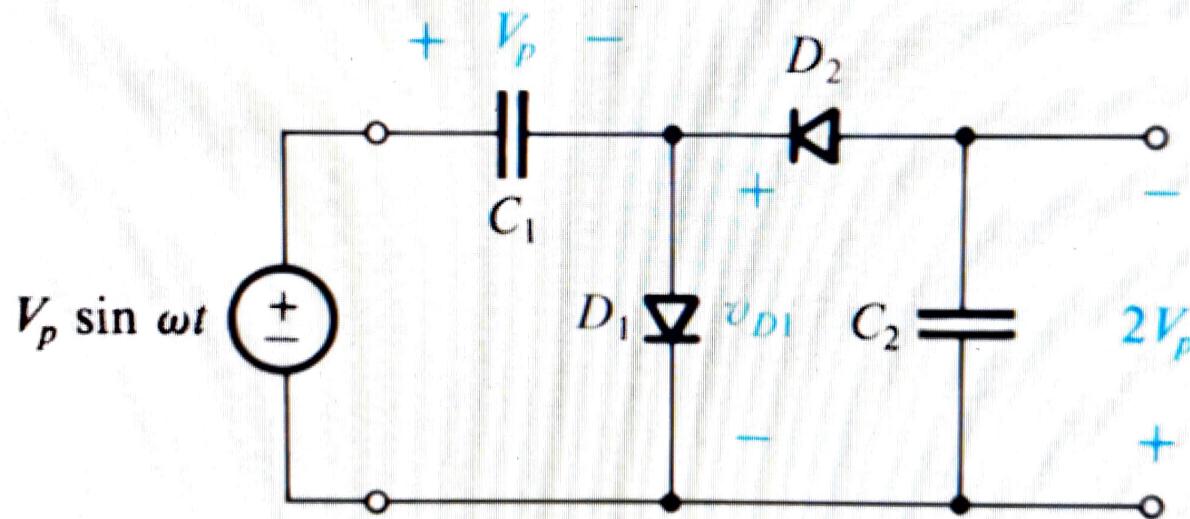


# Limiter circuits

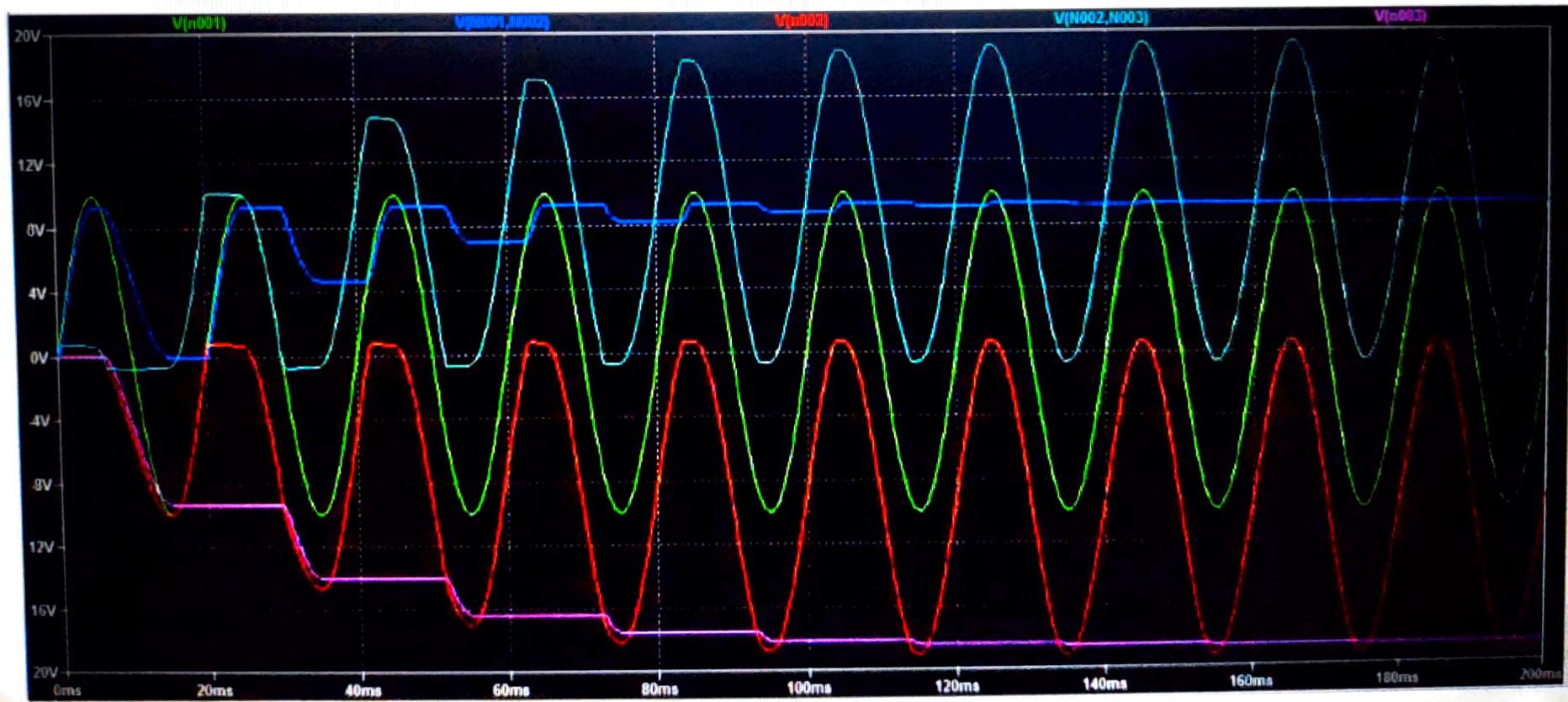
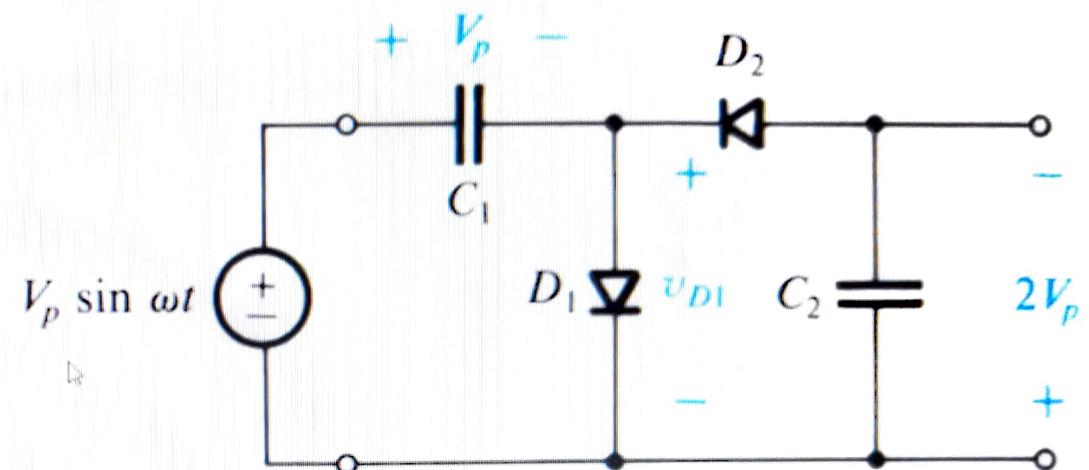
- Transfer characteristic of a limiter circuit



# Voltage doubler



$$C = 10\mu F$$



# Material classification

{ Gold  
O<sub>2</sub>

- Material classification based on conductivity ( $\sigma$ )/resistivity ( $\rho$ )

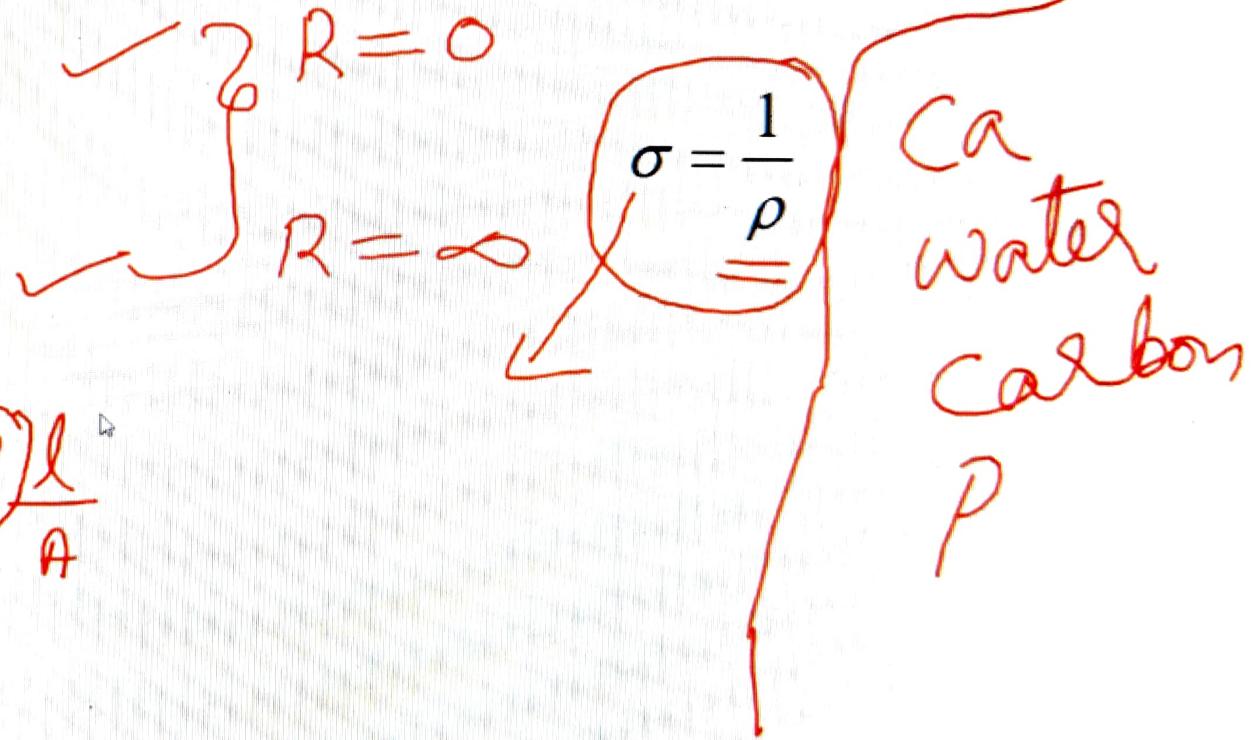
- Conductors

- Semiconductors

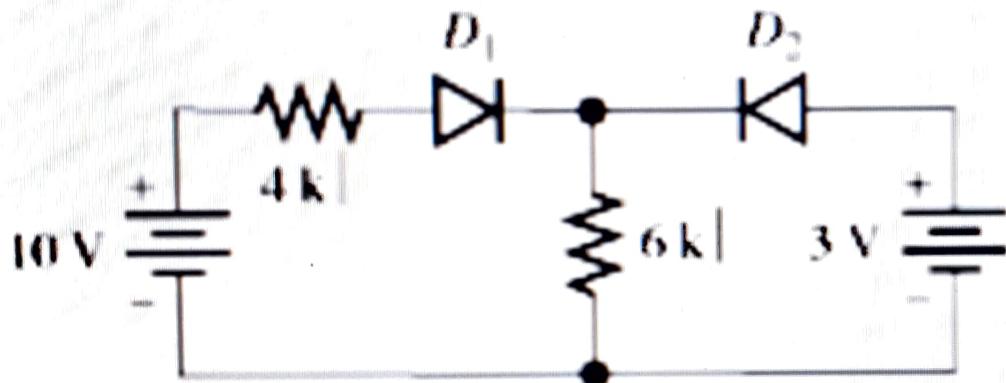
- Insulators

②

$$R = \rho \frac{l}{A}$$



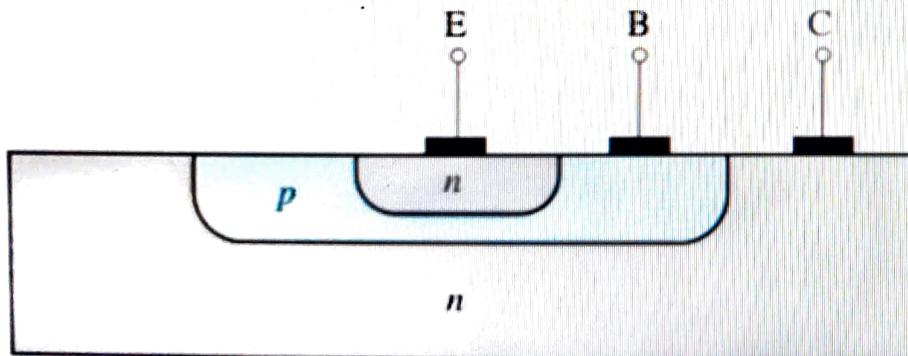
- Find current flowing in  $6k\Omega$  resistor. Assume ideal diode model



D1	D2
ON	ON
OFF	OFF
ON	OFF
OFF	ON

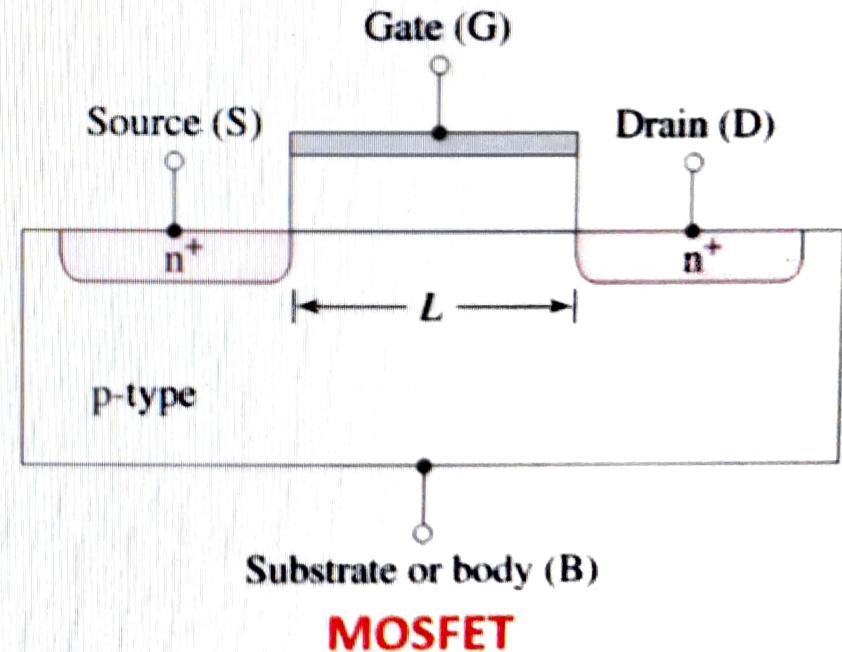
# Transistors

- *Transistor*
  - *Transfers resistance*
- Transistor types
  - BJT (bipolar)
  - FET (Unipolar)



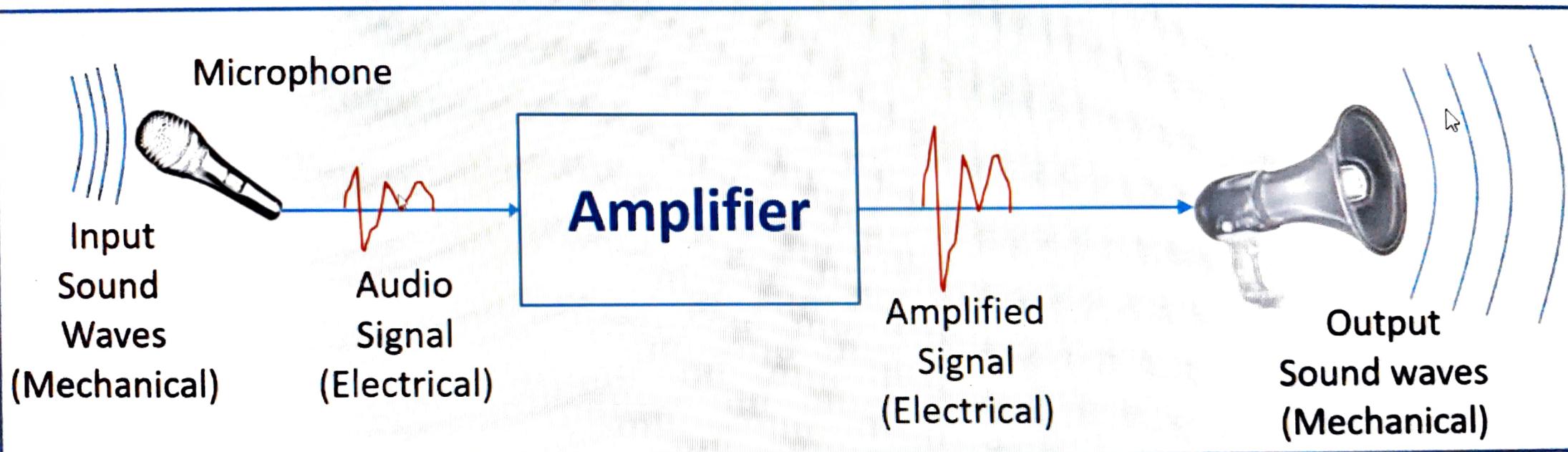
BJT

- Applications
- Amplifier
  - Switch



MOSFET

# Amplifier: Basic PA system

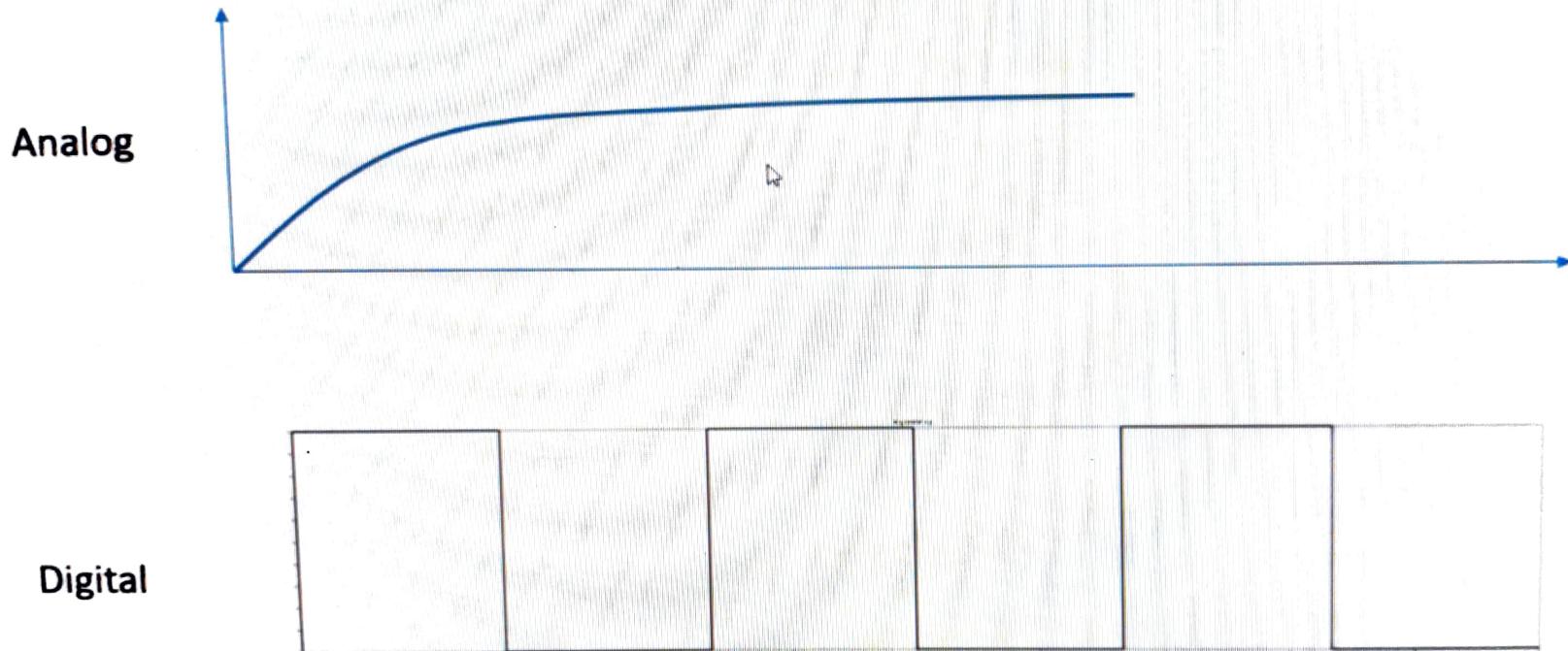


To amplify audio signal

1. Sound wave → electrical signal
2. Amplified electrical signal
3. Electrical signal → Amplified sound wave

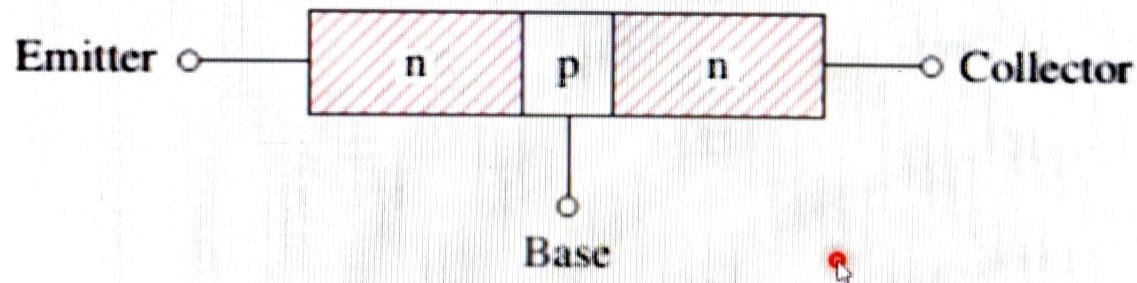
PA: Public Address

# Switching



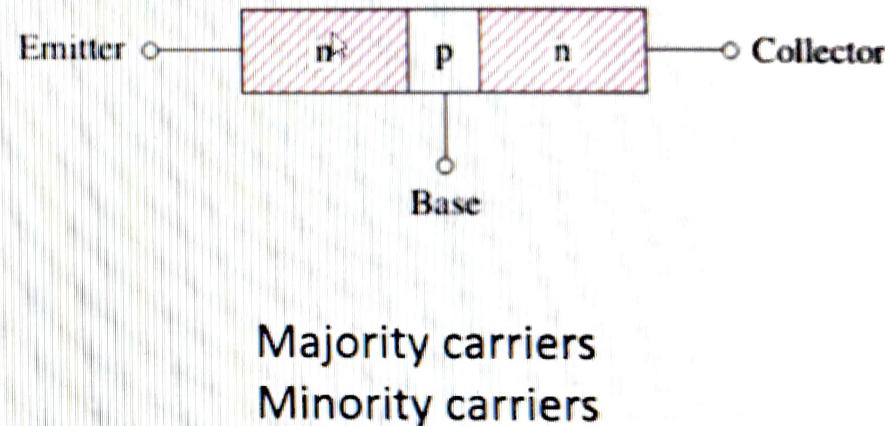
# Bipolar junction transistor

- Transistor principle:  
*the voltage between two terminals (Emitter and Base) controls the current through the third terminal (Collector)*

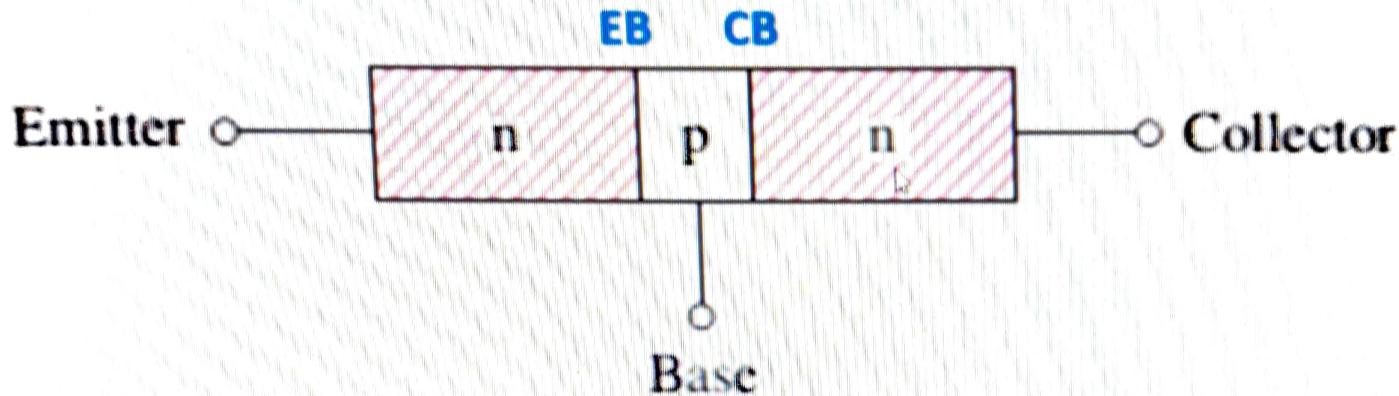


# Bipolar junction transistor (BJT)

- Three doped regions
- Three terminals
- Two pn junctions
- Single pn junction has two modes
  - FB
  - RB
- BJT has two junctions
  - Four modes of operation



# Modes of operation

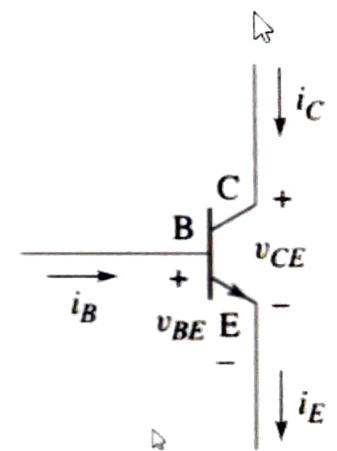
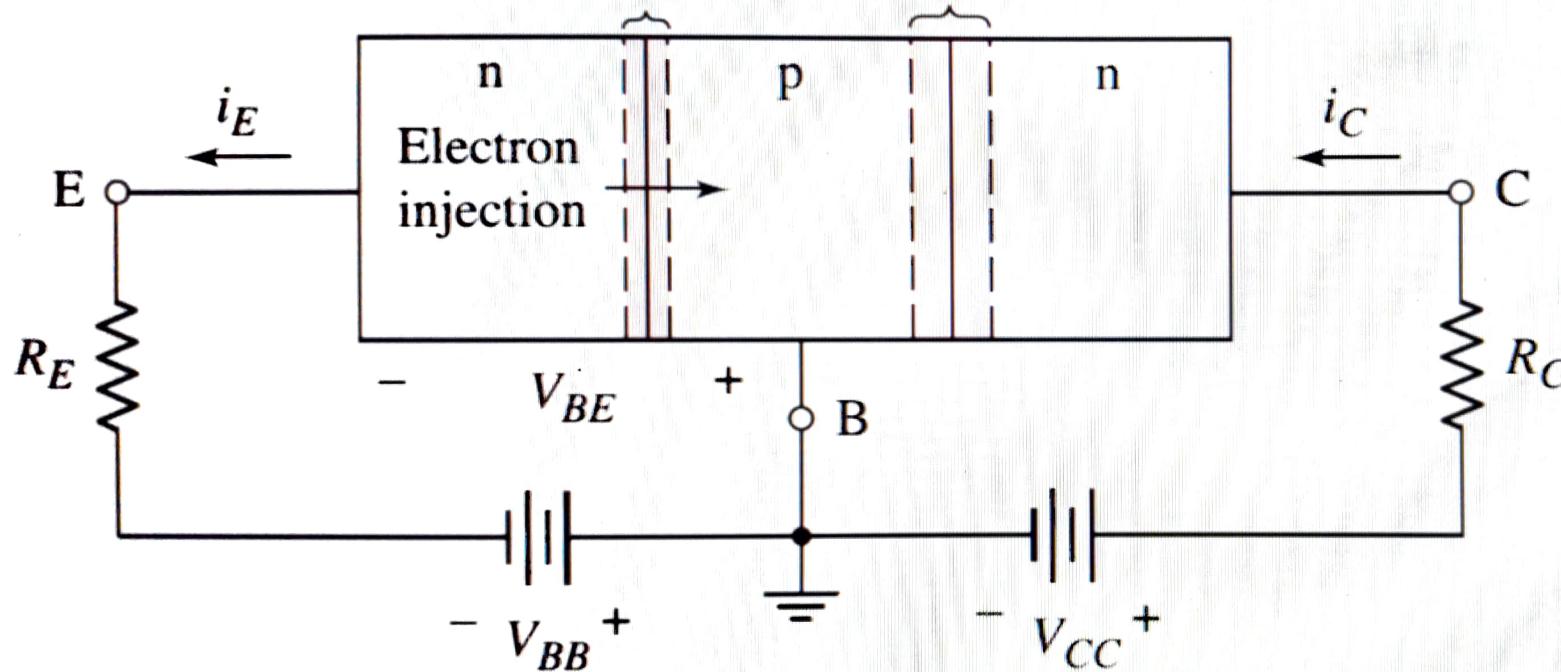


EB	CB	Mode
FB	RB	Active
FB	FB	Saturation
RB	RB	Cut-off
RB	FB	Reverse active

Draw the structural diagrams clearly mentioning the depletion region widths.

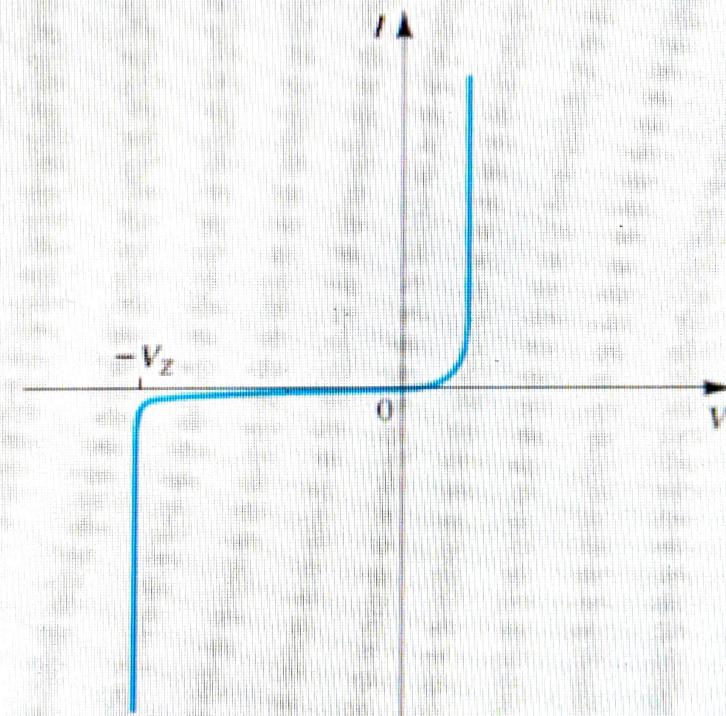
# Transistor as an amplifier

Base-emitter  
(B-E) junction      Base-collector  
(B-C) junction

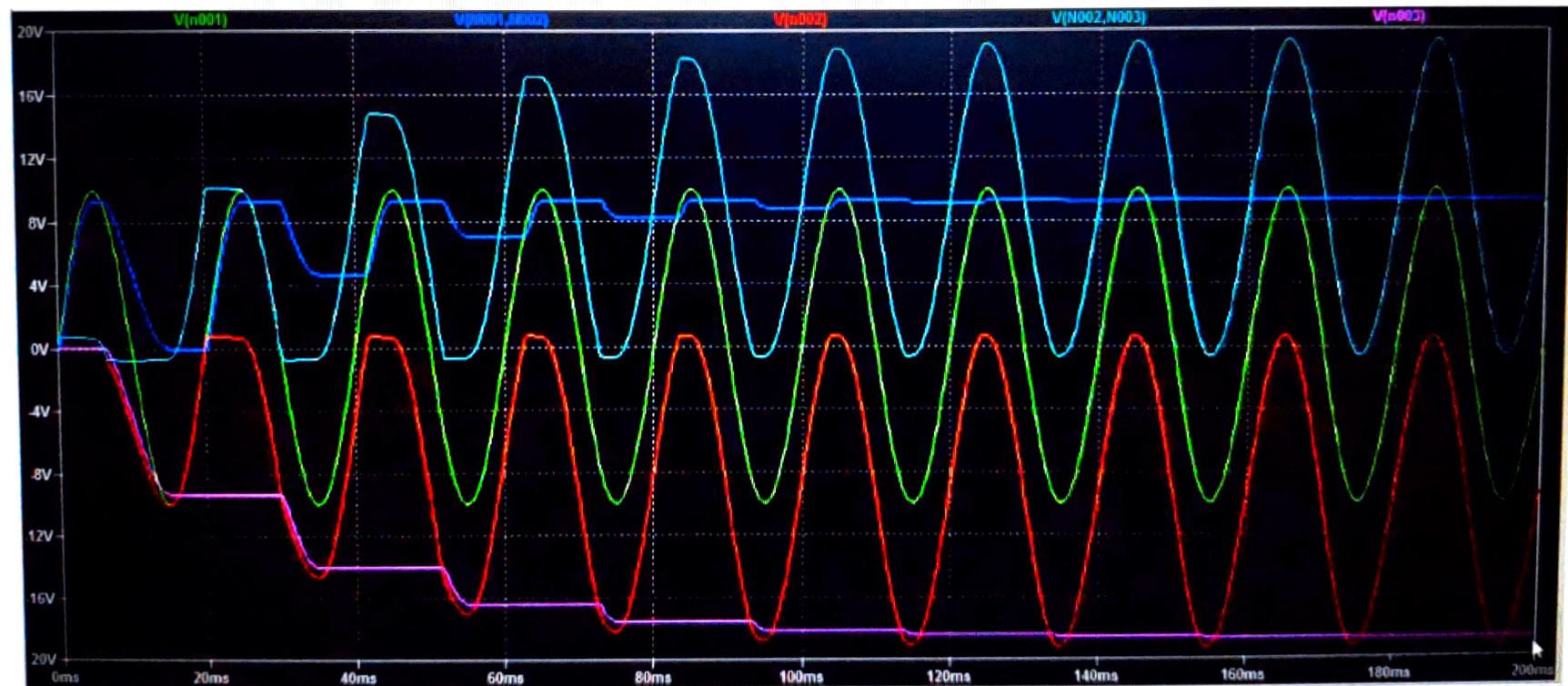
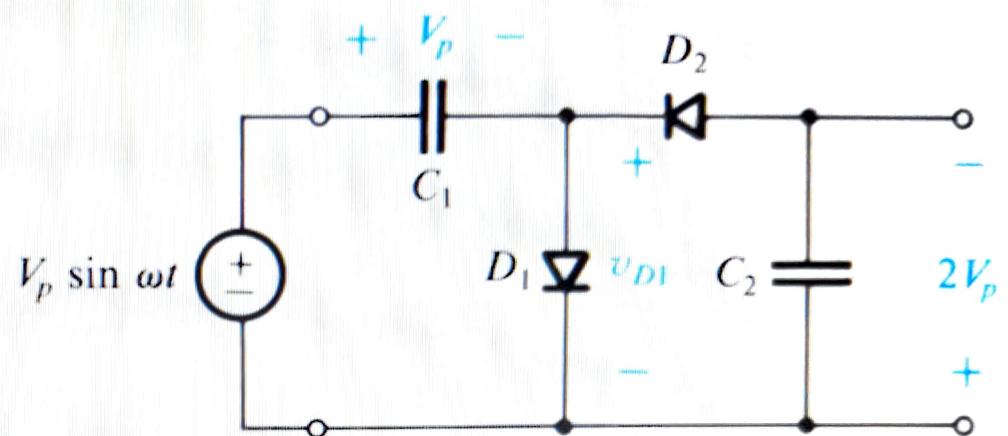


Current flow in a *npn* transistor biased to operate in the active mode

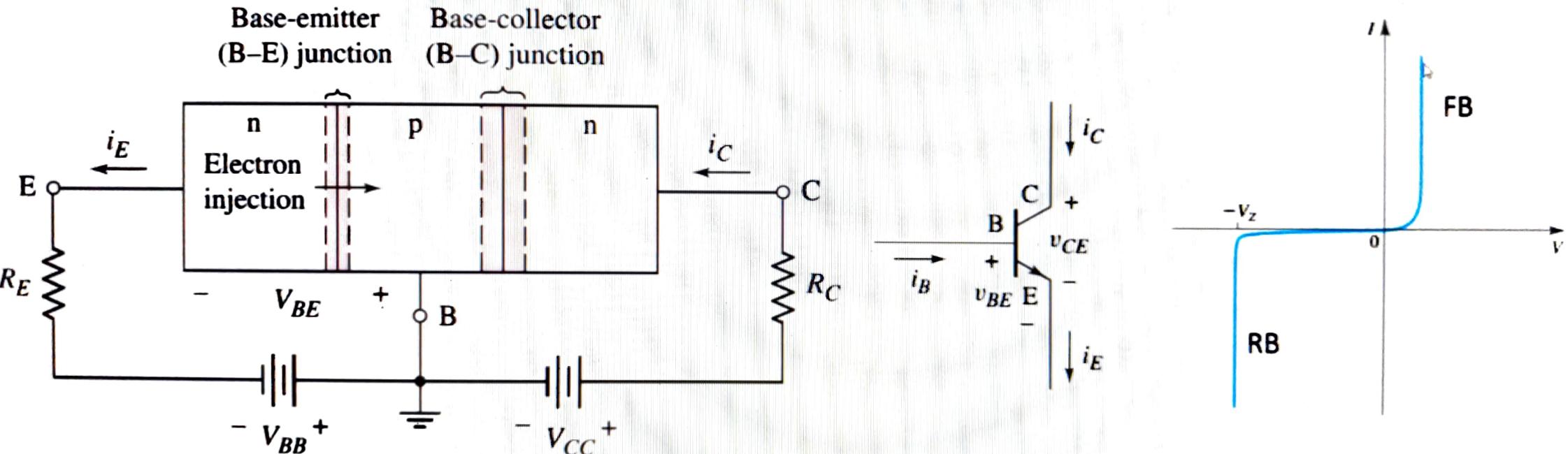
# I-V characteristics of diode



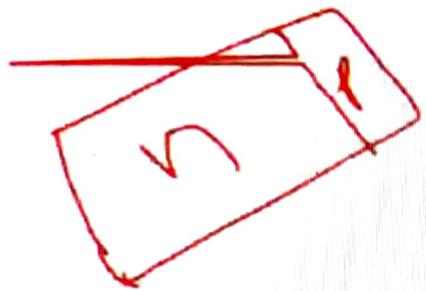
$$C = 10\mu F$$



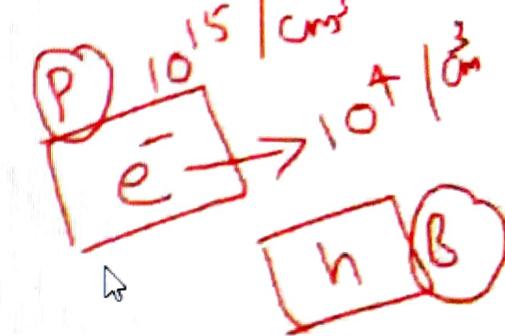
# Transistor as an amplifier



Current flow in a *npn* transistor biased to operate in the active mode

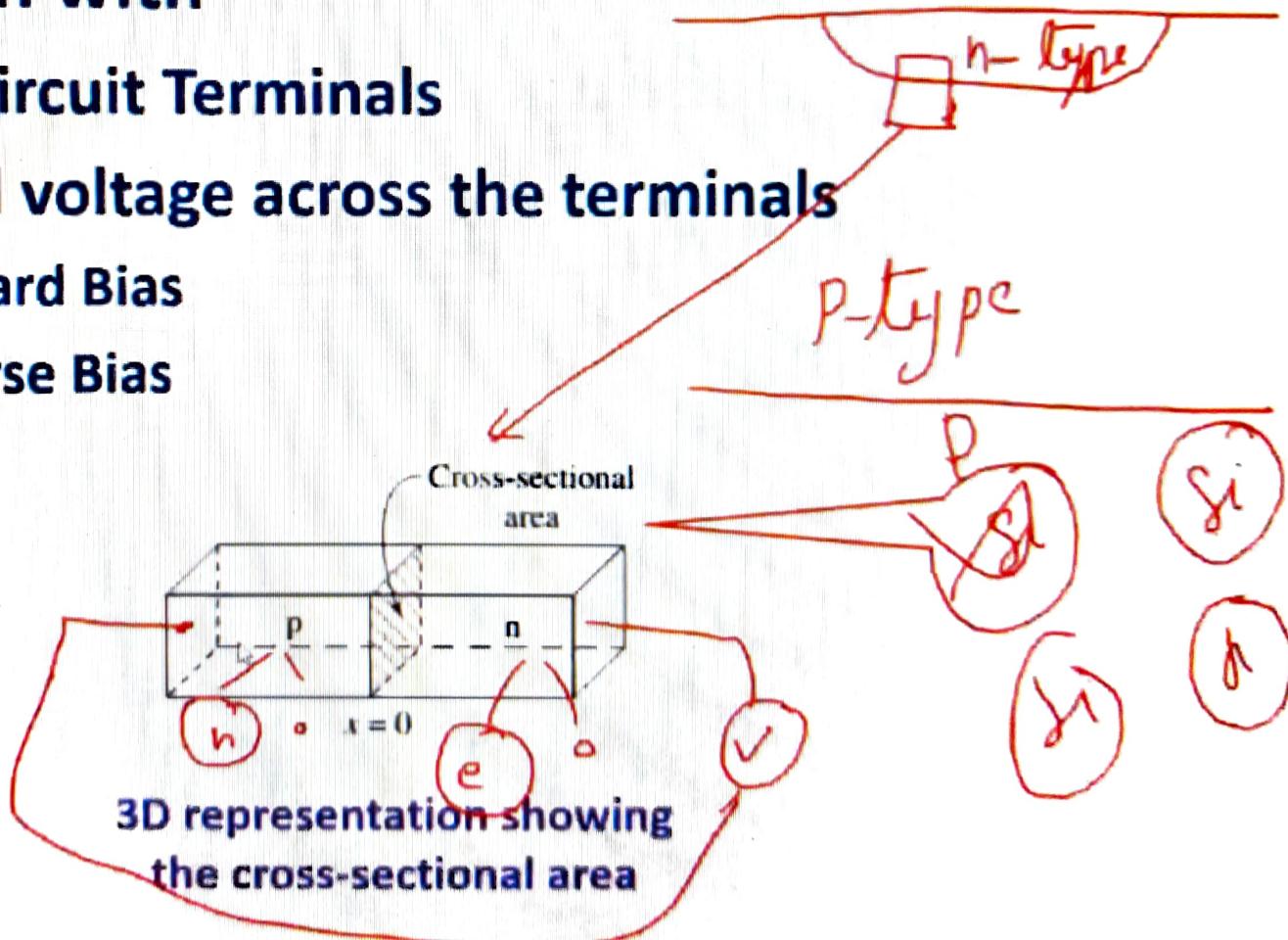


# The pn Junction

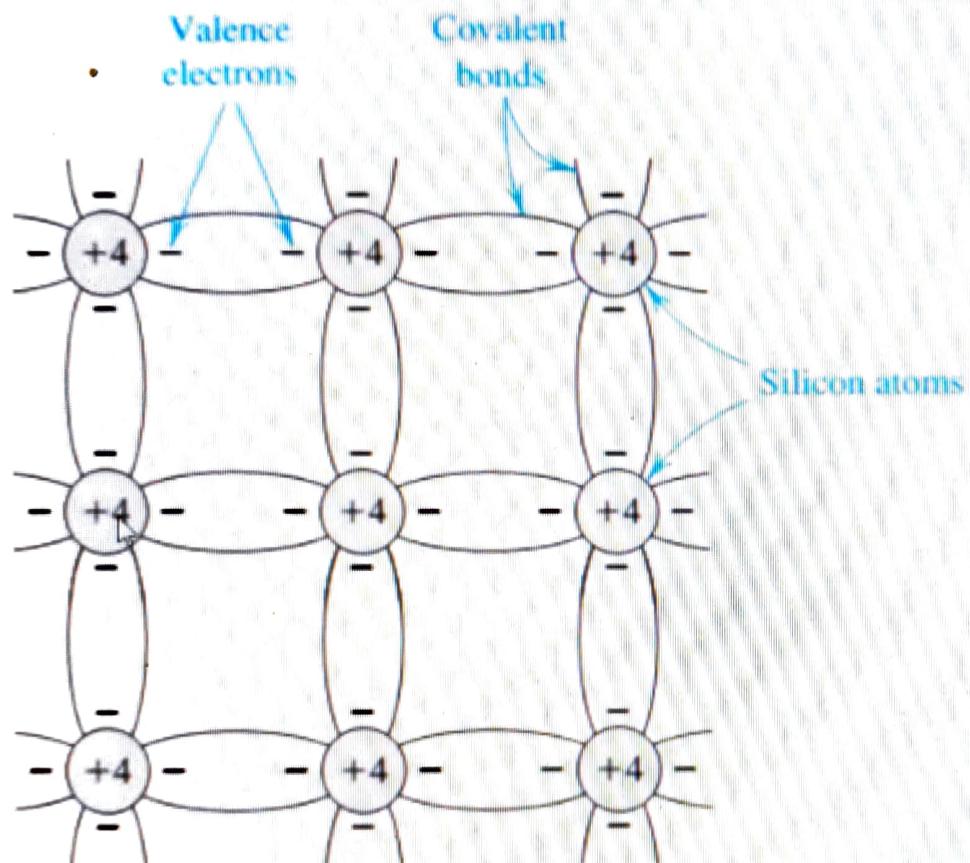


- Operation with
  - Open-Circuit Terminals
  - Applied voltage across the terminals
    - Forward Bias
    - Reverse Bias

$e^-$   
 $h$



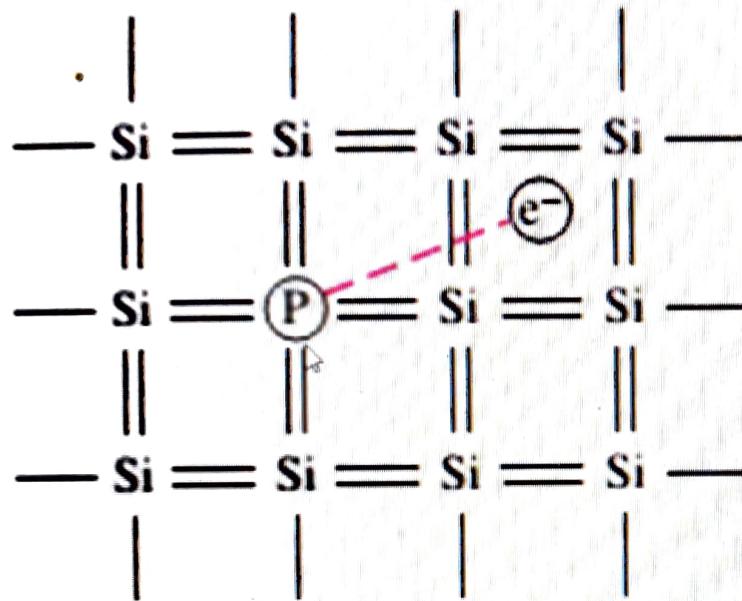
# Single crystal silicon at $T = 0 \text{ K}$



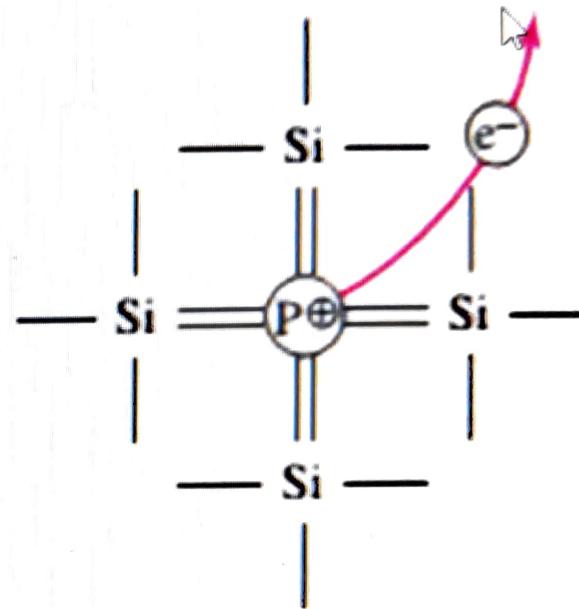
Silicon Lattice constant  $5.43 \text{ \AA}$

Two-dimensional representation of single crystal silicon at  $T = 0 \text{ K}$ ; all valence electrons are bound to the silicon atoms by covalent bonding

# n-type semiconductor



Two-dimensional representation of a silicon lattice doped with a phosphorus atom and valence electron **e<sup>-</sup> mobile carrier**

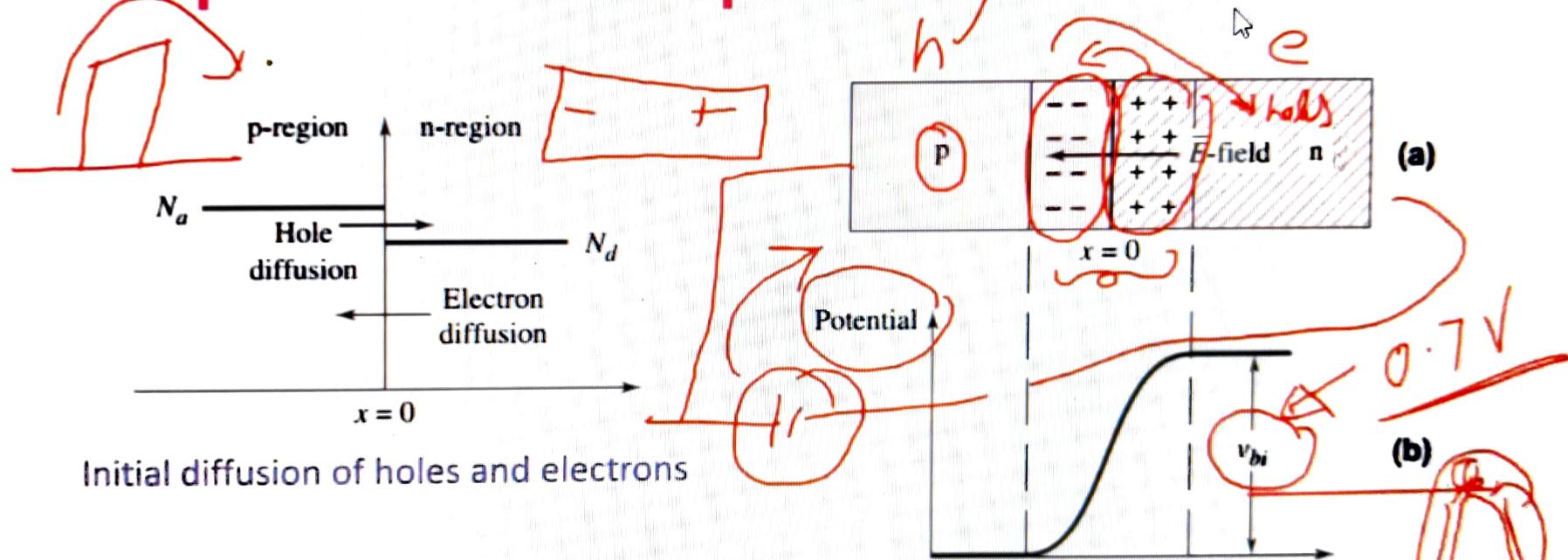


the resulting positively charged phosphorus ion after the fifth valence electron has moved into the conduction band  
**P<sup>+</sup> immobile ion**

# The pn Junction

0.7 V

## Operation with Open-Circuit Terminals

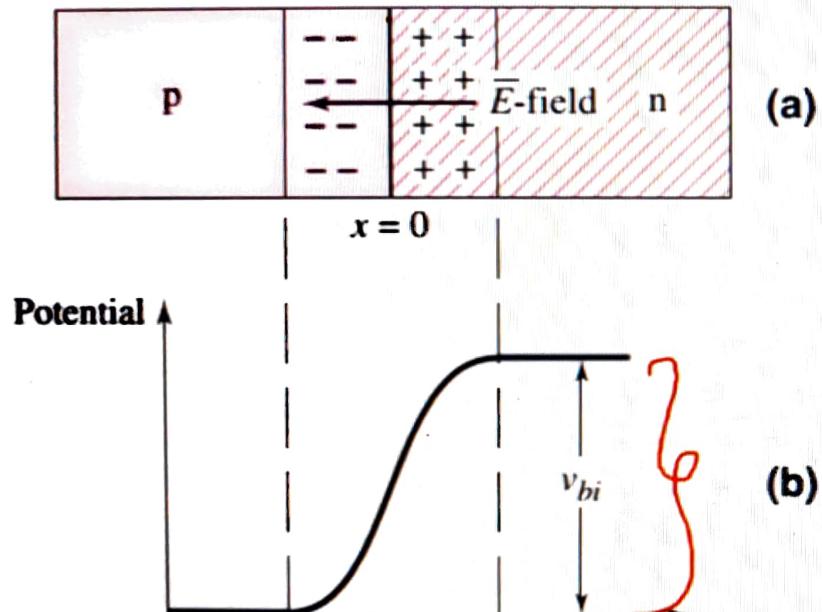


The pn junction in thermal equilibrium.

(a) The space charge region with negatively charged acceptor ions in the p-region and positively charged donor ions in the n-region; the resulting electric field from the n- to the p-region.

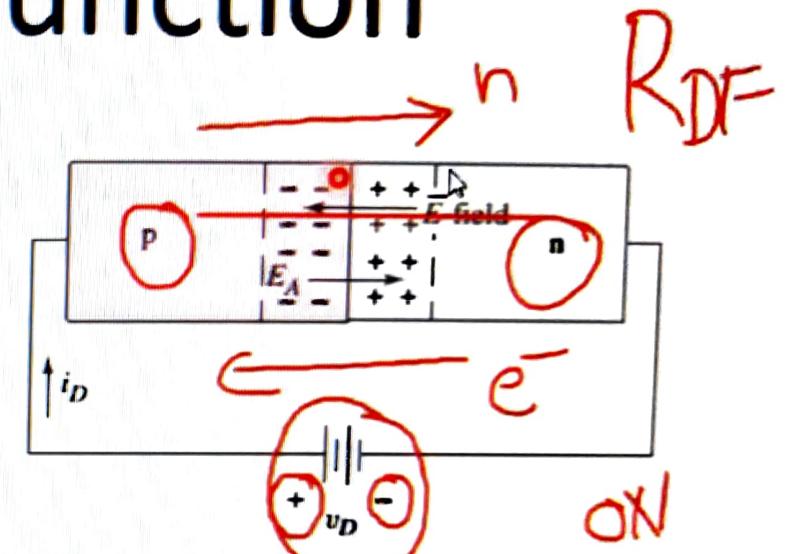
(b) The potential through the junction and the built-in potential barrier  $V_{bi}$  across the junction.

# Built-in potential



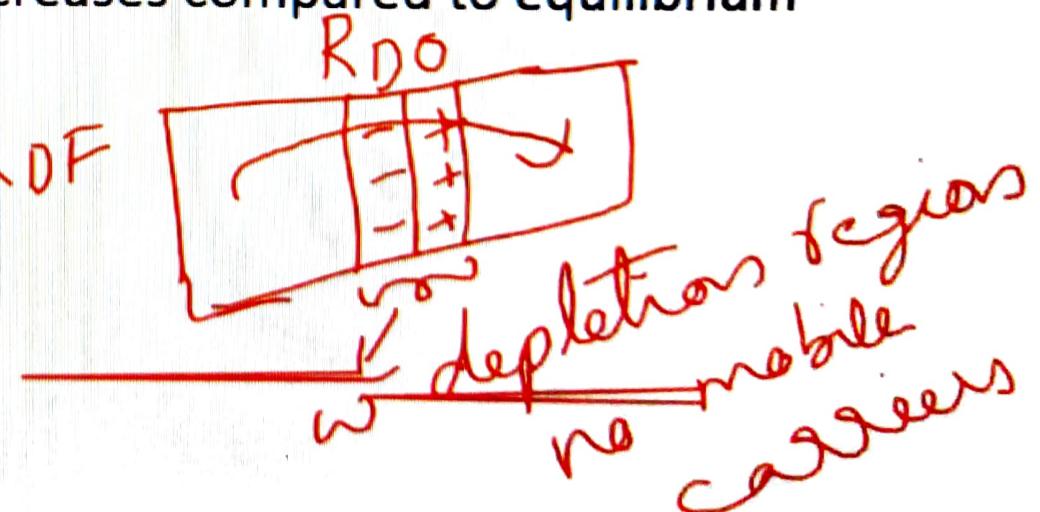
# Forward-Biased pn Junction

- If a positive voltage  $v_D$  is applied to the p-region and negative voltage to n-region then the diode is forward biased



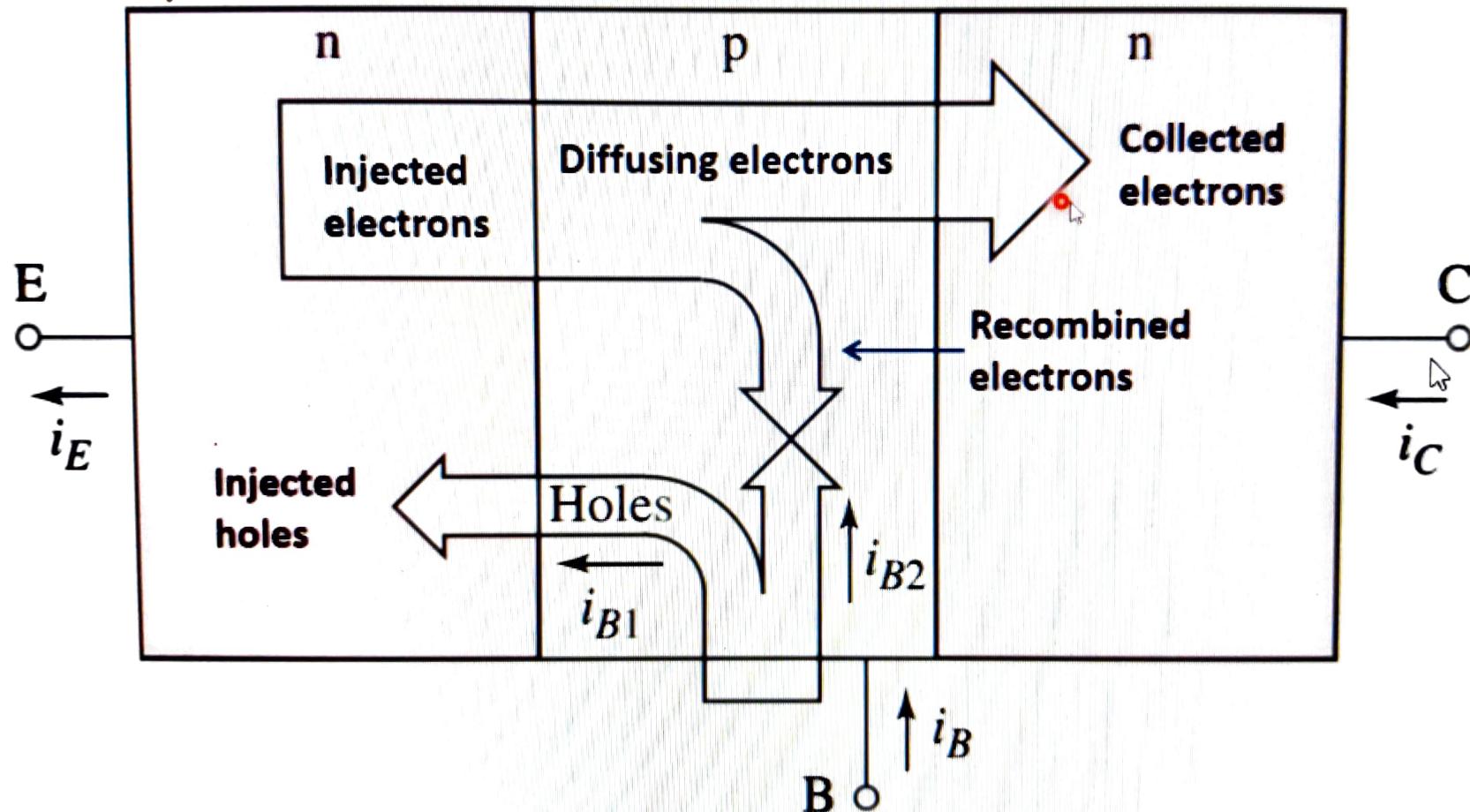
- Majority carrier electrons from the n-region diffuse into the p-region, and majority carrier holes from the p-region diffuse into the n-region
- Width of the space charge region decreases compared to equilibrium width of the space charge region

$$R_{DD} > R_{DF}$$



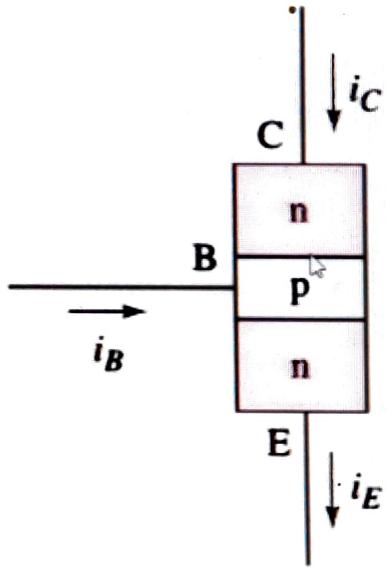
# Currents in npn BJT

Doping levels: Emitter high, collector low and base moderate

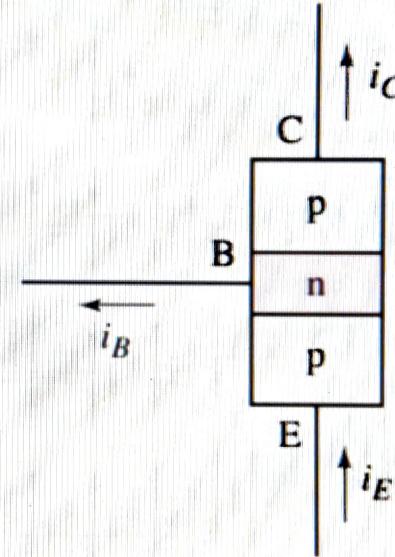
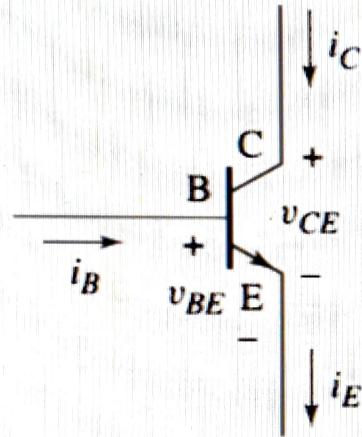


Electron and hole currents in a npn bipolar transistor biased in the forward-active mode

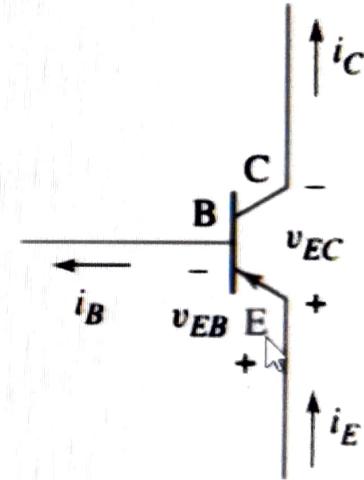
# Block diagrams and circuit symbols for BJT



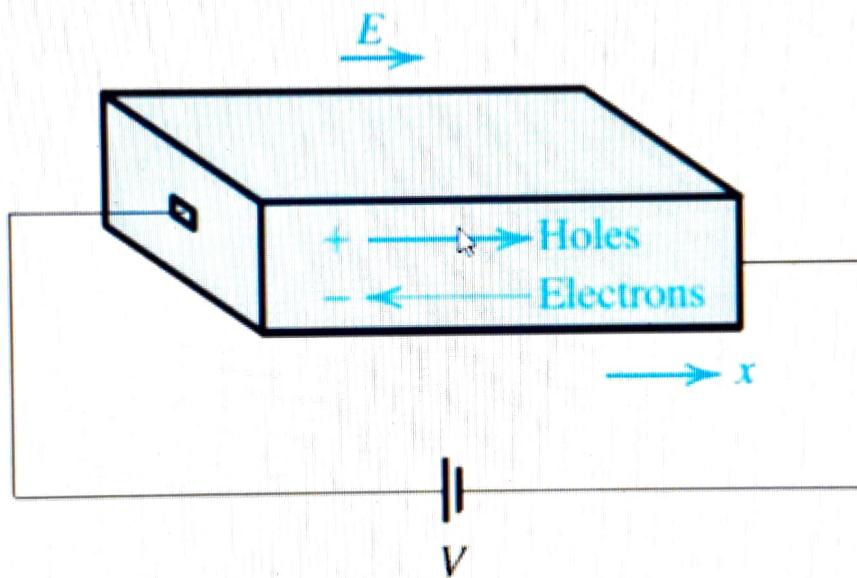
NPN transistor



PNP transistor



# Directions of applied electric field and resulting carrier drift velocity and drift current density



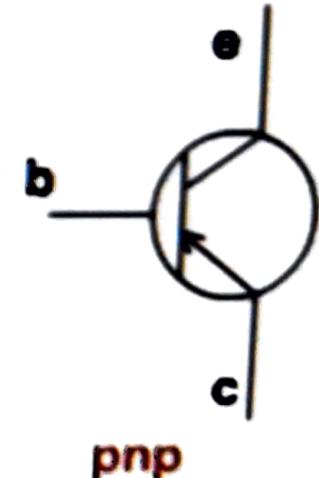
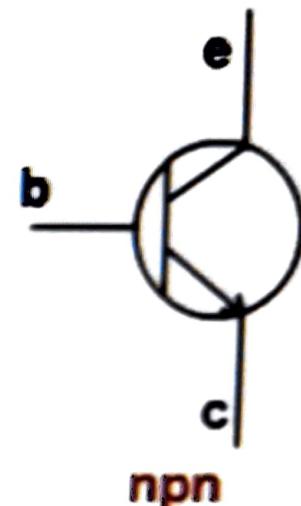
An electric field  $E$  established in a bar of silicon causes

- the holes to drift in the direction of  $E$  and
- the free electrons to drift in the opposite direction of  $E$
- Both the hole and electron drift currents are in the direction of  $E$

# Currents in BJT

- The total current flowing into the transistor must be equal to the total current flowing out of it
- the emitter current  $I_E$  is equal to the sum of the collector ( $I_C$ ) and base current ( $I_B$ )
- $I_E = I_C + I_B$
- $I_C = \alpha I_E$
- $I_C = \beta I_B$
- $\alpha$  = Common base current gain
- $\beta$  = Common emitter current gain

$$\beta = \frac{\alpha}{1 - \alpha}$$



# Problem

**Objective:** Calculate the collector and emitter currents, given the base current and current gain.

Assume a common-emitter current gain of  $\beta = 150$  and a base current of  $i_B = 15 \mu\text{A}$ . Also assume that the transistor is biased in the forward-active mode.

**Solution:** The relation between collector and base currents gives

$$i_C = \beta i_B = (150)(15 \mu\text{A}) \Rightarrow 2.25 \text{ mA}$$

and the relation between emitter and base currents yields

$$i_E = (1 + \beta)i_B = (151)(15 \mu\text{A}) \Rightarrow 2.27 \text{ mA}$$

the common-base current gain is

$$\alpha = \frac{\beta}{1 + \beta} = \frac{150}{151} = 0.9934$$