



Cloud based Analog IC Design Hackathon

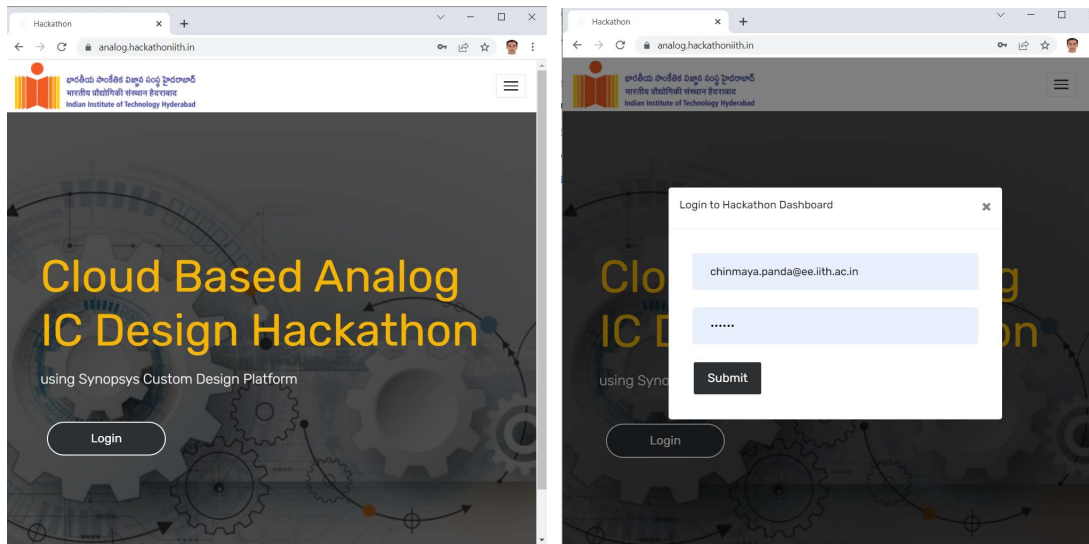
Lab access procedure

Prepared by: Chinmaya panda (IIT Hyderabad) & Kunal Ghosh (VLSI System Design)

Please follow the steps to open Synopsys tools in cloud server

1- Open below webpage & click on login button

<https://analog.hackathoniiith.in/>



2- Give your login details and press the Submit button.


Note: You will get 2 login IDs one is for lab details having webpage & another one is for server XRDP remote connection.


Webpage login ID will be your email ID & password is **vsdiat**
(Please don't use the server login details in web login page)

3- In your home page you can find various information. If you will go to your Courses menu, then you can find the lab usage and design related videos
Check the lab access video to watch the recorded version of this document.

Hackathon x +

analog.hackathoniith.in/home

 భారతీయ ప్రాచీనత విజ్ఞాన సంస్థ హైదరాబాద్
भारतीय प्रौद्योगिकी संस्थान हैदराबाद
Indian Institute of Technology Hyderabad


Chinmaya Panda

OVERVIEW

- Home
- Profile
- Lab Instance
- Courses
- Assessments
- Feedback
- Change Password
- Logout

ANNOUNCEMENTS


Platform Login ID - Your email ID
Password - vsdial

Lab Credentials

Lab Login ID - First Name (which you had used while registering for the workshop)
Password - vsdial

4- Click on Lab Instance to open the remote server.
(Wait till you will get below image.)

Login to snps-analog-hackathon

 Just connecting

Session:

username:

password:

OK Cancel

5- Give your username (first section of your email ID)

Example: if your email id is kunalpghosh@gmail.com, then your <username> is

Username is: **kunalpghosh**

Password is: **vsdiat**

Note:(Please use your username not the above mentioned one)

6- After successful login you should be able to see the below screen.

It is your Desktop in CentOS operating system.

Then right click and create a New Folder, where you will save all of your designs



Example: We have created one folder named **analog_ic_hackathon**

7- Then go to that folder by double clicking & open terminal by right clicking.

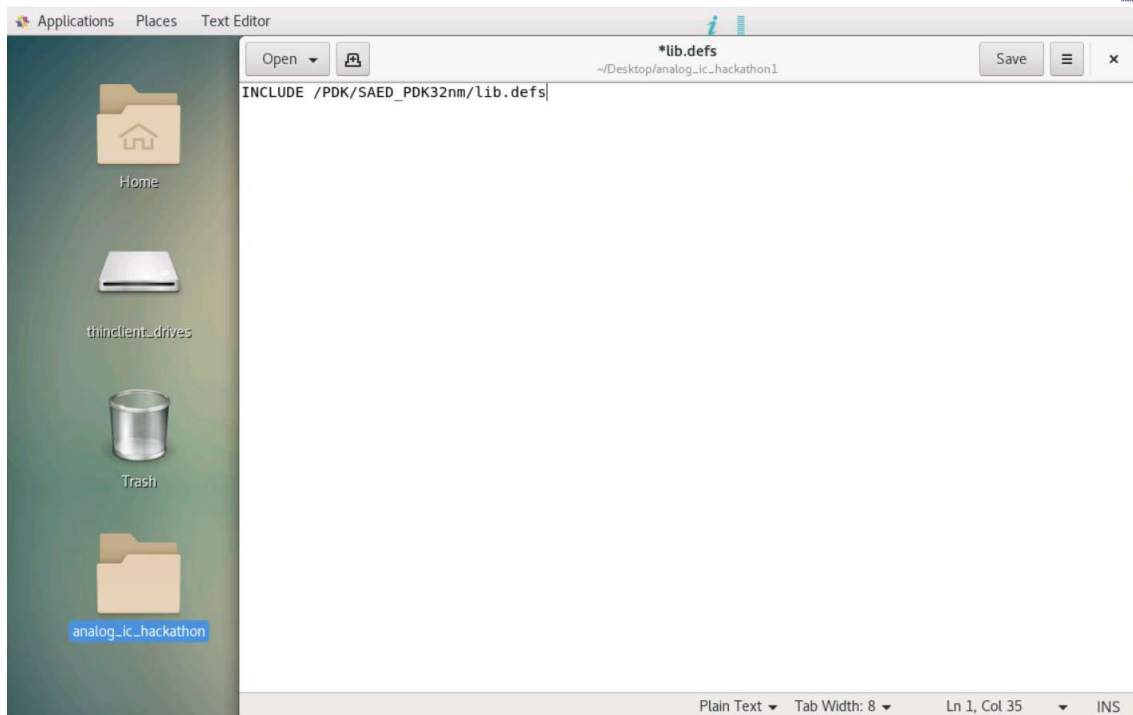
(Right click ->Open in Terminal)

Then write below command to create one library definition file

gedit lib.defs

Then write the below line, save by clicking right top save button and close the text editor.

INCLUDE /PDK/SAED_PDK32nm/lib.defs



8- Type below commands in same terminal

tcsh

source /Applications/Synopsys/cshrc_syn_2021.09

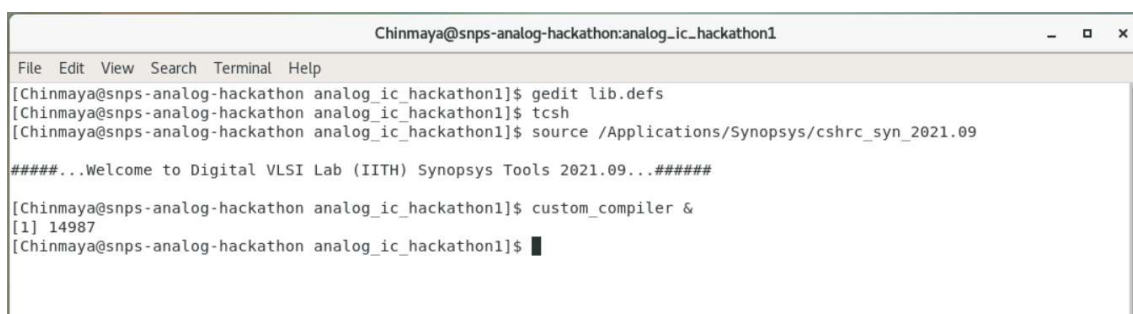
You should get one welcome message if all the spellings are correct.

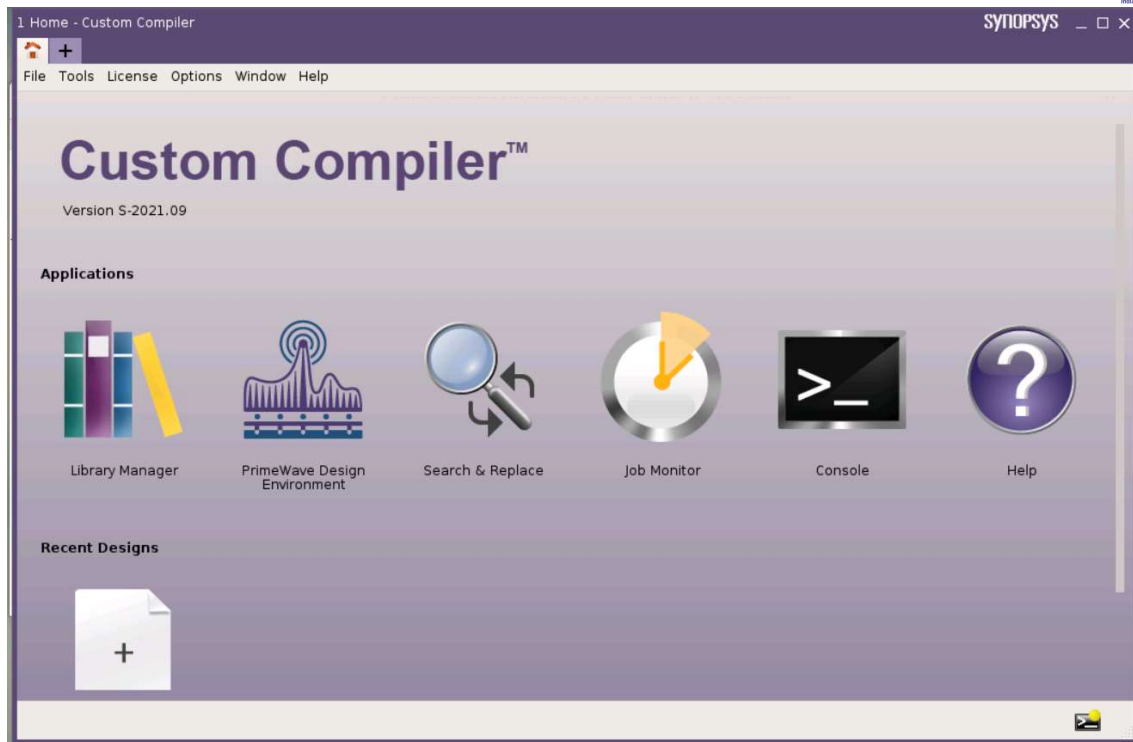
9- Now you are ready to open the Synopsys custom compiler by writing below command

custom_compiler &

You should get below screen

Note: maximum time you will get error if you have skipped or written wrong or spelling mistake in any step mentioned in point 7, 8 & 9





10- Then follow the instructions provided in the webpage to start your design.