LNCT UNIVERSITY, BHOPAL

Enrolment No............

CS-103
B.TECH CSE I SEMESTER
EXAMINATION [JAN-2024]
COMPUTER ARCHITECTURE

Maximum Marks:70

Time Allowed :3Hours

Note:-Attempt all questions internal choice are given.

(SECTION –A)

1. **Short Answer Type Questions(Attempt Any Five) [5x6=30]**

   i. Write uses of Flynn's classification.

   ii. What are different Mapping schemes?

   iii. Explain the General register organization.

   iv. Write short notes on improving the cache memory performance method.

   v. Explain First in, First out, and Buffer.

   vi. Explain Bus and Memory Transfers.

   vii. What is Parallel Processing?

(SECTION –B)

2. **Long Answer Type Questions (Attempt Any Four) [04x10=40]**

   i. Describe in brief.

      a. LRU algorithms.

      b. Memory Management Hardware

   ii. Explain the basic principle of computer architecture.

   iii. Draw the instruction format and explain.

   iv. What is Virtual Memory? Write it's significance in memory organization.

   v. Explain Flynn's classification of parallel processing in detail.

   vi. Differentiate between register reference instruction and memory reference instruction. Explain each in detail

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