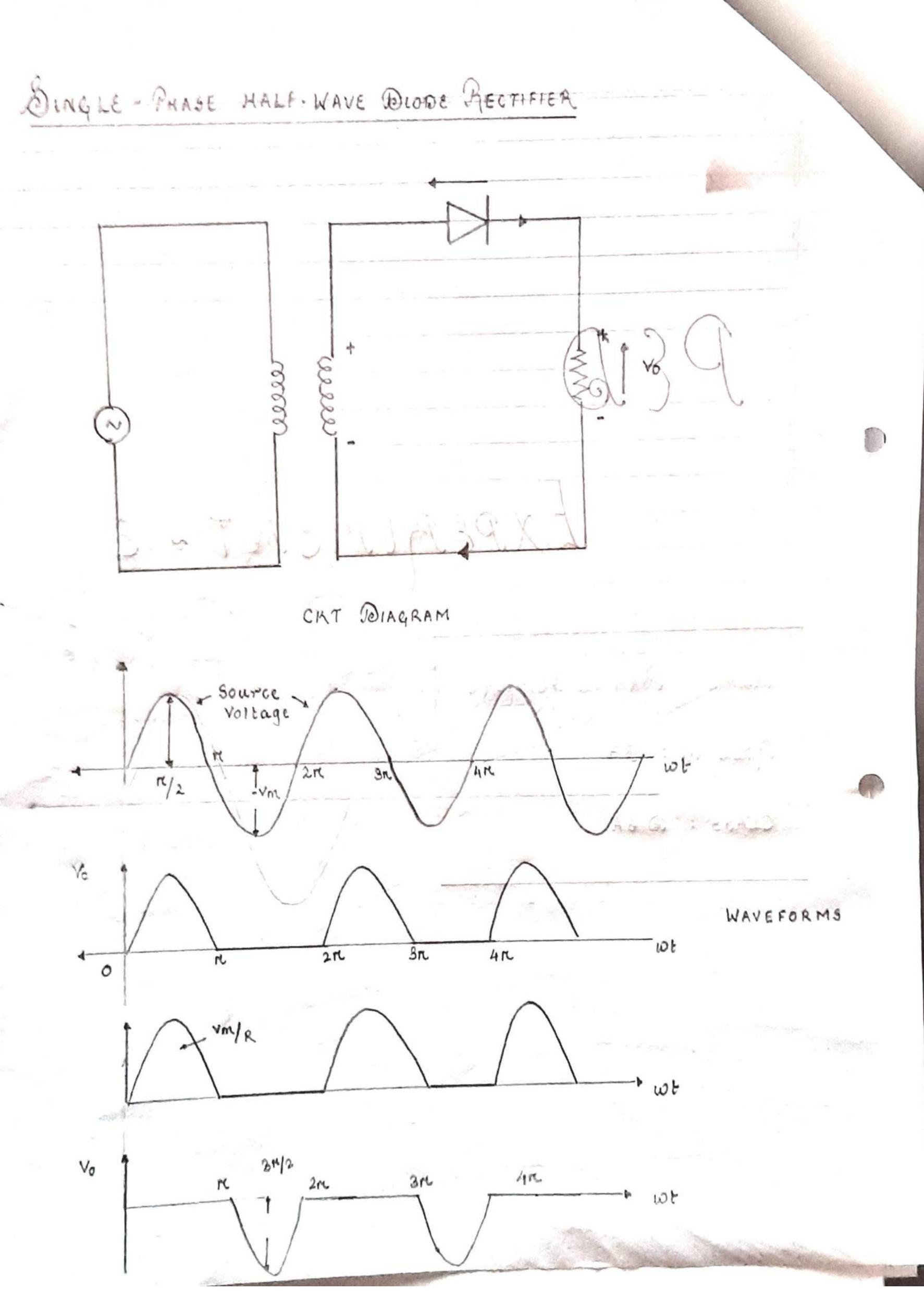
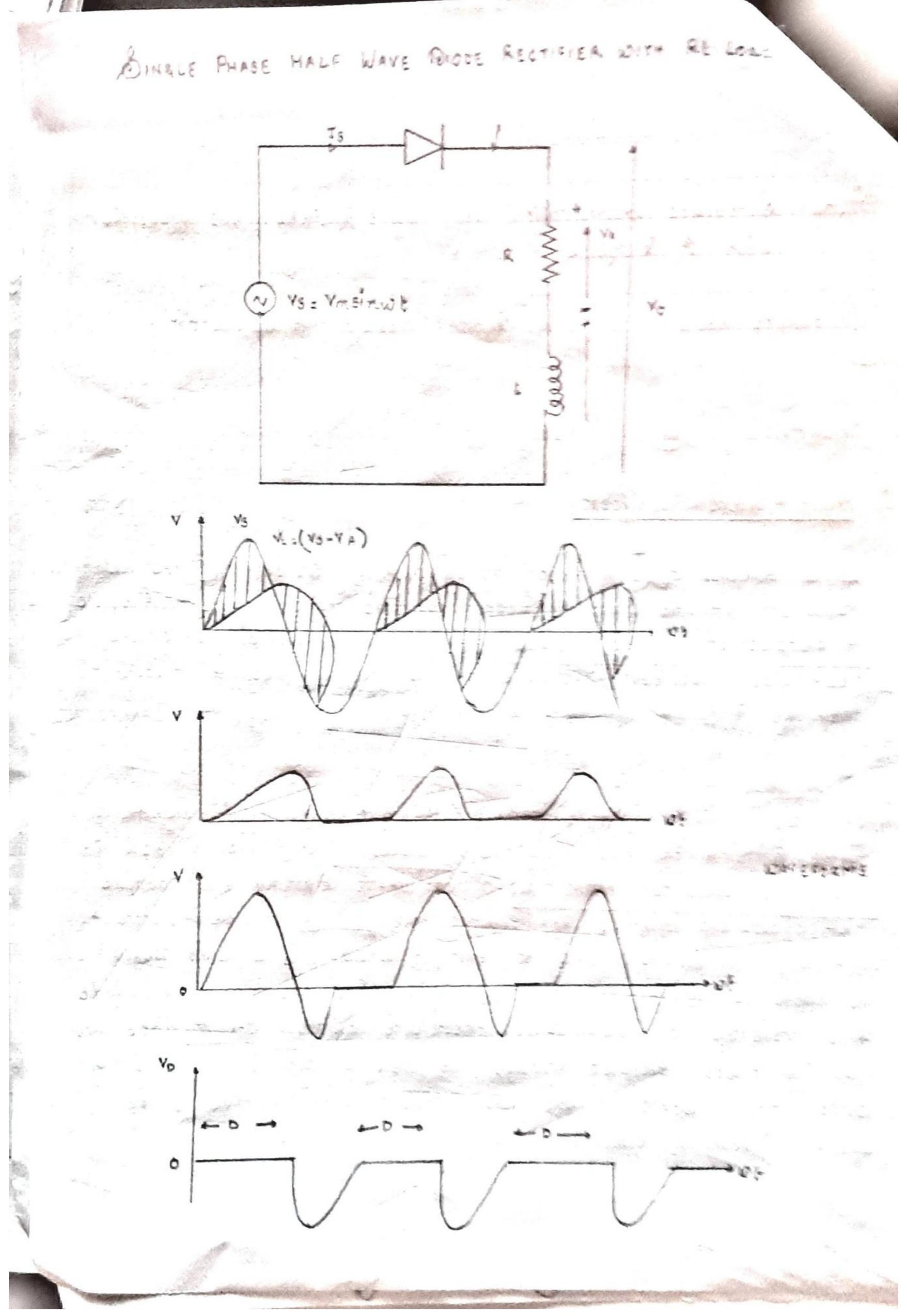


Scanned by TapScanner



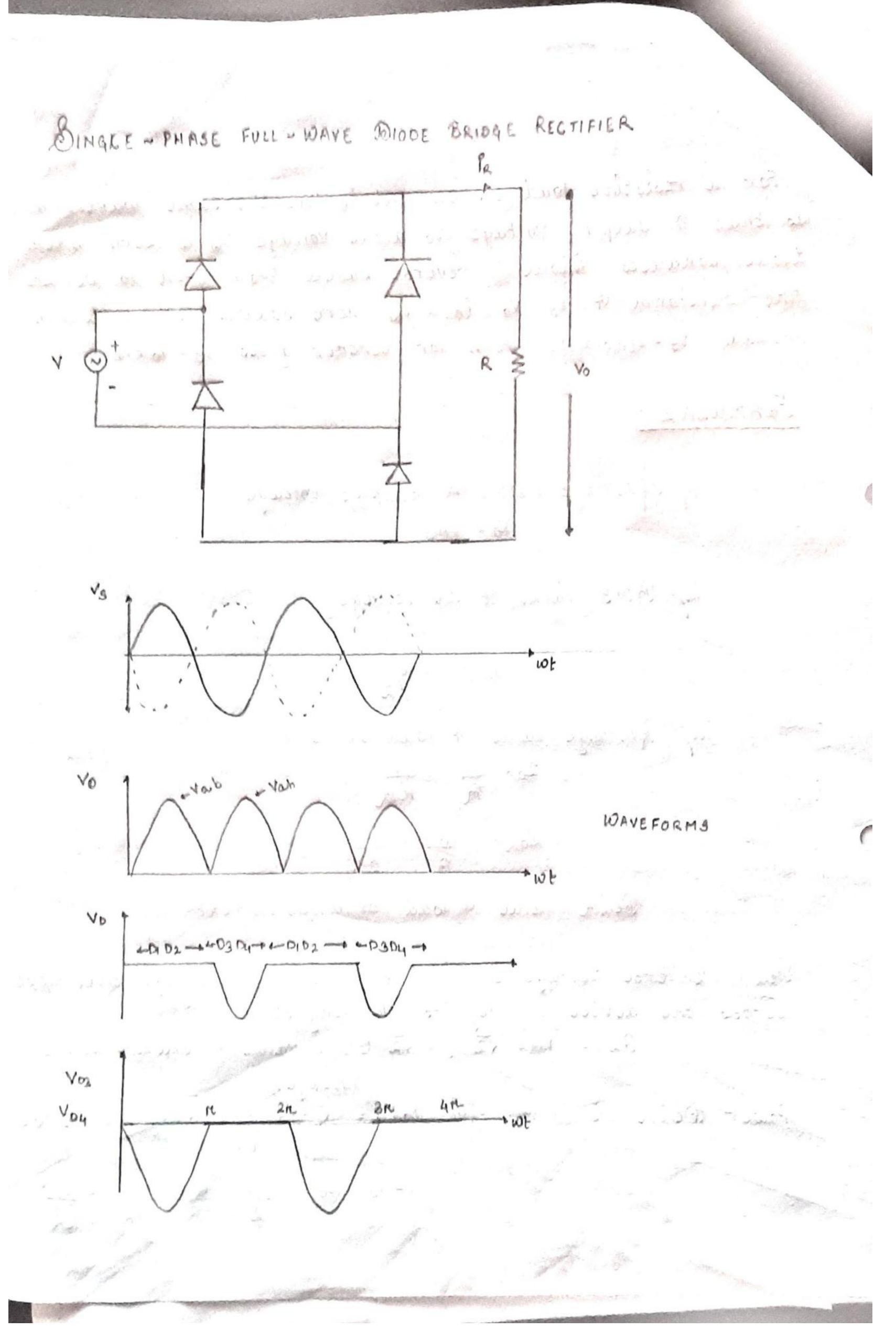
Scanned by TapScanner

1	
A	in: Bimulate uncontrolled Hall Wave & Full Wave Bectifier with B & BL Load
	water of a fix house
8	Doftware Used: Multisim, Diode, Resistor & Inductor.
	Theory:
01	11-18 1/2 10 Daniel's or
67	Half - Wave Rectifier:
	of output or voltage As such it is also called single-phase one-pulse rectifier. The load on the output side of rectifier may be A AL or AL with a flywheel diade.
	A Load:
- W	cht diagram of a single-phase half-wave rectifier is shown in
	fig a During the tve half cycle, diode is forward biased by therefore conducts from wt=0° to wt= n. During the positive
	half cycle olp voltage Vo= source voltage Vs & load ourrent
	io = Vo/A 1t wt = n, Vo=0 & for A Load, & 20 & also zero.
	15 soon as Vs tends to become negative after wt=r, dlode
	Dis reverse plased, le les therefore turned off & goes lonto
	blocking state. Output Voltage as well as olp current are
	zero from wt=n to wt=2n. After wt=2n, dode es
	again forward biased.
daram	FOR EDUCATIONAL USE



Scanned by TapScanner

-	
	as that of output voltage va diada vallage verticas
1	reverse based from int = n to int = 2 n
	The waveform of vs, vo, io & vp. Here source voltage Is sinu- soidal vs - Vinsinut KVL for circuit gives vs = vo + vp
	FORMULAE:
	of the second se
	i) Frerage Value & Output voltage
	$V_0 = V_m/n$
	ii) RMS value & olp voltage
	$V_{OR} = V_{m}/2$
	iii) Average value of load current
	$\frac{\mathcal{I}_0 = V_0}{\mathcal{R}} = V_{\mathcal{M}}$
	IOR = Vor = Vm
	R 2R
	Peak value of load, or diode current: Vm/R
	Peak Inverse voitage (PIV) is the maximum voitage that appe
	across the device during its blocking state.
	$PIV = Vm = \sqrt{2} Vg = \sqrt{2}$ (rms value of transform secon voltage)
	Power Delivered to resistive load = (rms load voltage) (rms loa
	current
	$= V_{OP} I_{OP} = V_{M} V_{M} = V_{M}^{2} = V_{5}^{2}$ $= V_{OP} I_{OP} = V_{M} V_{M} = V_{M}^{2} = V_{5}^{2}$ $= V_{OP} I_{OP} = V_{M} V_{M} = V_{M}^{2} = V_{5}^{2}$ $= V_{OP} I_{OP} = V_{M} V_{M} = V_{M}^{2} = V_{5}^{2}$
ram	FOR EDUCATIONAL USE



Input Power factor. Power delivered to load Vor Ior - Vor 0.707 109 Input VA AL load: A single phase one-pulse diode rectifier feeding AL Load current continues to flow-even after source voltage vs has become negative, because of presence of inductance l'in the load ont voltage VA- ion has the same waveshape as that of io Inductor voltage VL. Vs-Va The current io flows till 2 areas A&B are equal Area A represents the energy stored by h. & area B the energy released by h. It must be noted that average value of voltage VL across Enductor L Es Lero. when bord at wir B; VL=0, VA=0 & voltage Vs appears as reverse bias across diode as shown. At B, voltage Vp across diode jumps from zero to VmsinB where B>n. Here B=7 is also the conduction angle of the dlode. Average value of olp voltage. Vm (1-cosp) Average value of load or olp current Io= vo = vm (1-cos B) li Full-wave Rectifier: A single phase full wave bridge rectifier employing diodes is shown when "a' is tre wirt 66°, diodes De ne conduct together so that old voltage is vab Each of the diodes D3 & D4 is subjected to reverse voltage of Vs. when b'is tre wirt to "a", diodes Poz. Dy conduct together & output voltage Vba. Each of the 2 diodes 101 & 102 experiences a reverse voltage of Vs. Johnulne: Average value of olp is given by 1/2 S Vm. sinwtolwto Vm, (1-coswtrr)

Conclusion: Bimulation of uncontrolled half & full wave rectifier have been executed successfully on Multisim and are well comprehended.