

A

B

C

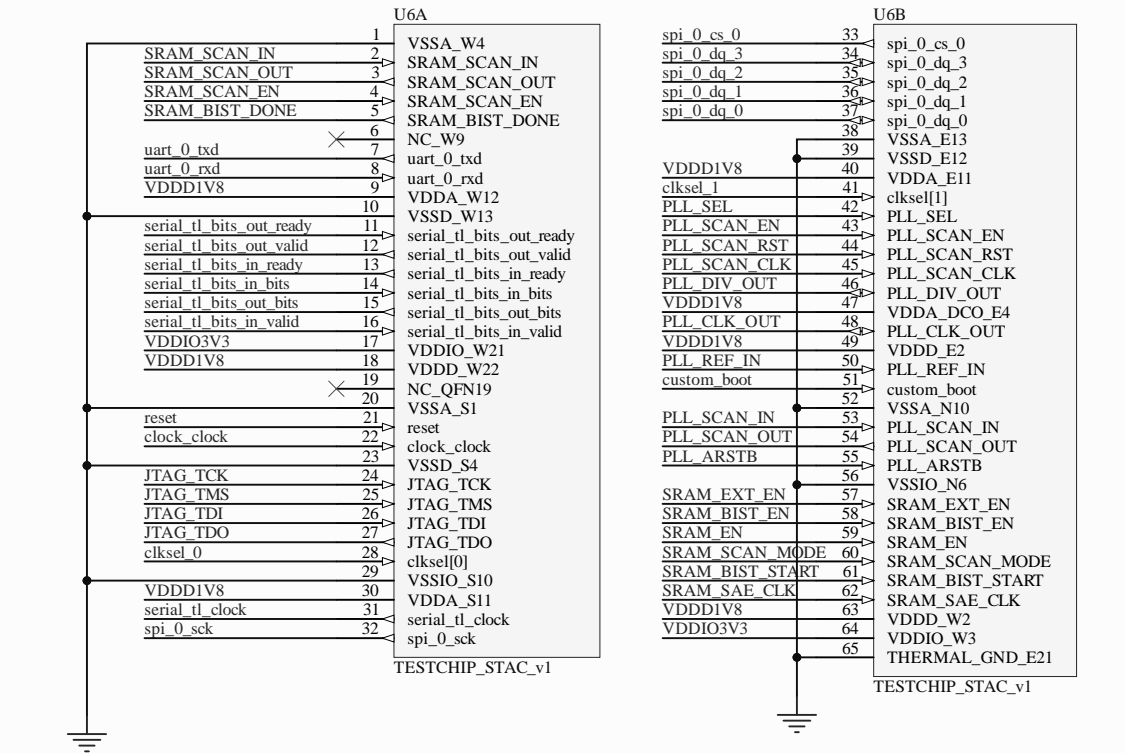
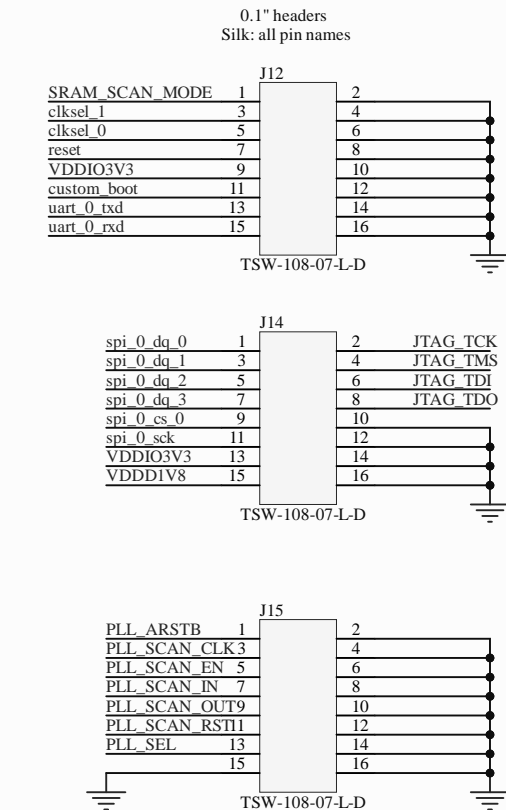
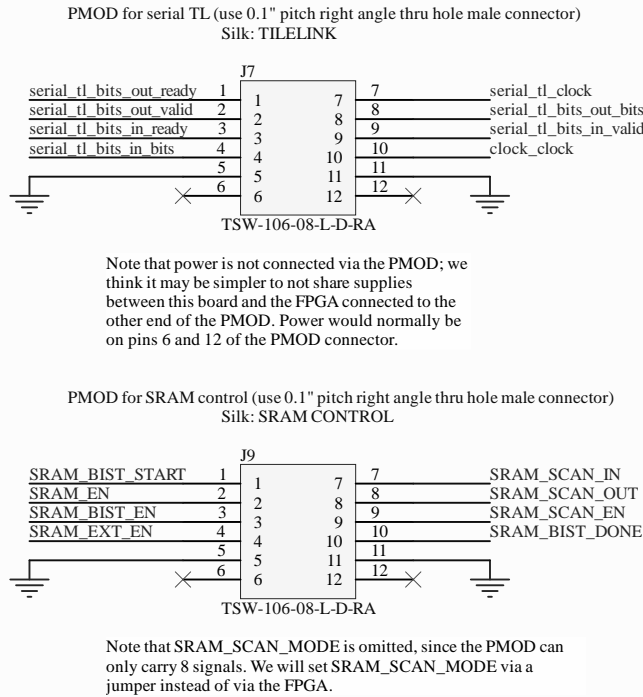
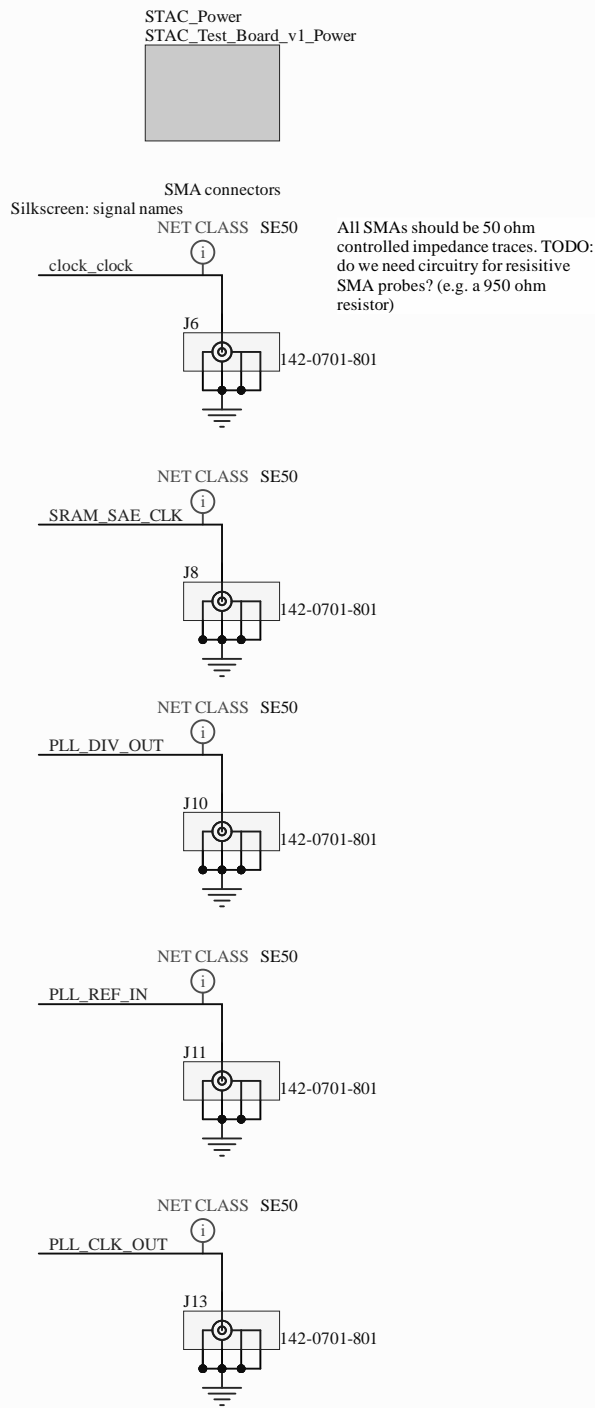
D

A

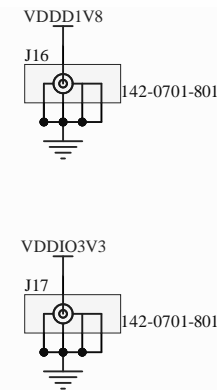
B

C

D

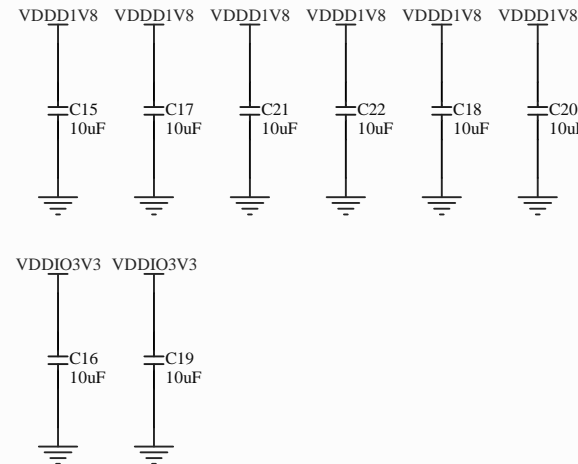


SMA connector for cable to spectrum analyzer (using an SMA-connectorized DC blocking capacitor at the spectrum analyzer's input) for measuring noise on these power domains.



Note that the VDDA pins on the STAC chip are connected to VDDDD1V8. This is because no on-chip circuitry uses VDDA.

All caps close to chip package (1 cap per pin)



Title		
STAC-V1 Top		
Size	Number	Revision
B		X1
Date:	10/26/2023	Sheet 1 of 2
File:	C:\Users\...\STAC_Test_Board_v1.SchDoc	Drawn By: Rahul Kumar

