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## **GUJARAT TECHNOLOGICAL UNIVERSITY**

BE - SEMESTER-VII (NEW) EXAMINATION - WINTER 2021

Subj	ect	Code:3170701 Date:22/1	2/2021
•		Name:Complier Design	
•		:30 AM TO 01:00 PM Total Ma	rks• 70
Instru			113. 70
		Attempt all questions.	
		Make suitable assumptions wherever necessary.	
		Figures to the right indicate full marks.	
	4.	Simple and non-programmable scientific calculators are allowed.	
			MARKS
Q.1	(a)	Define following terms:	03
	()	i. Compiler	
		ii. Interpreter	
		iii.Token	
	<b>(b)</b>	Explain activation tree?	04
	(c)		07
	(0)		
0.2	( )		0.2
Q.2		Explain input buffering methods.	03
	<b>(b)</b>		04
		i. Augmented Grammar	
		ii. LR(0) Item	
	(a)	iii.LR(1) Item  Draw the DFA for the regular expression (a b)*abb using set	07
	(c)		U7
		construction method only.  OR	
	(c)		07
	(0)	it into DFA. (a   b)* a b* a	U7
Q.3	(a)		03
Q.C	(b)		04
	()	$S \rightarrow 1AB \mid \epsilon$	
		$A\rightarrow 1AC \mid 0C$	
		$B\rightarrow 0S$	
		$C\rightarrow 1$	
	(c)	Explain operator grammar. Generate precedence function table for	07
		following grammar.	
		E -> EAE   id	
		A -> +   *	
		OR	
Q.3	(a)	Differentiate Top Down Parsing and Bottom up parsing	03
	<b>(b)</b>		04
	<b>(c)</b>		07
		S -> aSA   €	
0.4		A -> bS   c	0.2
<b>Q.4</b>	(a)		03
	<b>(b)</b>		04
		<ol> <li>Call-by-value</li> <li>Call-by-reference</li> </ol>	
		3. Copy-Restore	
		4. Call-by-Name	
	(c)	Explain Peephole Optimization.	07

<b>Q.4</b>	(a)	Draw a DAG for expression: $a + a * (b - c) + (b - c) * d$ .	03
_	<b>(b)</b>	Compare: Static v/s Dynamic Memory Allocation.	04
	<b>(c)</b>	Translate following arithmetic expression	07
		-(a*b)+(c+d)-(a+b+c+d) into	
		1] Quadruples	
		2] Triple	
		3] Indirect Triple	
Q.5	(a)	Explain symbol table. For what purpose, compiler uses symbol table?	03
	<b>(b)</b>	Explain Basic-Block Scheduling.	04
	(c)	Explain synthesized attributes with the help of an example.	07
		OR	
Q.5	(a)	Define a following:	03
		i. Basic block	
		ii. Constant folding	
		iii. Handle.	
	<b>(b)</b>	Write difference(s) between stack and heap memory allocation.	04
	(c)	Explain Pass structure of assembler.	07
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