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Batch-04

Tutorial 2

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Computer Architecture & Microprocessors

Final Answers -

Q.1 \rightarrow B

Q.2 \rightarrow A

Q.3 \rightarrow D

Q.4 \rightarrow B

Q.5 \rightarrow D

Q.6 \rightarrow 256

Q.7 \rightarrow C

Q.8 \rightarrow 1023

Q.9 \rightarrow C

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Q.1. The ASCII code for letter A is,

$$65 = \underline{\underline{(1000001)_2}}$$

Option: (B)

Q.2. $\overline{(A+B)} + \overline{C} = (A+B)C$ — Option: (A)

Q.3. A negative number can be represented by any of the forms shown in (A), (B) or (C) but 2's complement is used in computers. \Rightarrow All of the above.

Option: (D)

Q.4. No. of RAM chips = $\frac{2048 \times 8}{128 \times 8} = \underline{\underline{16}}$

Option: (B)

Q.5. $A \oplus B \oplus C$; $A \odot B \odot C$

$$\begin{aligned} A \oplus B \oplus C &= (A\bar{B} + \bar{A}B) \oplus C \\ &= (A\bar{B} + \bar{A}B)\bar{C} + \overline{(A\bar{B} + \bar{A}B)}C \\ &= \underline{A\bar{B}\bar{C}} + \underline{\bar{A}B\bar{C}} + \underline{\bar{A}\bar{B}C} + \underline{ABC} \end{aligned}$$

$$\begin{aligned} A \odot B \odot C &= (AB + \bar{A}\bar{B}) \odot C \\ &= (AB + \bar{A}\bar{B})C + \overline{(AB + \bar{A}\bar{B})}\bar{C} \\ &= \underline{ABC} + \underline{\bar{A}\bar{B}C} + \underline{A\bar{B}\bar{C}} + \underline{\bar{A}B\bar{C}} \end{aligned}$$

\Rightarrow It holds for all values

\Rightarrow Option: (D)

Q.6. 11 bit computer $\Rightarrow 2^{11}$ encodings.

Address field is 4 bits long

5 2-address instructions = $5 \times 2^4 \times 2^4 = 5 \times 2^8$ encodings.

32 1-address instructions = $32 \times 2^4 = 2^9$ encodings.

\Rightarrow No. of zero-address instructions

$$= 2^{11} - [5 \times 2^8 + 2^9]$$

$$= 8 \times 2^8 - 5 \times 2^8 - 2 \times 2^8$$

$$= 2^8$$

$$= \underline{\underline{256}}$$

Q.7 \rightarrow Instruction opcode is an instruction code, not a type of memory.

Option: (C)

Q.8 \Rightarrow Processor has 128 different instructions

\Rightarrow 7 bits of instructions.

No. of bits available for operand field = 7 (given)

\Rightarrow Remaining bits = $24 - (7 + 7)$
 $= \underline{\underline{10}}$

\Rightarrow The maximum value stored in a 10 bit register
 $= (1111111111)_2 = \underline{\underline{1023}}_{10}$

Q.9. Instruction size = 16 bit.

Size of address field = 7 bits.

No. of total encodings = 2^{16}

No. of encodings for 2 2-address = $2 \times 2^7 \times 2^7 = 2^{15}$

No. of encodings for 250 1-address = $250 \times 2^7 = 125 \times 2^8$

\Rightarrow Remaining = $2^{16} - (2^{15} + 125 \times 2^8)$

$$= (28 - 125) \times 2^8 = 3 \times 2^8 = \underline{\underline{768}}$$

Option: (C)