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Tricky Circuit (Proteus)

OBJECTIVE:

Participants must design, debug, or simulate challenging electronic circuits using Proteus software. The tasks may include completing circuit designs, or building custom solutions — all under time constraints.

TEAM:

• Each team must consist of 1-2 players.

• All players must be students of the same or affiliated institute.

• One team member must act as captain and will be the only point of contact with organizers.
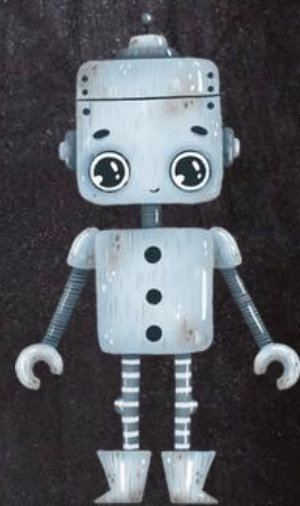
RULES:

1. The competition involves Full design challenge/Debugging in given time limit (typically 30–45 minutes).
2. All teams will be assigned identical challenges.
3. Internet usage is strictly prohibited during the challenge.
4. Teams will submit their final Proteus project file (.pdsprj) within the time limit
5. Judges may ask participants to explain their design logic.
6. All circuit outputs must match given input/output specs or expected simulation behavior
7. No use of AI assistants or code copiers (e.g., ChatGPT, Copilot) during the event

SOFTWARE REQUIREMENTS:

Proteus 8 Professional or later (provided on event PCs)

Challenge Types

- Design Completion: Complete an incomplete circuit (e.g. Timer circuits, sensor simulations, MCU-based systems (e.g., blinking pattern, UART simulation)).
- Simulation Output Match: Match simulation behavior to a given waveform, logic truth table, or output timing
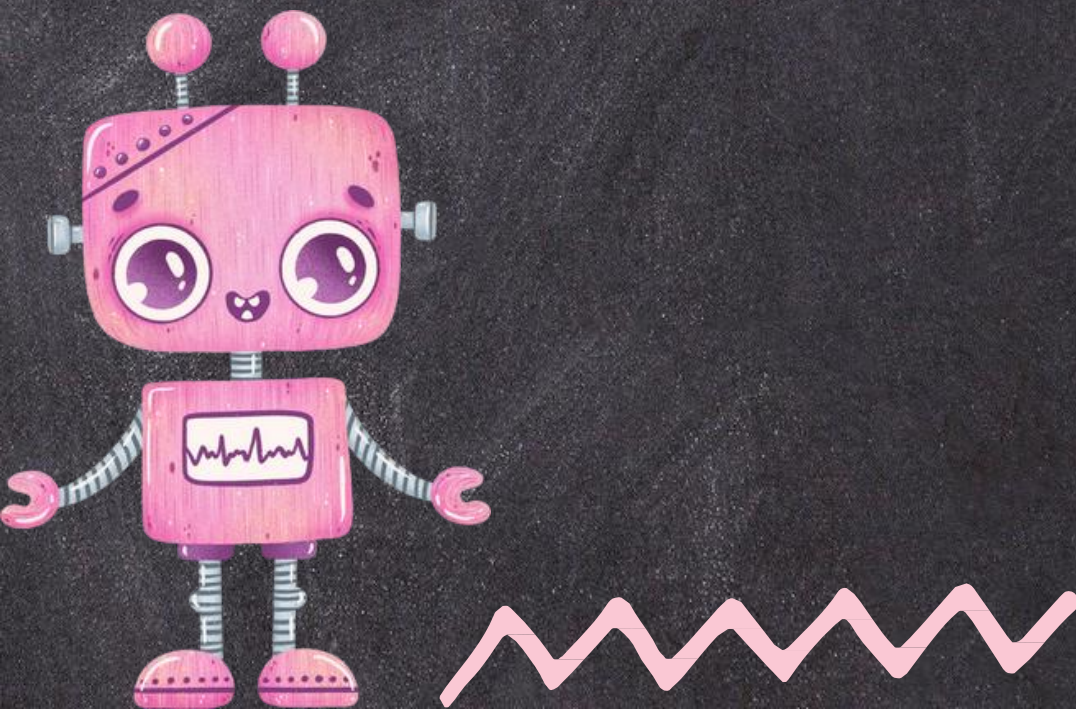
SCORING CRITERIA

Max Score per round: 100

Criteria	Points
Correct Simulation Output	50
Neat & Logical Schematic	30
Explanation & Debugging Strategy	20

PENALTIES:

Issue	Penalty
Late submission	-5 per minute
Logic incorrect but syntactically valid	-10
Using external help (AI/web)	Disqualification

DISQUALIFICATION:

- Use of mobile phones or internet during challenge
- Submission of plagiarized or copied circuits
- Misbehavior with organizers or other teams
- Exceeding round time by more than 5 minutes

WINNING CRITERIA:

- Top scores will win.
- In case of tie: Faster submission wins

VENUE: LAB 38

For any queries contact:

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