SVKM'S NMIMS

MUKESH PATEL SCHOOL OF TECHNOLOGY MANAGEMENT ENGINEERING / SCHOOL OF TECHNOLOGY MANAGEMENT ENGINEERING

Program: B. Tech (AI /AI & DS/ AI & ML/ Computer) & MBA

Year: II/III

Semester: III/V

Tech (AI/ Computer)

AcademicYear: 2022-2023

Subject: Operating Systems

Marks: 100

Time: 10.00 am - 1.00 pm

Date: 04 February 2023

Durations: 3 (Hrs)

Re-Examination (2022-23)

No. of Pages: 2

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Instructions: Candidates should read carefully the instructions printed on the question paper and on the cover of the Answer Book, which is provided for their use.

1) Question No. _1___ is compulsory.

2) Out of remaining questions, attempt any __4__ questions.

3) In all ___5_ questions to be attempted.

4) All questions carry equal marks.

5) Answer to each new question to be started on a fresh page.

6) Figures in brackets on the right hand side indicate full marks.

7) Assume Suitable data if necessary.

	Answer briefly:					[20]
a.	Explain critical section problem in detail.					[05]
b.	What are the procedures that I/O subsystem supervises?					[05]
c.	Explain the difference between long term and short term and medium term schedulers.					[05]
d.	What do you understand by context switching? Explain swapping of two processes in detail with diagram.					[05]
					2	
a.	Identify the problem associated with bounded buffer. How to solve this problem using semaphore elaborate with suitable example?					[10]
b.	What is the need of page replacement algorithms? Elaborate all the types of page replacement algorithms. Find page faults in LRU algorithm for the order 6, 7, 8, 9, 6, 7, 1, 6, 7, 8, 9, 1, 7, 9, 6 with available frames as 3.					[10]
a.	What is difference Consider the follow in milliseconds:	Process P1 P2 P3 P4	preemptive and rocesses with the Burst Time 4 2 5	non-preemptive e length of the CP Arrival Time 0 1 2 3	CPU scheduling? U burst time given	[10]
	a. b. c. d.	a. Explain critical sectors. b. What are the process. c. Explain the different d. What do you under detail with diagram. a. Identify the problem semaphore elaborate b. What is the need of replacement algority, 1, 6, 7, 8, 9, 1, 7, 1, 6, 7, 8, 9, 1, 7, 1, 6, 7, 8, 9, 1, 7, 1, 6, 7, 8, 9, 1, 7, 1, 6, 7, 8, 9, 1, 7, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1,	a. Explain critical section problem b. What are the procedures that I/C c. Explain the difference between d. What do you understand by condetail with diagram. a. Identify the problem associated semaphore elaborate with suitable b. What is the need of page replareplacement algorithms. Find page 7, 1, 6, 7, 8, 9, 1, 7, 9, 6 with average a. What is difference between Consider the following set of prin milliseconds: Process P1 P2 P3	a. Explain critical section problem in detail. b. What are the procedures that I/O subsystem support of the problem associated with bounded by semaphore elaborate with suitable example? b. What is the need of page replacement algorithms. Find page faults in LRI 7, 1, 6, 7, 8, 9, 1, 7, 9, 6 with available frames at Consider the following set of processes with the in milliseconds: Process Burst Time P1 4 P2 2 P3 5	a. Explain critical section problem in detail. b. What are the procedures that I/O subsystem supervises? c. Explain the difference between long term and short term and mediu d. What do you understand by context switching? Explain swapping detail with diagram. a. Identify the problem associated with bounded buffer. How to solve semaphore elaborate with suitable example? b. What is the need of page replacement algorithms? Elaborate all replacement algorithms. Find page faults in LRU algorithm for the 7, 1, 6, 7, 8, 9, 1, 7, 9, 6 with available frames as 3. a. What is difference between preemptive and non-preemptive Consider the following set of processes with the length of the CP in milliseconds: Process Burst Time Arrival Time P1 4 0 P2 2 1 P3 5 2	a. Explain critical section problem in detail. b. What are the procedures that I/O subsystem supervises? c. Explain the difference between long term and short term and medium term schedulers. d. What do you understand by context switching? Explain swapping of two processes in detail with diagram. a. Identify the problem associated with bounded buffer. How to solve this problem using semaphore elaborate with suitable example? b. What is the need of page replacement algorithms? Elaborate all the types of page replacement algorithms. Find page faults in LRU algorithm for the order 6, 7, 8, 9, 6, 7, 1, 6, 7, 8, 9, 1, 7, 9, 6 with available frames as 3. a. What is difference between preemptive and non-preemptive CPU scheduling? Consider the following set of processes with the length of the CPU burst time given in milliseconds: Process Burst Time Arrival Time P1 4 0 P2 2 1 P3 5 2

		Draw the Gantt chart for preemptive SJF and non-preemptive SJF scheduling. Calculate average waiting time and average turn-around time.				
CO-3; SO-1; BL-3	b.	Suppose the requests to be addressed are- 82,170,43,140,24,16,190. And the Read/Write arm is at 50, and it is also given that the disk arm should move "towards the larger value". Find the number of head movements using: I. SSTF Algorithm II. LOOK Algorithm				
Q4						
CO-1; SO- 1,6; BL-2	a.	Discuss the following in brief: I. Tightly Coupled Systems II. System Calls				
CO-3; SO-1; BL-1	b.	Define file and list various file attributes. Explain different allocation methods w.r.t contiguous allocation, linked allocation, indexed allocation.				
Q5			٨			
CO-2; SO- 1,6; BL-3	a.	Consider 3 processes P1, P2 and P3, which require 5, 7 and 4 time units and arrive at time 0, 1 and 3. Draw the Gant chart, process completion sequence and average waiting time for: I. Round robin scheduling with CPU quantum of 2 time units. II. FCFS				
CO-2; SO-2; BL-3	b.	I. A system of 4 processes (P0, P1, P2, and P3) with the following allocation and Max matrix in which only 3 instances of A and 3 instances of B are the only resources available at a particular instance. As per the following scenario, Will the system be in a Safe state? If yes, what is the Process termination sequence order? Allocation Max A B C A B C P0	[06]			
BL-2		II. Explain the Safety algorithm in the context of Deadlock.	[04]			
Q6						
CO-3; SO- 1,6; BL-3	a.	How will you implement segmentation in a user program having six segments? Depict logical address space, segmentation table and physical memory for the above using diagram. Also throw light on advantages and disadvantages of segmentation.				
CO-2; SO- 1,6; BL-5	b.	Answer the following: I. Consider a counting semaphore is initialized with value 10. Evaluate a value of S after executing 6 times p() and 8 time v() function on S. II. Explain and write code of p () and v() i.e. wait () and signal(). III. List the problems which can be solved using semaphores, elaborate any one with solution.				
Q7						
CO-2; SO-2; BL-3	a.	Interpret the different methods to prevent the existence of a deadlock with respect to the reasons for it.				
CO-3; SO- 1,6; BL-2	b.	Illustrate various techniques of address binding by means of a diagram.				
CO-2; SO- 1,6; BL-1	c.	What is thread? what are different types of threads.				