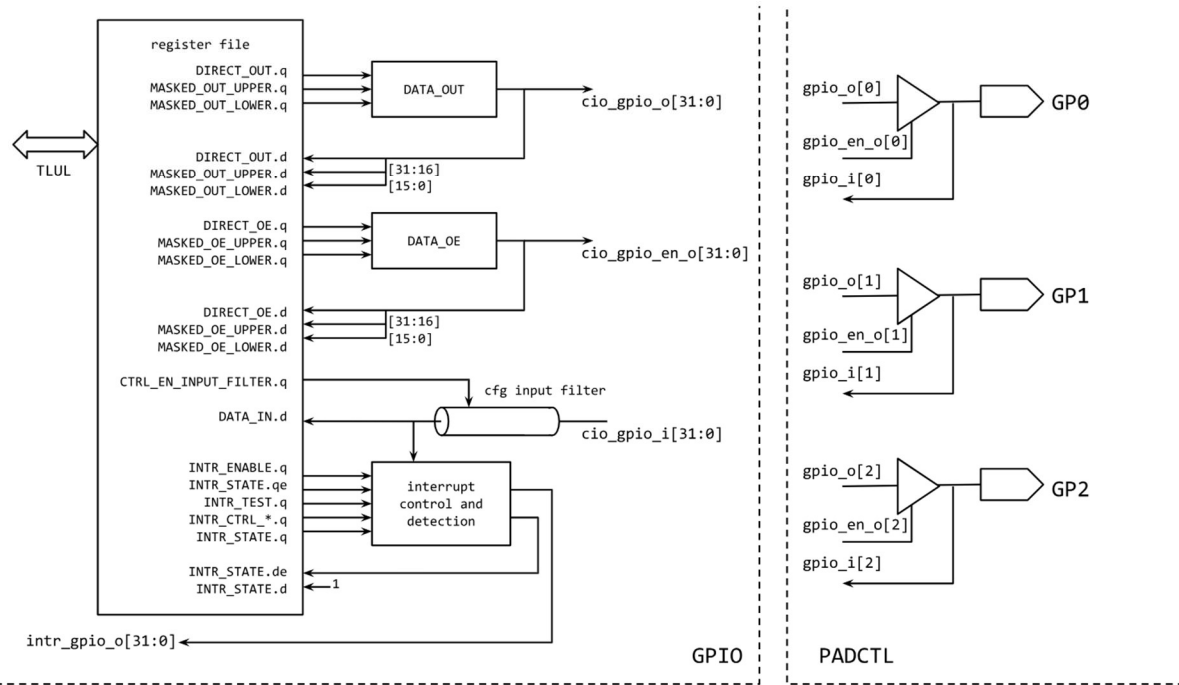


Block Diagram



Design Details

GPIO Output logic

