

# SRIMOL K S

+91-9207520833  
srimolks001@gmail.com  
linkedin.com/in/srimol-k-s



## PROFILE

Skilled in Verilog, C, Cadence Virtuoso, and ModelSim, with hands-on experience in data compression, AI-driven edge devices, and IoT projects. Internship at Tessolve provided exposure to industrial automation and communication protocols. Certified in AI and MATLAB, with achievements including an Assistant Researcher offer at NIT Sikkim and recognition at Project Expo 2023.

## EDUCATION

<b>Amrita Vishwa Vidyapeetham, Kollam</b> <i>M. Tech in VLSI Design</i>	June 2024-July 2026 SGPA 8.76
<b>Rajagiri School of Engineering and Technology</b> <i>B. Tech in Electronics and Communication Engineering</i>	June 2019-July 2023 CGPA 7.52
<b>Senior Secondary Education at GHSS Thanniam</b> <i>Bio-Math Stream</i>	July 2017- April 2019 percent 89

## SKILLS

### Programming Skills

C, Verilog

### Technical skills

Altium Designer, Cadence Virtuoso, Jetson Nano board, FPGA design flow using Xilinx Vivado, Modelsim RTL Simulator

## INTERNSHIP

### Tessolve

<b>Embedded IOT &amp; Industrial Automation</b>	Jan 2021-Jun 2021
<ul style="list-style-type: none"><li>Gained knowledge on development boards such as STM32, integrating hardware and software systems.</li><li>Worked with communication protocols, including UART and I2C to enable seamless device communication.</li></ul>	

## PROJECTS

<b>Comparison of Huffman and Canonical Huffman coding</b>	Sept 2024
<ul style="list-style-type: none"><li>Designed and implemented Huffman and Canonical Huffman coding in Verilog for data compression applications.</li><li>Conducted a detailed comparison of power consumption and hardware area, demonstrating that Canonical Huffman coding achieves superior performance.</li></ul>	

- Optimized the design for FPGA implementation, ensuring efficient utilization of hardware resources and scalability for larger datasets.

#### **Implementation of Leaf Disease Detection on Edge Device**

Oct 2023

- Conducted comparative analysis of SoCs and Arduino Nano 33 BLE for inference time, power efficiency, and accuracy across AI models.
- Identifying Google Coral Dev Board as the most efficient for continuous AI tasks and Jetson Nano for flexible, occasional computations.

#### **SMS-Based Fire Alarm and Detection System**

Sept 2022

- Developed a smoke detection system with buzzer and SMS alerts, integrated with an automatic sprinkler for fire suppression in residential and industrial settings.

#### **Energy-Efficient Home Automation System**

May 2020

- Designed a home automation system using Raspberry Pi and Zigbee protocol.
- Programmed control logic to manage appliances remotely via a smartphone application.

### **CERTIFICATIONS**

---

**Introduction to Machine Learning (ML)** An online non-credit course authorized by IBM and offered through Coursera

**Verilog HDL: VLSI Hardware Design Comprehensive Masterclass by UDEMY Altium Education**

**PCB Basic Design Course**

### **ACHIEVEMENTS**

---

- Received an offer to join as an Assistant Researcher at NIT Sikkim, showcasing strong academic and technical capabilities.
- Secured the Consolation Prize in the Project Expo 2023 organized by Electronauts for an innovative project demonstration.
- Successfully organized the Cultural Fest 2022, demonstrating leadership, planning, and event management.