TELLURI VANI SAI BHAVANI

CONTACT

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📍 Bengaluru, Karnataka

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INTERNSHIP

- Integrated Telecom/Data Network & Cyber Security. (04/10/2021-03/11/2021)

TECHNICAL SKILLS

- Verilog
- Circuit Design
- Model Sim
- Virtuoso, Vivado

LANGUAGE

- English
- Telugu
- Hindi

INTEREST

- Arts and craft
- Gardening
- Cooking

STRENGTHS

- Persistent
- Adaptability
- Self-motivated
- Creative Thinking

CERTIFICATIONS

- NSE Network Security Associate July 2021
- HCIA-AI (Huawei Talent) April 2021
- Maven silicon (ongoing)

01 CAREER OBJECTIVE.

As a new graduate, I aim to implement my knowledge of semiconductor technology and integrated circuit design domains and be associated with a progressive organization to apply and hone my skills to advance my expertise in VLSI design, verification, and problem-solving.

02 EDUCATION

Master of Technology in VLSI
Amrita Vishwa Vidyapeetham
SGPA: 6.29
Bachelor of Technology in ECE
KLEF Deemed to be University
CGPA: 7.79
Class 12
Narayana Junior College
Score:875
Class 10
Sri Chaitaniya School
CGPA:8.8

03 PROJECTS

- **IOT-Based Smart Agriculture System**

The main factor we accomplished in this system is to monitor and control several factors in our field wirelessly to improve the yield of crops.

- **Efficient modular hybrid adders and Radix-4 booth multipliers for DSP applications**

Adders and multipliers are the fundamental elements of a signal-processing architecture. Improving the speed of addition and multiplication operations while minimizing power consumption and area is the problem of interest in this paper. Two versions of modular hybrid adder structures are proposed.