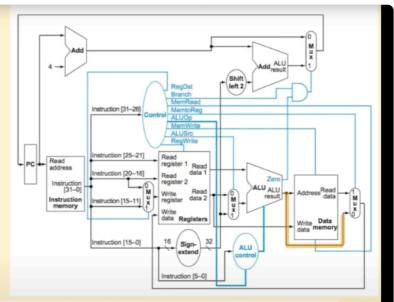
A-Type instructions:	· · · · · · · · · · · · · · · · · · ·	
https://youtu.be/lqHKJyYGKXk — sub — o4 — sct	Resource	MemRead control signal that allows Dmem read if == 0 then don't read if == 1 then read from address and place on Read data bus CA16 - MIPS control signals Auto
Memory unstructions: - lw	. .	Add verses 4 years age Min's corrol signals in the CPUmore CAT - MIPS Pipeline 1 CAT - MIPS Pipeline 1 (CAT - MIPS Pipe
- sw Branch instructions: - Jeg.		
Regwhite Mem Read -A.	k Y/N question	Can-the instruction
Mem Write Branch		do it
Raput ALUSAC Mento Reg	at the Mux ito	deude

MemtoReg

controls MUX at far right

if == 0 then write data from ALU to register

if == 1 then write data from Dmem to register



Instruction	RegDst	ALUSTC	Memto- Reg	Reg- Write	Mem- Read	Mem- Write	Branch	ALUOp1	ALUOp0
R-format	1	0	0	1	0	0	0	1	0
٦w	0	1	1	1	1	0	0	0	0
SW	X	1	Х	0	0	1	0	0	0
beq	Х	0	Х	0	0	0	1	0	1