## Rahul Vigneswayaw . K Hardware Architecture for Deep Learning - CS6490. Spring 2023-24. Dept. of CSE, IIT Hyderabad

<u>Quiz-5</u> Total marks: 10	Time: 12 minutes
Name: Rahul Vignisuraran K	9
Roll number: CS23MTECH02002	
DNNExplorer generates hardware design: [1]     Specific for a given CNN     b. Common design for many CNNs	
<ol> <li>The total number of MAC operations performed by CGNet is always a. True</li> <li>False</li> </ol>	ys constant for a given CNN: [1]
3. How is the pruning in channel gating (CGNet) different from the we	
> In weight fruing we fruine out weights cortain threshold in a static manner	Hat are below after training
> In Correct we do it dynamically on the	re fly wased on
> Weight kruning doesn't dekend on unfict  > Channel gating is much more struction  than weight kruning.  > Channel gating forwres the input effective  while weight kruning krunes trained  4. Why does DNNExplorer use a custom pipeline design for each of  generic structure for the later layers? [3]  > When Checked empirically the varior  the first half is much higher comp	while Canet does, well with white Caned wort unput weight weight in the few initial layers and the barred to second
> Higher CTC variance indicates the n	occusity for much
> Highur CTC variance indicates the M more specialized kikeline them a general	ic works fou all
one.	

CGNet uses a banked SRAM structure (splitting the weight values into small sized SRAM banks).
 Why CGNet needs to use it and how it helps? [3]

Halfing Maries and America

> Etaket General conv happens as W\* 21 dut

Carret conv happens like Wp\* 21p + Wor \* 21or

-> Therefore splitting the weight values unto small SRAM banks help ancrease increase the newse of the banks teather than farsing through the entire weights overytime.

> Convet being weight stationary & dynamic in nature will exponentially invuesse—the access cost if not for the banked SRAMS.