EE 648 – VLSI Design Binary Coded Hexadecimal for a 7 Segment Display



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Contents

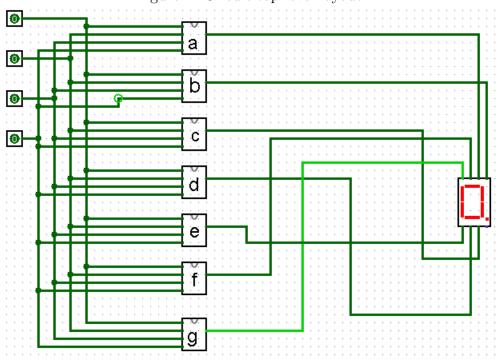


Figure 1: Circuit top level layout