EE 648 – VLSI Design

Binary Coded Hexadecimal for a 7 Segment Display
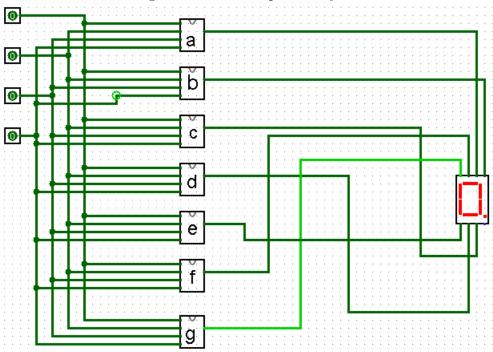
Ryker DIAL

Cody GOSSEL

Zach KREHLIK

May 1, 2015

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Figure 1: Circuit top level layout