

GF2 Software - Second Interim Report

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1 Example Definition Files and Circuits

1.1 Example 1 - Full Adder Circuit

1.1.1 Full Adder Circuit Diagram

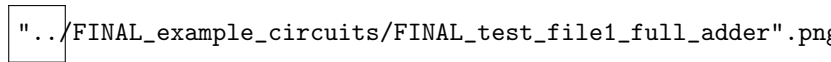


Figure 1: Full Adder Circuit Diagram

1.1.2 Full Adder Circuit Definition File

1.2 Example 2 - Shift Register

1.2.1 Shift Register Circuit Diagram

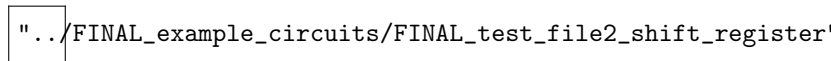


Figure 2: Shift Register Circuit Diagram

1.2.2 Shift Register Circuit Definition File

1.3 Example 3 - Combinational Circuit

1.3.1 Combinational Circuit Diagram

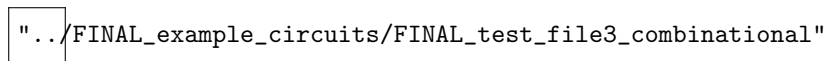


Figure 3: Shift Register Circuit Diagram

1.3.2 Combinational Circuit Definition File

1.4 Example 4 - dtype Circuit

1.4.1 dtype Circuit Diagram

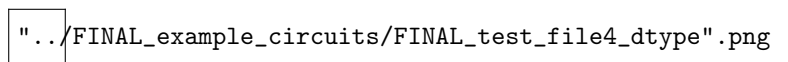


Figure 4: dtype Circuit Diagram

1.4.2 dtype Circuit Definition File