

# Lab 0: Preparing for ECE342 Labs

## Introduction

In this lab, you will review concepts from ECE241 and ECE243 and learn new things such as SystemVerilog and testbenches. The material in this lab is self-taught. There will be no dedicated lab session but you are expected to complete the tutorials listed here on your own prior to starting labs. The table below lists the tutorials you should complete and the suggested order of completion. You will find each of these tutorials on Quercus.

<b>Tutorial name</b>	<b>Description</b>	<b>Complete by</b>
SystemVerilog Tutorial	Introduction to SystemVerilog, an updated and more robust version of Verilog.	Lab 1
ModelSim Guide	Review of Modelsim.	Lab 1
Testbench tutorial	Introduction to testbenches, as a replacement for .do files.	Lab 1
Creating Generic Hardware	Introduction to parametrized modules and generate blocks.	Lab 2