

**Ver0.5-2024-01-12**，问题如下：

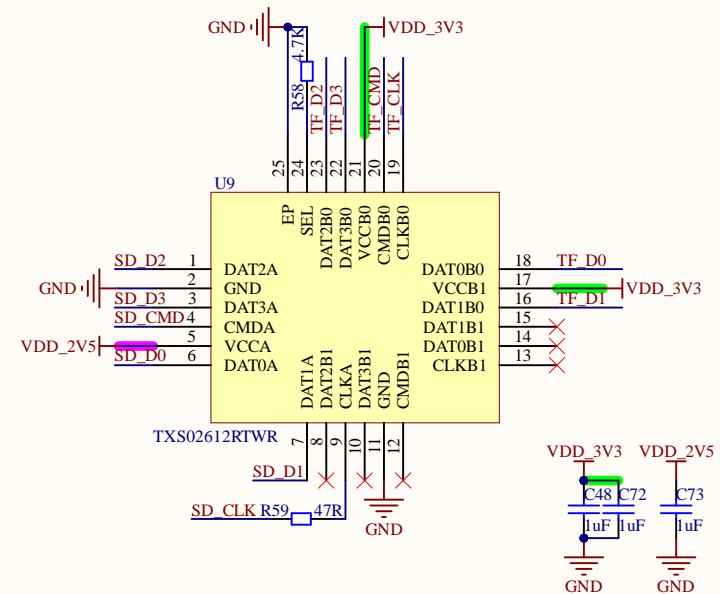
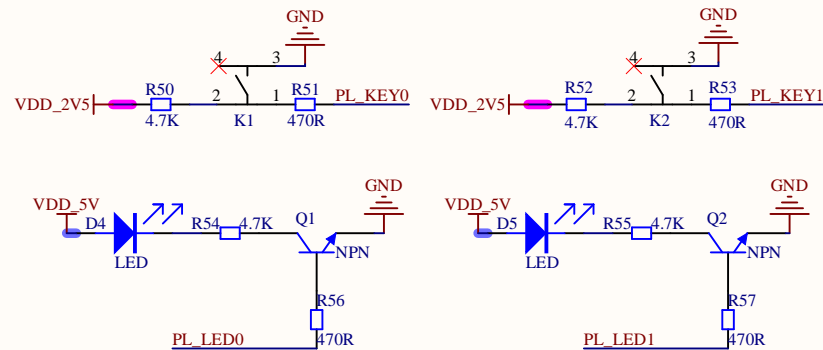
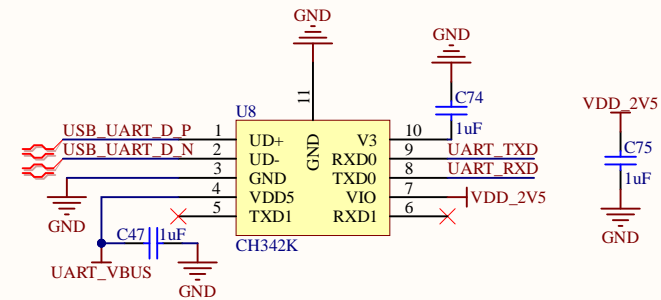
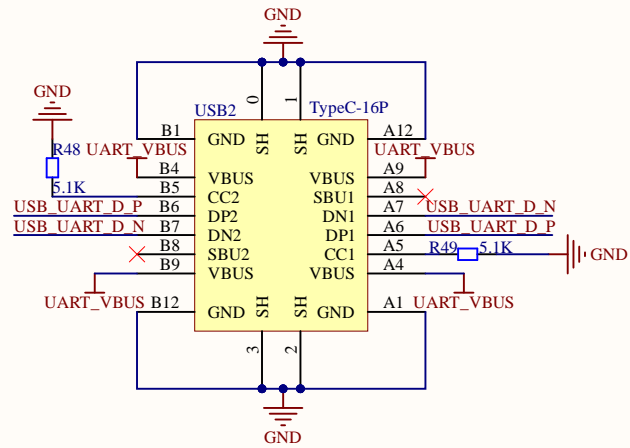
- 1、**BTB**封装需要修改，调换大小定位柱位置。
- 2、**TPS7A8101**的输入电压范围**2.2-6.5V**，需要修改。
- 3、**MIO500**和**BANK34**与**35**的供电电压全部修改为**2.5V**以满足核心板**DDR**基准芯片需求；**BANK501**保持**1.8V**不变。

**Ver0.6-2024-01-30**，正常工作。

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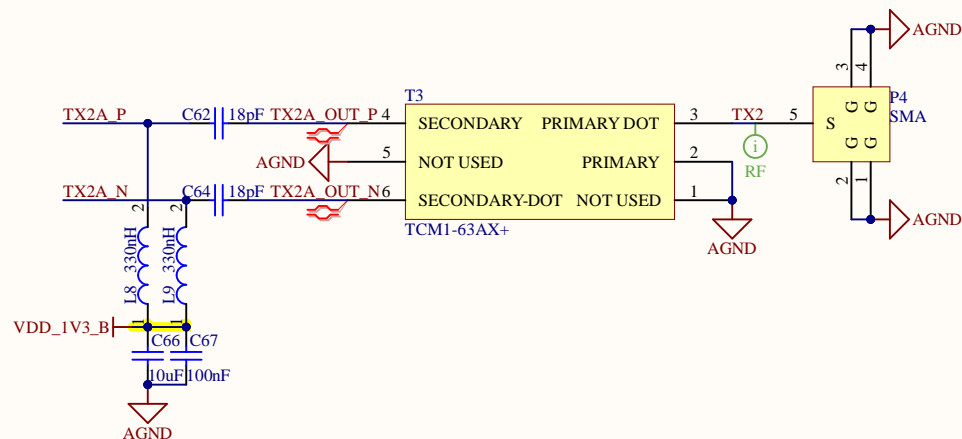
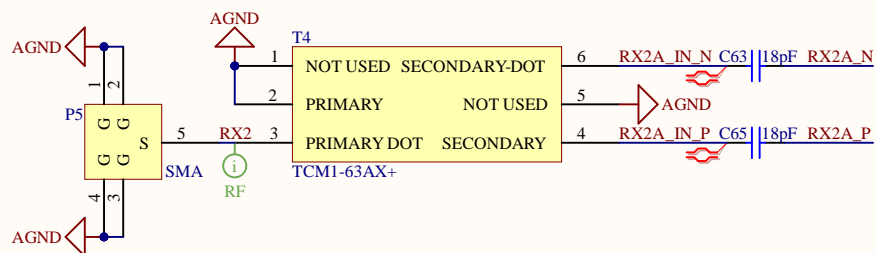
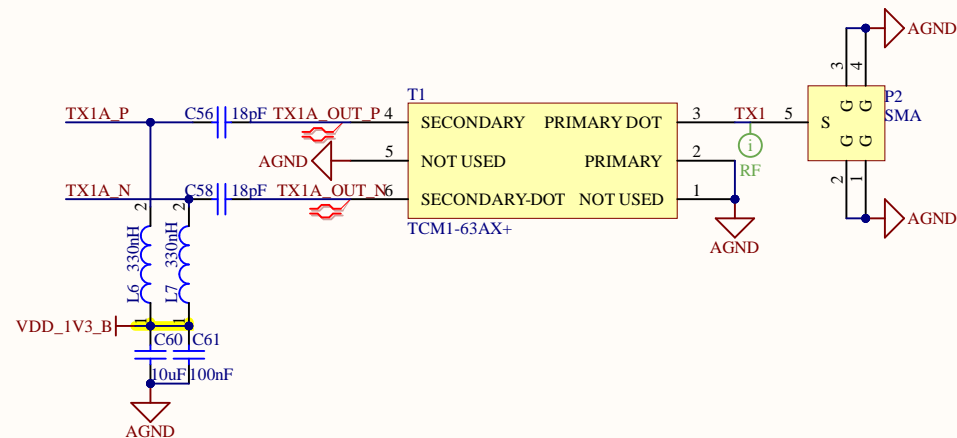
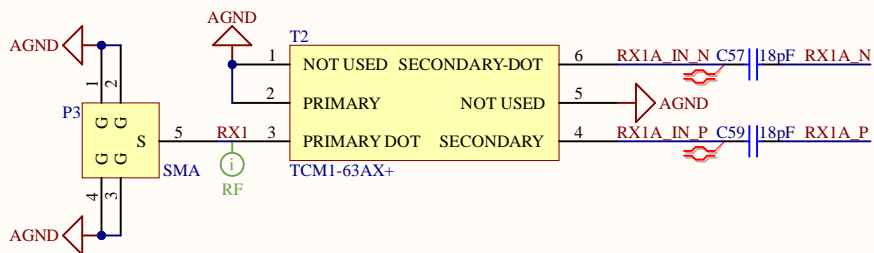






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- 1, 芯片内部的TX&RX端均存在直流偏置，需要进行AC耦合。
- 2, TX侧需要给1.3V 75mA直流偏置，保证正常工作。
- 3, 电容选用C0G或N0P，电感的ESR尽量小。

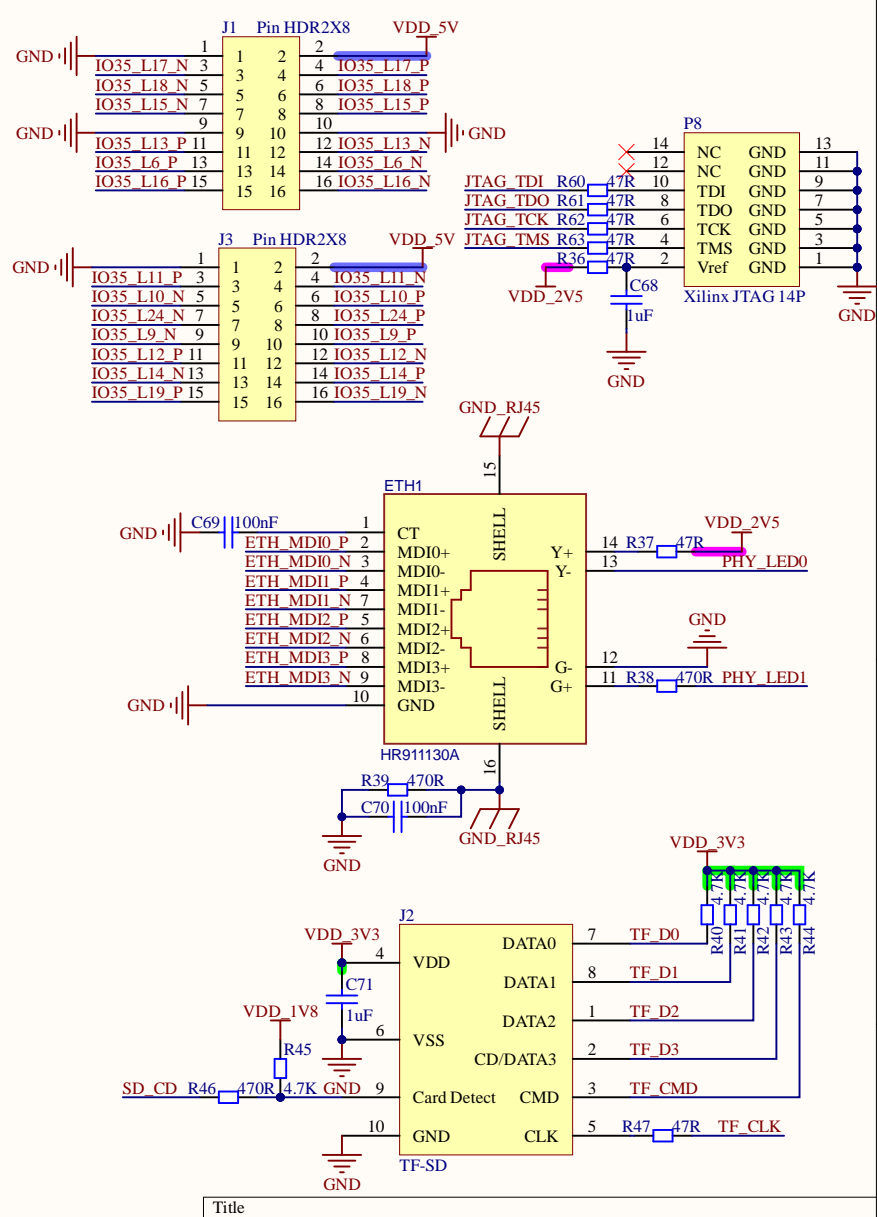
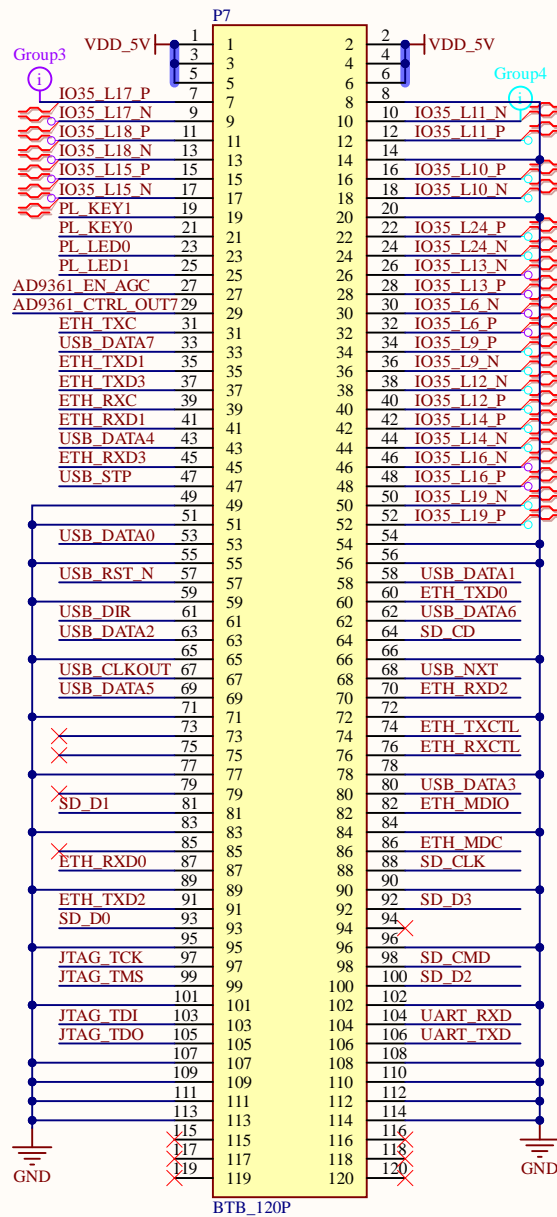
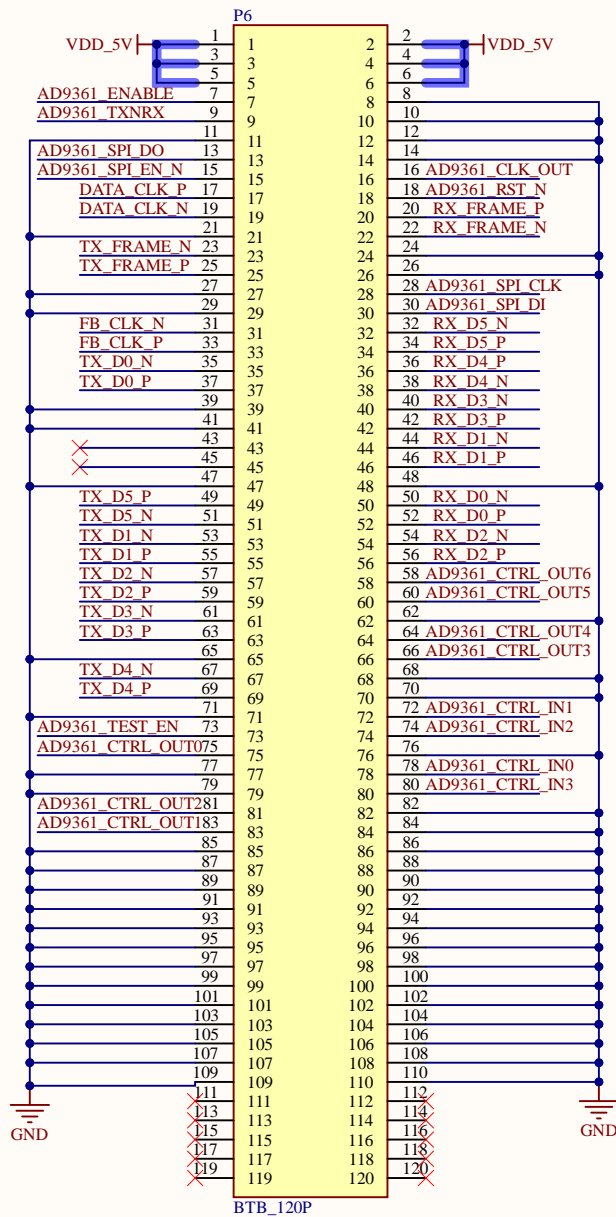
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A

B

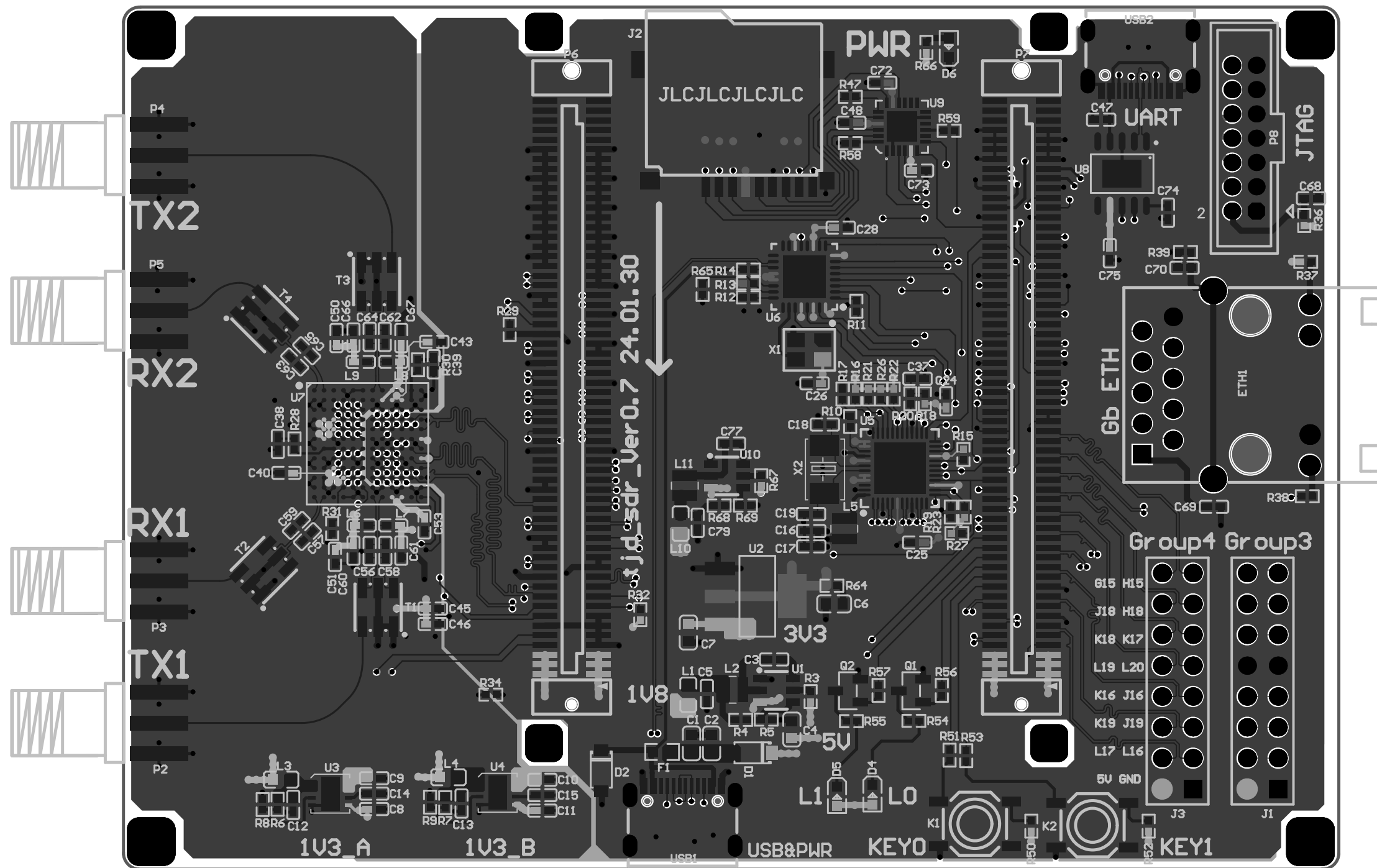
C

D



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拆除核心板R58以使用IO34\_L1\_N引脚





Line #	Name	Description	Designator	Quantity	Manufacturer 1	Manufacturer Part Number 1	Manufacturer Lifecycle 1	Supplier 1	Supplier Part Number 1	Supplier Unit Price 1	Supplier Subtotal 1
	1uF	CAP no polarity	C8, C10, C20, C21, C22, C23, C24, C25, C26, C27, C28, C29, C30, C31, C32, C33, C34, C35, C36, C37, C38, C39, C40, C41, C44, C45, C46, C47, C48, C49, C50, C51, C52, C53, C68, C71, C72, C73, C74, C75, C76	41							
	4.7K	Res	R3, R14, R15, R16, R17, R18, R19, R20, R21, R22, R23, R24, R25, R26, R27, R29, R32, R40, R41, R42, R43, R44, R45, R50, R52, R54, R55, R58, R67	29							
	10uF	CAP no polarity	C1, C4, C5, C6, C7, C9, C11, C12, C13, C16, C17, C42, C43, C60, C66, C78, C79	17							
	18pF	CAP no polarity	C18, C19, C55, C56, C57, C58, C59, C62, C63, C64, C65	11							
	47R	Res	R11, R36, R37, R47, R59, R60, R61, R62, R63	9							
	100nF	CAP no polarity	C2, C3, C14, C15, C61, C67, C69, C70, C77	9							
	470R	Res	R38, R39, R46, R51, R53, R56, R57, R66	8							
	3A Max	Ferrite-bead	L1, L3, L4, L10	4							
	5.1K	Res	R1, R2, R48, R49	4							
	10K	Res	R5, R8, R9, R69	4							
	330nH	Inductor	L6, L7, L8, L9	4							
	NC	Res	R13, R33, R35, R65	4							
	SMA	SMA RF Connector	P2, P3, P4, P5	4							
	TCM1-63AX+	Balun	T1, T2, T3, T4	4							
	1R	Res	R28, R30, R64	3							
	2.2uH	Inductor	L2, L5, L11	3							
	LED	LED	D4, D5, D6	3							
	6.25K	Res	R6, R7	2							
	BTB_120P	120Pin 0.8mm BTB Connector	P6, P7	2							
	NPN	NPN	Q1, Q2	2							
	Pin HDR2X8		J1, J3	2							
	SW_SMD4	SMD Switch4, C2686829	K1, K2	2							
	TPS7A8101	Low noise ldo with 1A	U3, U4	2							
	TPS563201	3A Sync DC-DC	U1, U10	2							
	TypeC-16P	TypeC-16P	USB1, USB2	2							
	0R	Res	R34	1							
	1N4007	Diode	D1	1							
	2.49K	Res	R10	1							
	8.06K	Res	R12	1							
	12V 3A	Fuse	F1	1							
	13.7K	Res	R68	1							
	14.3K	Res	R31	1							
	22.6K	Res	R4	1							
	24MHz	Oscillator	X1	1							
	25M	Crystal	X2	1							
	40M TCXO	Oscillator	X3	1							
	AD9363	70MHz-6GHz Transceiver	U7	1							
	AMS1117	1A LDO	U2	1							
	CH342K	USB to UART	U8	1							
	D5V0L2B3SO-7	Dual TVS	D3	1							
	HR911130A	ETH transformer	ETH1	1							
	IPEX		P1	1							
	NC	CAP no polarity	C54	1							
	RTL8211E-VL-CG	ETH phy	U5	1							
	TF-SD	TF-SD Card	J2	1							
	TVS	TVS Diode	D2	1							
	TXS02612RTWR	Voltage level conversion	U9	1							
	USB3320C	USB2.0 phy	U6	1							
	Xilinx JTAG 14P	Xilinx JTAG 2.0mm*14P	P8	1							