EXPERIMENT 3 Multiplication of 16 bit numbers

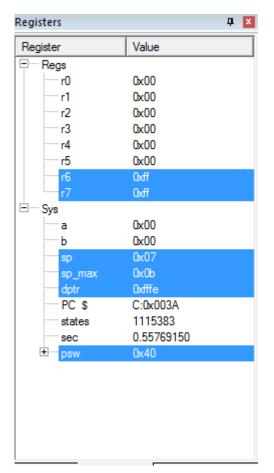
Submitted by, T Raja Aadhithan

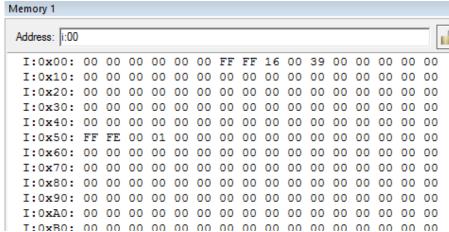
Code:

16mul.asm

```
1 ; multipling 16 bit number
 2 :1ST NUMBER IS 1234
 3 : 2ND NUMBER IS 5678
          MOV R7, #0FFH ; LOWER BYTE OF NUMBER 1
 5
          MOV R6, #OFFH ; UPPER BYTE OF NUMBER 1
 6
7
          MOV R5, #OFFH ; LOWER BYTE OF NUMBER 2
          MOV R4, #0FFH ; UPPER BYTE OF NUMBER 2
8
Q
10 ;multipling lower byte of 1st operand
11
12
          MOV A,R5
13
          JZ L8 ; checks if R5 is sero
     L4: LCALL L1 ; repeated addition loop
15
          DJNZ R5,L4
16
17 ; multipling higher byte of 1st operand
18
19
      L8: MOV A,R4
20
                  ; checks if R4 is sero
          JZ L5
21
      L6: LCALL L3 ; repeated addition loop
22
          DJNZ R4,L6
23
24
      L5: MOV 51H, DPL ; moves value of DPTR to these registers
25
          MOV 50H, DPH
          SJMP L9
26
                     ; jumps to last line
27
28 ;1st SUBROUTINE
29
      L1: MOV A,53H
30
31
          ADD A,R7 ; adds lower byte of 2nd operand
32
          MOV 53H, A
33
          MOV A,52H ; 4th byte of result
34
          ADDC A,R6 ; adds higher byte of 2nd operand
35
          MOV 52H, A ; 3rd byte of result
          JNC L2
36
37
          INC DPTR ; 1st and 2nd byte of results
38
          CLR C
39
      L2: RET
40
41 ; 2ND SUBROUTINE ( multiplying higher byte by 100H)
42
      L3: MOV R3, #0FFH
43
44
      L7: LCALL L1 ; calls subroutine 255 times
          DJNZ R3,L7
45
          LCALL L1 ; calls subroutine for 256th time
46
47
          RET
                   ; this sub routine has been executed 100H times
48
49
      L9:
                     ; result is seen at 50H 51H 52H and 53H registers
50
          END
```

OUTPUT:





The outputs are found at 50H to 53H registers