

EXPERIMENT 1.3

Addition and subtraction of two eight bit BCD numbers.

Code:

```
1      ORG 0000H
2      MOV A,#10H ; 1ST BCD NO
3      ADD A,#20H ; 2ND BCD NO
4      DA A      ; CONVERT TO BCD
5      MOV R0,A  ; STORE IN R0
6      END
```

Output:

stored in R0

Register	Value
Regs	
r0	0x30
r1	0x00
r2	0x00
r3	0x00
r4	0x00
r5	0x00
r6	0x00
r7	0x00
Sys	
a	0x30
b	0x00
sp	0x07
sp_max	0x07
dptr	0x0000
PC \$	C:0x0000
states	4
sec	0.00000
psw	0x00