FEEDBACK AMPLIFIER WITH CLOSED LOOP

AIM:

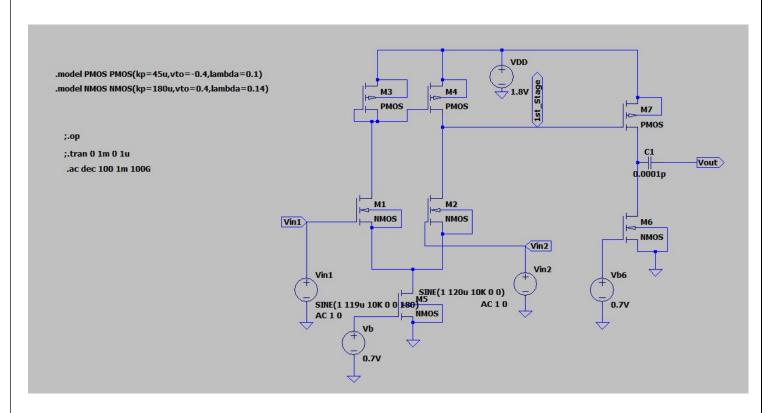
Design a two-stage operational amplifier with the first stage as a differential amplifier with active load for a gain of 30V/V and the second stage as a CS Amplifier with a gain of 20V/V. Assume I SS =0.1mA, V GS =0.7V, V th =0.4V and consider suitable technology parameters values.

- i) Compare the result with LTSPICE Simulation.
- ii) Perform the transient analysis of the same.

APPARATUS REQUIRED:

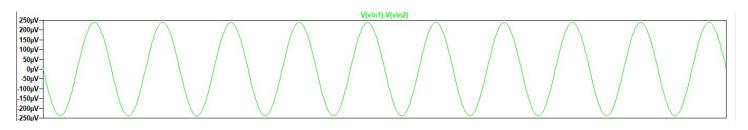
LTSpice Software.

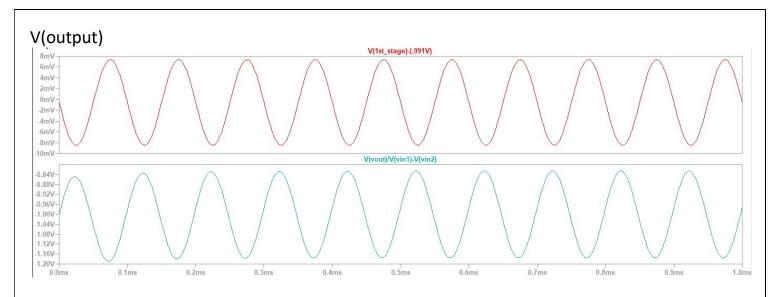
Circuit:



Output:

Vinput





Transfer Function:

```
--- Operating Point ---
V(n001):
                1.8
                                voltage
V(n002):
                0.990444
                                voltage
V(1st stage):
                0.990444
                                voltage
V(vin2):
                                voltage
                0.329224
V(n004):
                                voltage
                1
V(vin1):
                                voltage
V(n006):
                0.7
                                voltage
                0.599478
V(n003):
                                voltage
                5.99478e-017
V(vout):
                                voltage
V(n005):
                0.7
                                voltage
                0.000108321
Id (M6):
                                device current
Ig (M6):
                                device current
Ib (M6):
                -6.09478e-013 device current
                -0.000108321
                                device current
Is (M6):
Id (M5):
                0.00010454
                                device current
Ig (M5):
                0
                                device current
Ib (M5):
                -3.39224e-013 device current
Is (M5):
                -0.00010454
                                device current
                5.22699e-005
Id (M1):
                                device current
Iq (M1):
                                device current
Ib (M1):
                -6.7122e-013
                                device current
Is (M1):
                -5.22699e-005 device current
                5.22699e-005
Id (M2):
                                device current
Iq (M2):
                                device current
                -6.7122e-013
Ib (M2):
                                device current
                -5.22699e-005 device current
Is (M2):
Id(M7):
                0.000108321
                                device current
Ig (M7):
                -0
                                device current
                1.21052e-012
Ib (M7):
                                device current
                                device current
                -0.000108321
Is (M7):
                5.22699e-005
Id (M3):
                                device current
Ig (M3):
                                device current
Ib (M3):
                8.19556e-013
                                device current
Is (M3):
                -5.22699e-005 device current
```

RESULT:

The circuit is implemented and the gain of 600 V/V is attained.