

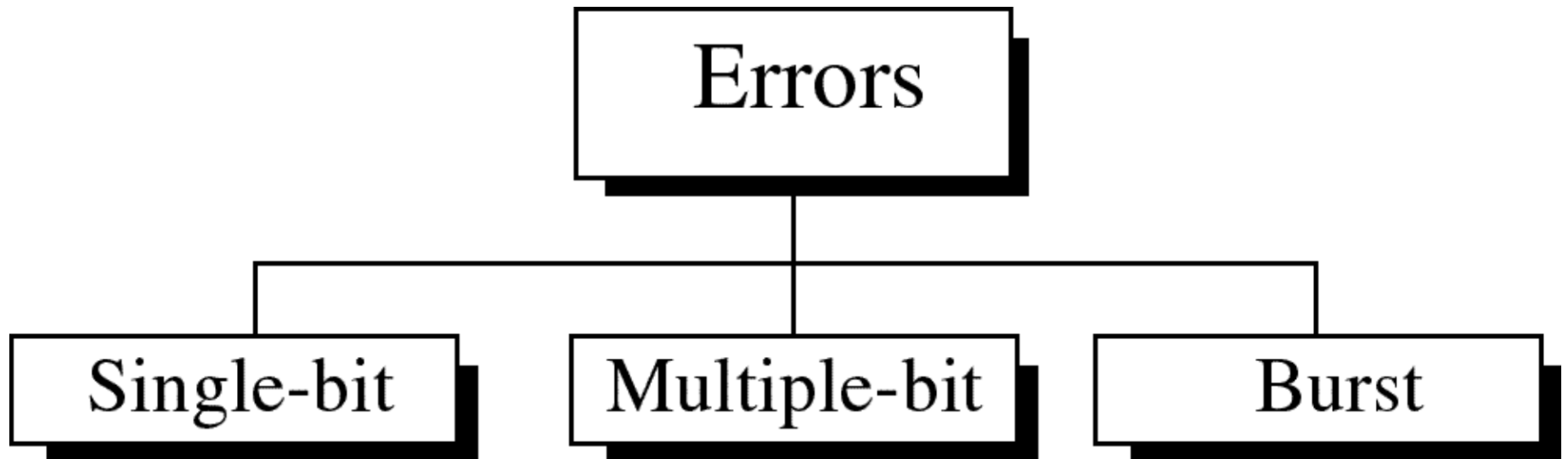
Error Detection and Correction

- **Types of Errors**
- **Detection**
- **Correction**

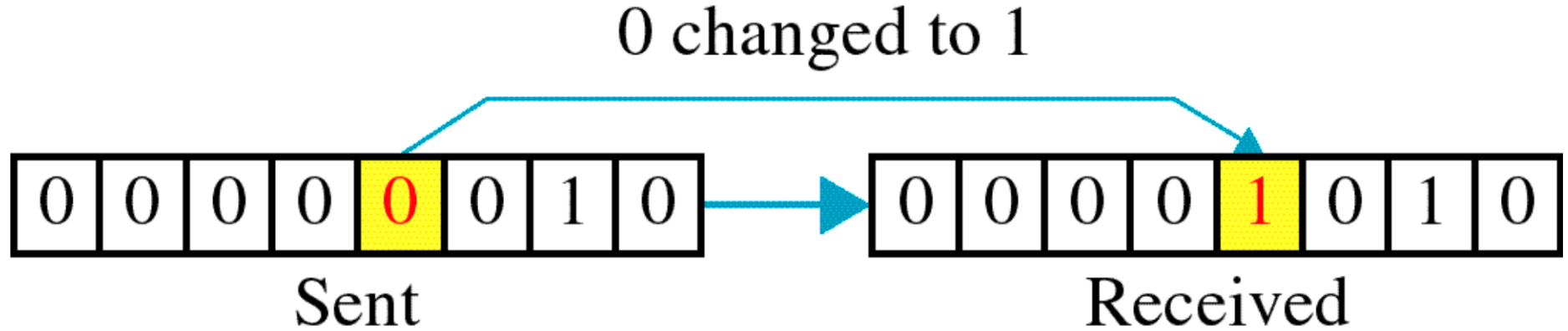
Basic concepts

- ★ Networks must be able to transfer data from one device to another with complete accuracy.
- ★ Data can be corrupted during transmission.
- ★ For reliable communication, errors must be detected and corrected.

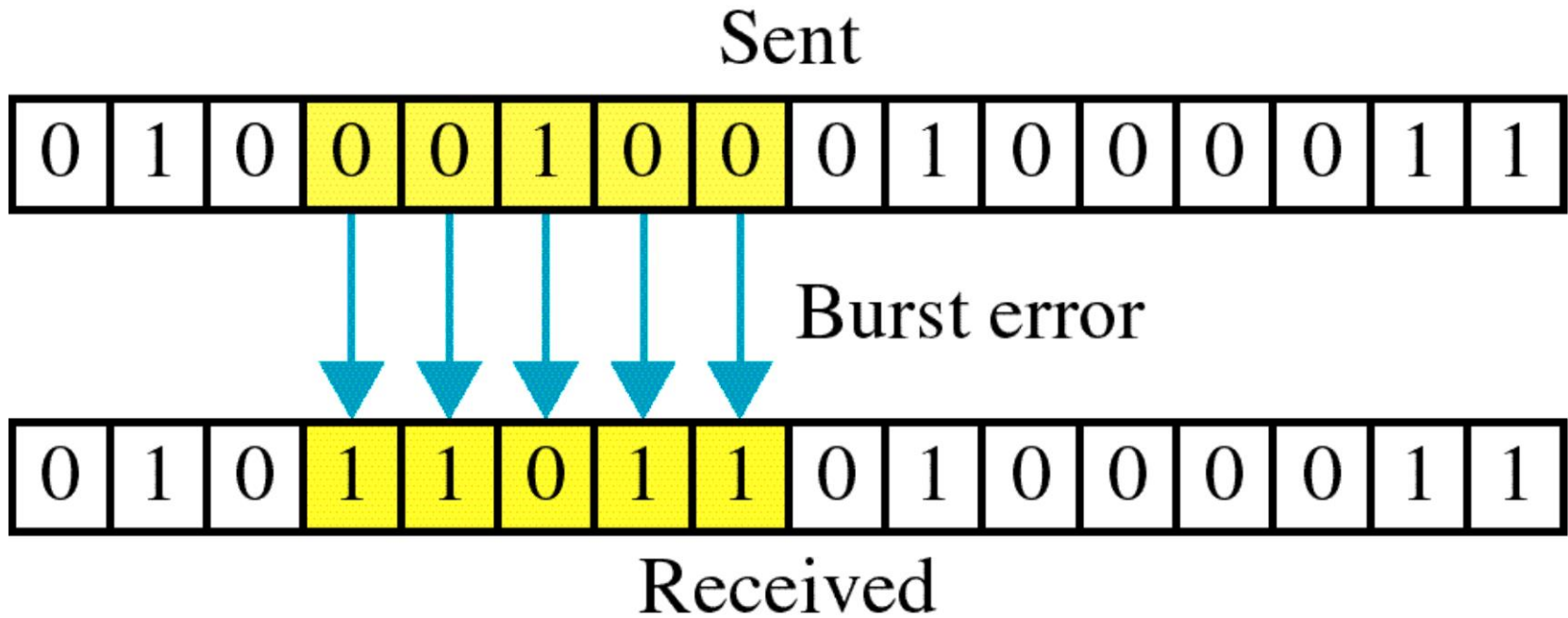
Types of Errors

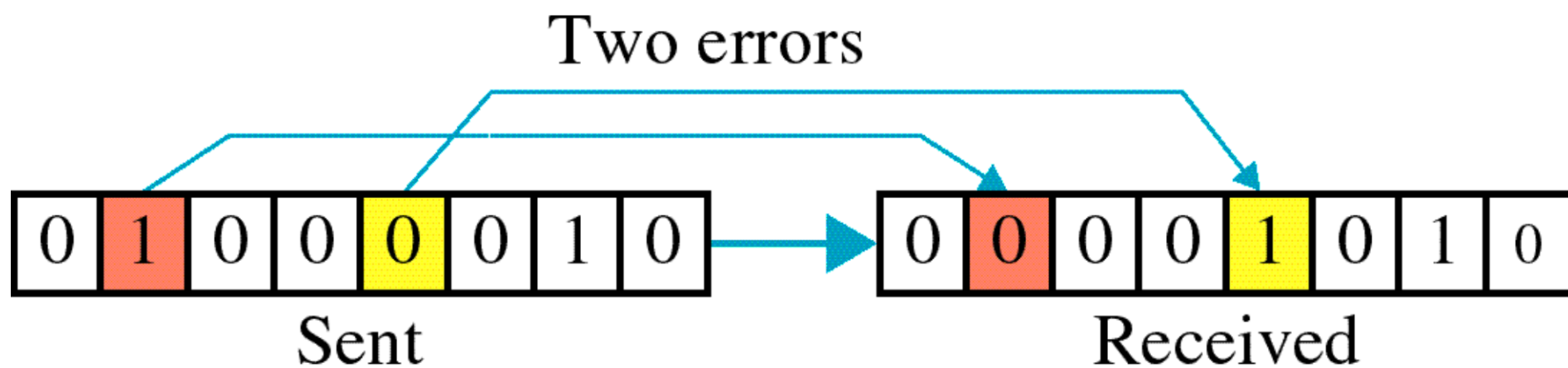


Single-bit error



Burst error





The term **burst error** means that two or more bits in the data unit have changed from 1 to 0 or from 0 to 1.

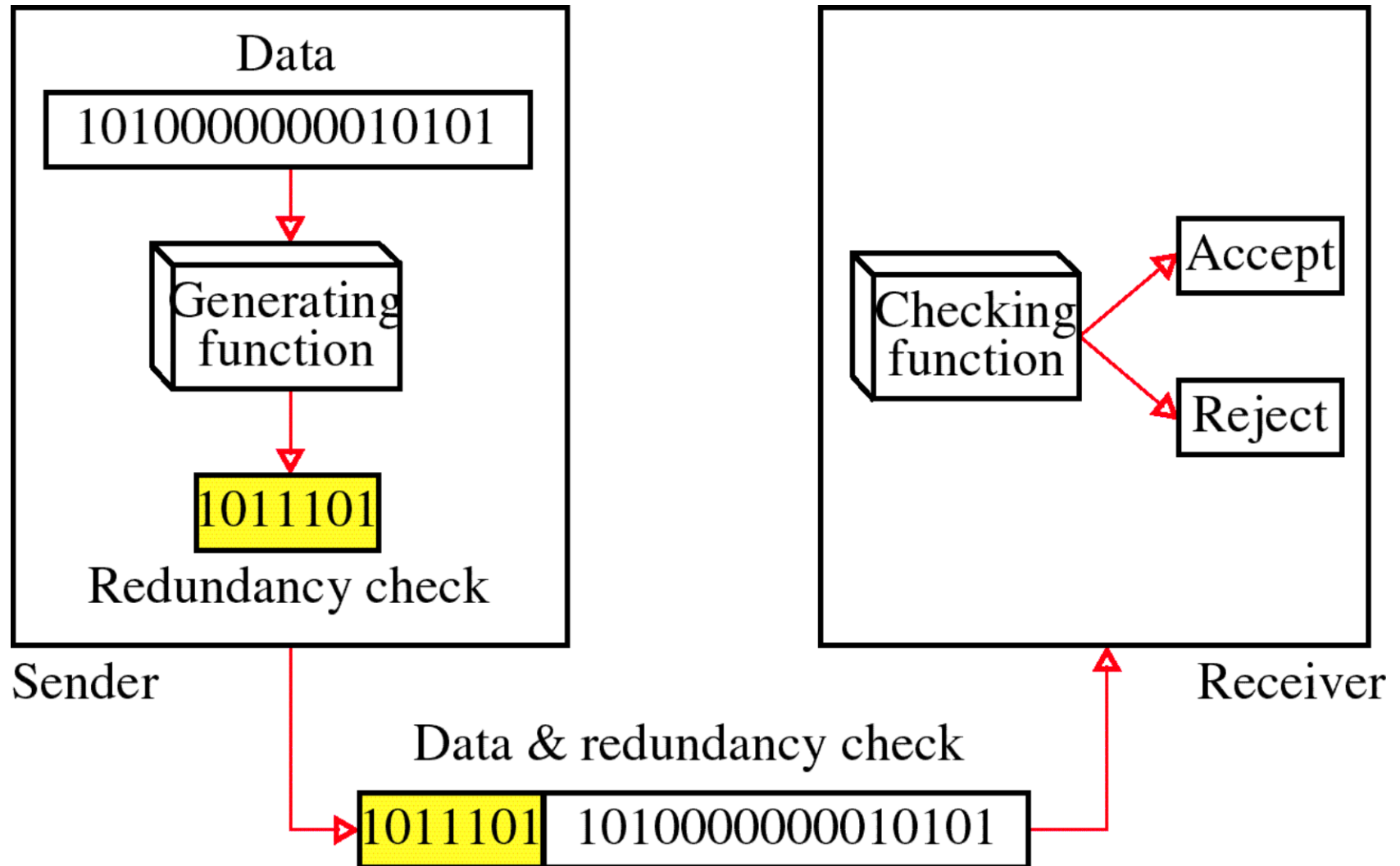
Burst errors does not necessarily mean that the errors occur in consecutive bits, the length of the burst is measured from the first corrupted bit to the last corrupted bit. Some bits in between may not have been corrupted.

Error detection

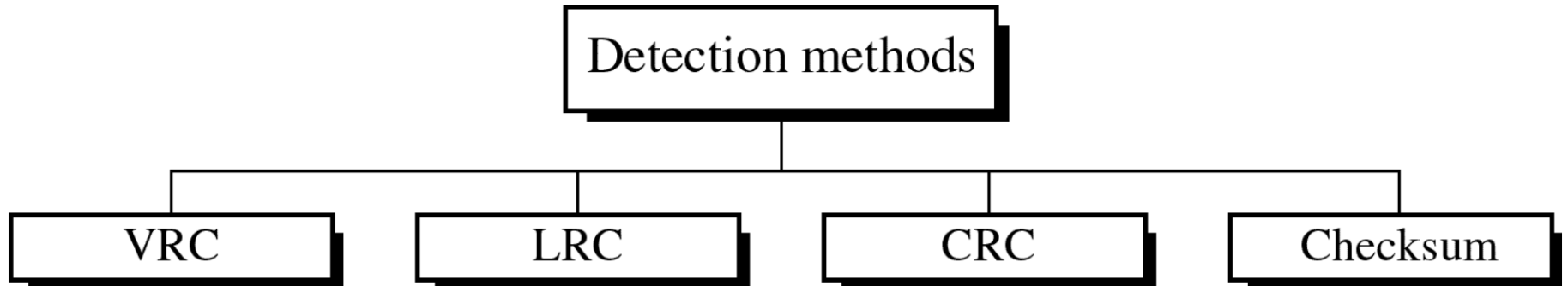
Error detection means to decide whether the received data is correct or not without having a copy of the original message.

Error detection **uses the concept of redundancy, which means** adding extra bits for detecting errors at the destination.

Redundancy



Four types of redundancy checks are used in data communications



Four types of redundancy checks

- 1. Vertical Redundancy Check(VRC)**
- 2. Longitudinal Redundancy Check(LRC)**
- 3. Cyclic Redundancy Check(CRC)**

Error Correction

It can be handled in two ways:

- 1) receiver can have the sender retransmit the entire data unit.
- 2) The receiver can use an error-correcting code, which automatically corrects certain errors.

Single-bit error correction

To correct an error, the receiver reverses the value of the altered bit. To do so, it must know which bit is in error.

Number of redundancy bits needed

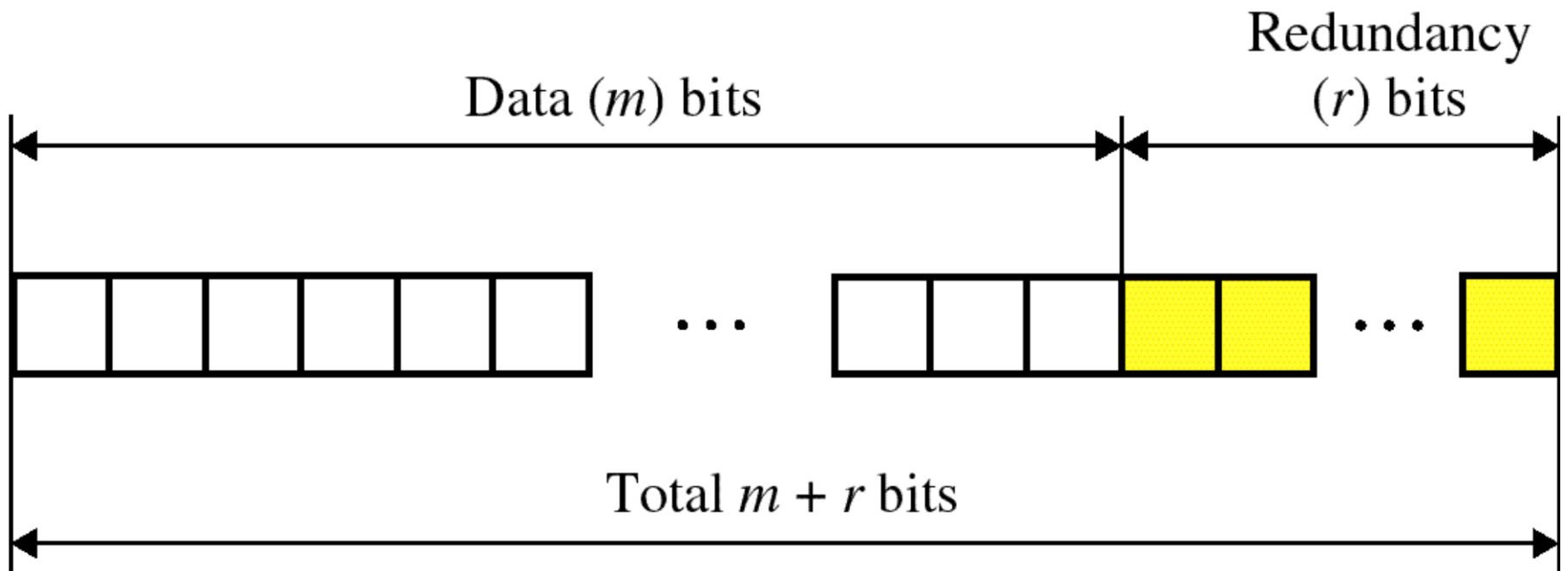
- Let data bits = m
- Redundancy bits = r

\therefore Total message sent = $m+r$

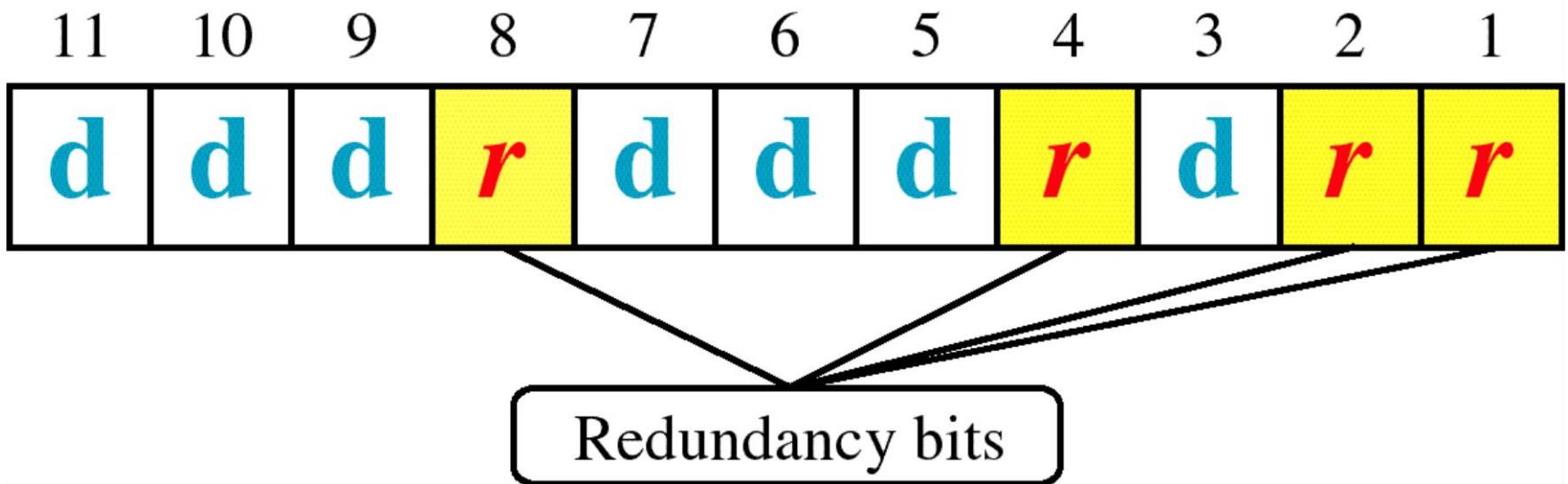
The value of r must satisfy the following relation:

$$2^r \geq m+r+1$$

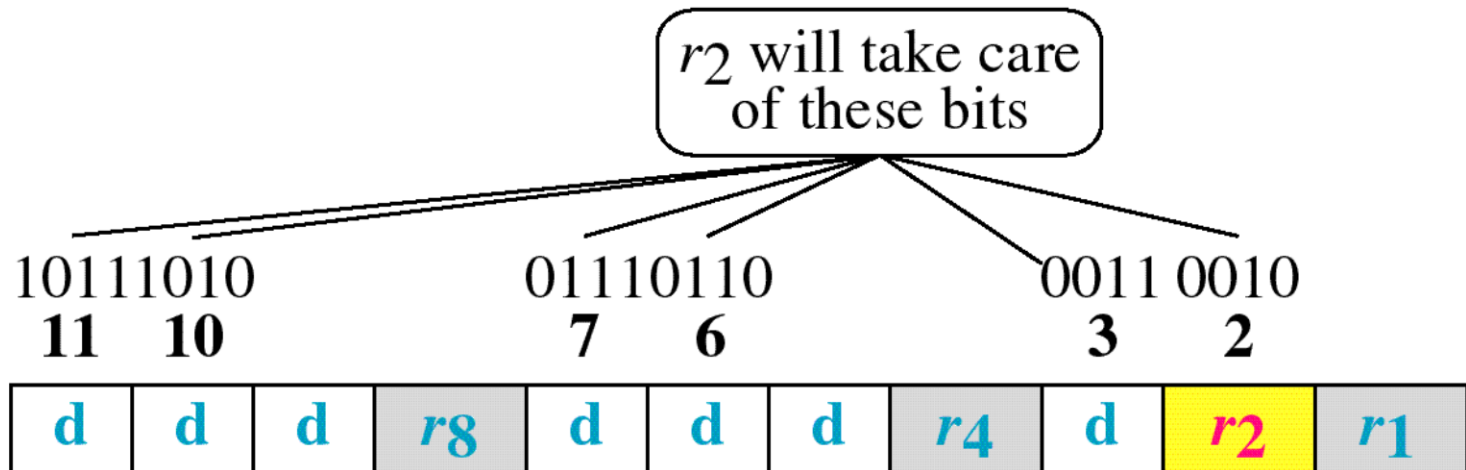
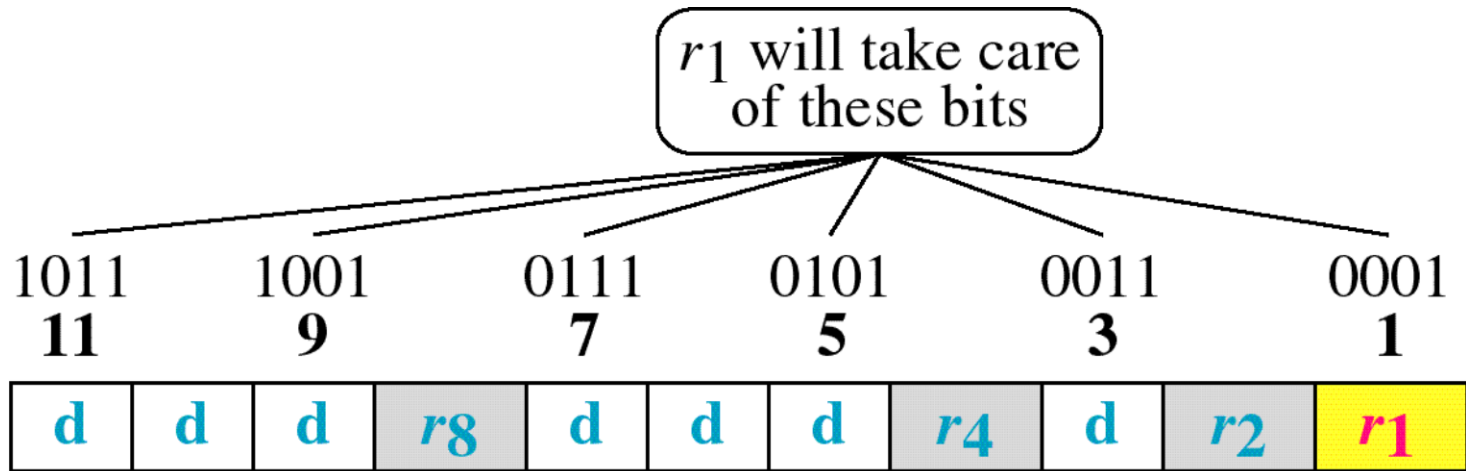
Error Correction



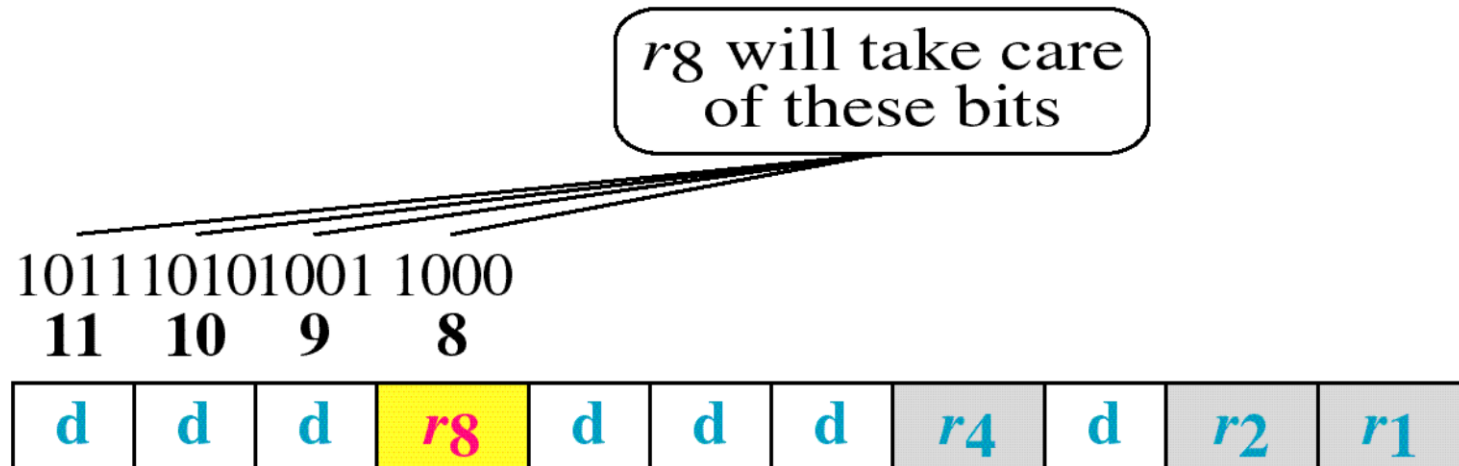
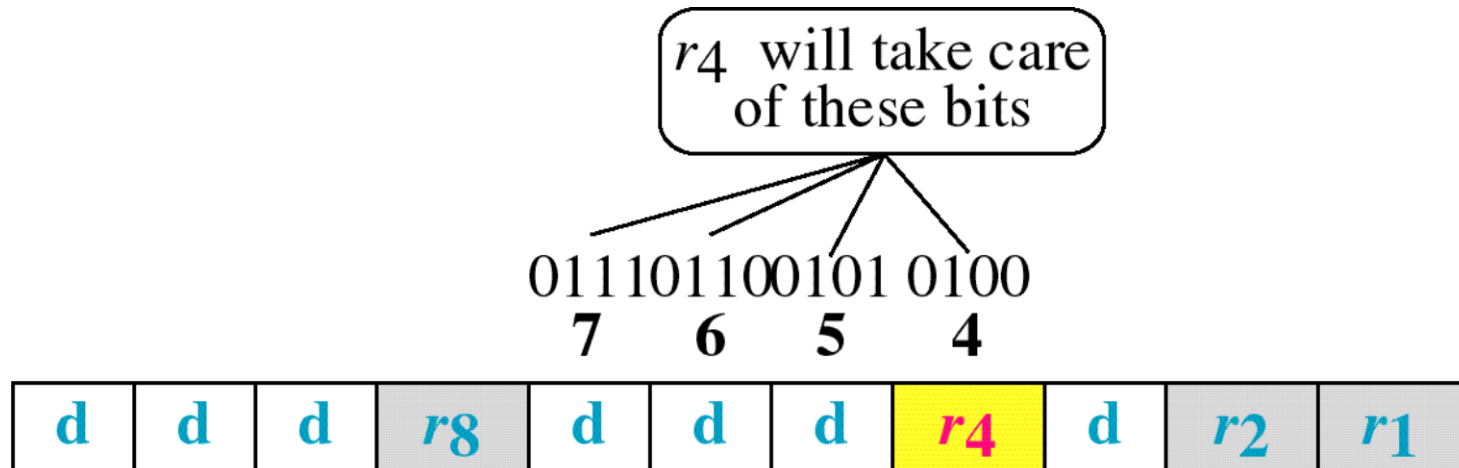
Hamming Code



Hamming Code



Hamming Code



Example of Hamming Code

Data: 1 0 0 1 1 0 1



Data	1	0	0		1	1	0		1		
------	---	---	---	--	---	---	---	--	---	--	--

Adding r_1

1	0	0		1	1	0		1		1
---	---	---	--	---	---	---	--	---	--	---

Adding r_2

1	0	0		1	1	0		1	0	1
---	---	---	--	---	---	---	--	---	---	---

Adding r_4

1	0	0		1	1	0	0	1	0	1
---	---	---	--	---	---	---	---	---	---	---

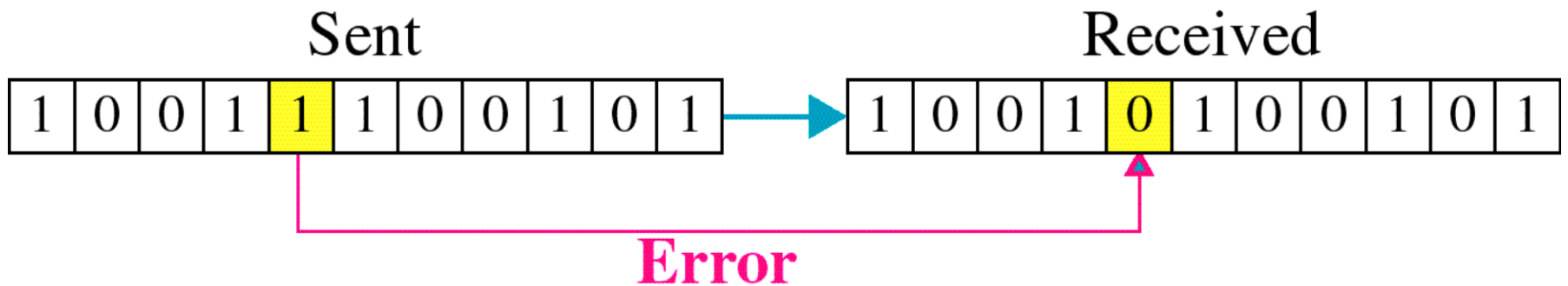
Adding r_8

1	0	0	1	1	1	0	0	1	0	1
---	---	---	---	---	---	---	---	---	---	---



Code: 1 0 0 1 1 1 0 0 1 0 1

Single-bit error



Error Detection

